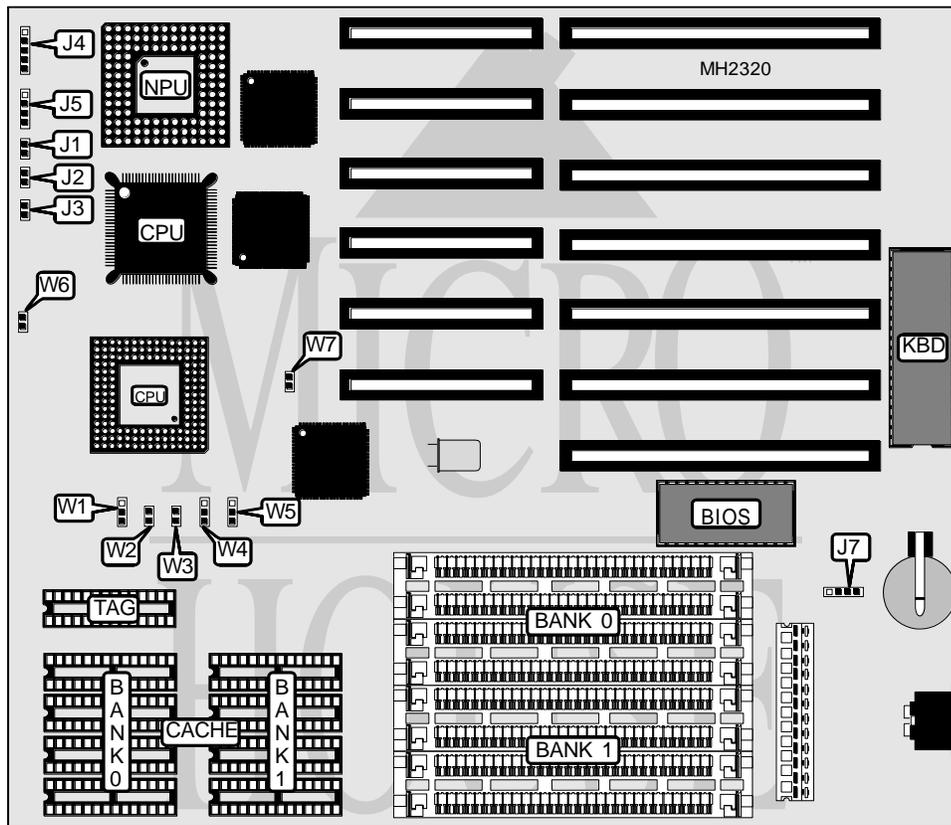


CHAINTECH COMPUTER COMPANY, LTD.

333UC/340UC/333UCB/340UCB

Processor	80386DX/CX486DLC
Processor Speed	33/40MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	258mm x 220mm
I/O Options	None
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J1	Speaker	J4
Turbo switch	J2	Power LED & keylock	J5
Turbo LED	J3	External battery	J7

Continued on next page. . .

CHAINTECH COMPUTER CO. LTD.

333UC/340UC/333UCB/340UCB

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DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	Tag
64K	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128K	(4) 32K x 8	NONE	(1) 8K x 8
256K	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	W1	W2	W3	W4	W5
64K	pins 2 & 3 closed	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed
128K	pins 1 & 2 closed	Open	Closed	pins 2 & 3 closed	pins 1 & 2 closed
256K	pins 2 & 3 closed	Closed	Closed	pins 2 & 3 closed	pins 2 & 3 closed

NPU CONFIGURATION		
Type	W6	W7
Disabled	Open	Open
Enabled	Closed	Closed