UNIDENTIFIED
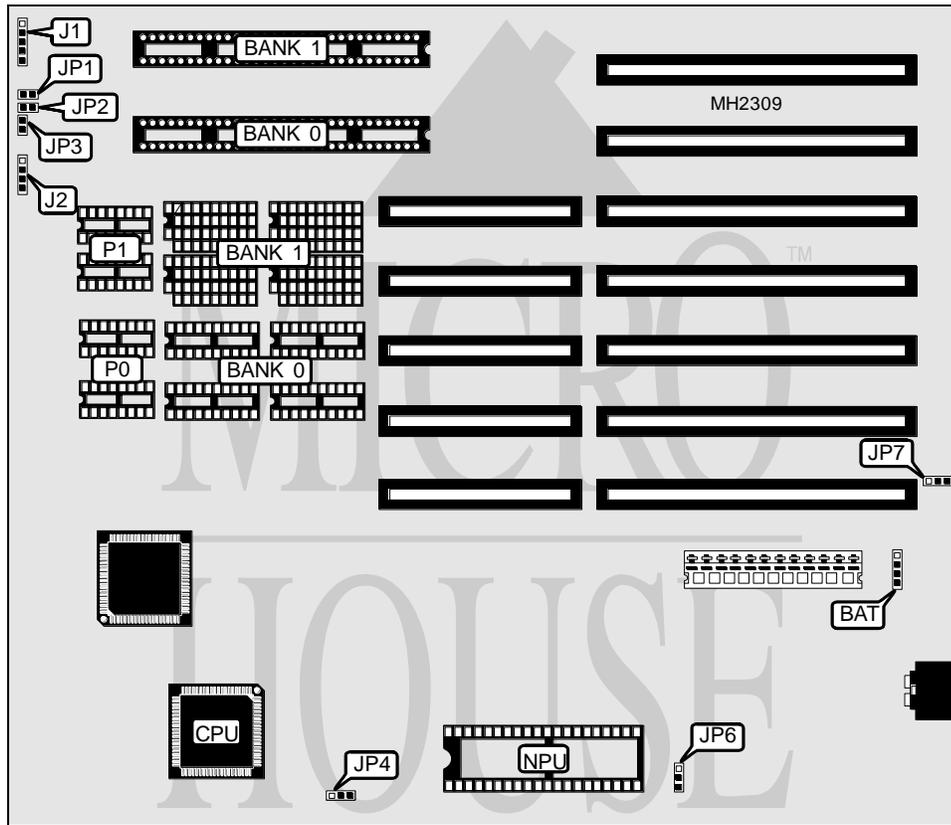# C & T TURBO MAINBOARD

Processor   80286
Processor Speed   12 MHz
Chip Set   C & T
Max. Onboard DRAM   4MB
Cache   None
BIOS   AMI/C & T
Dimensions   260mm x 220mm
I/O Options   None
NPU Options   80287

CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	BAT	Turbo LED	JP1
Power LED & keylock	J1	Reset switch	JP2
Speaker	J2	Turbo switch	JP3

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C & T TURBO MAINBOARD

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU synchronous with CPU	JP4	pins 1 & 2 closed
NPU asynchronous with CPU	JP4	pins 2 & 3 closed
í Monitor type select color	JP6	pins 2 & 3 closed
Monitor type select monochrome	JP6	pins 1 & 2 closed
í Power good signal detect from power supply	JP7	pins 2 & 3 closed
Power good signal detect from board	JP7	pins 1 & 2 closed

DRAM CONFIGURATION (DIP)		
Size	Bank 0	Bank 1
512KB	(4) 44256	NONE
640KB	(4) 44256	(4) 4464
1MB	(4) 44256	(4) 44256

DRAM PARITY CONFIGURATION (DIP)		
Size	P0	P1
512KB	(2) 41256	NONE
640KB	(2) 41256	(2) 4164
1MB	(2) 41256	(2) 41256

DRAM CONFIGURATION (SIP)		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9