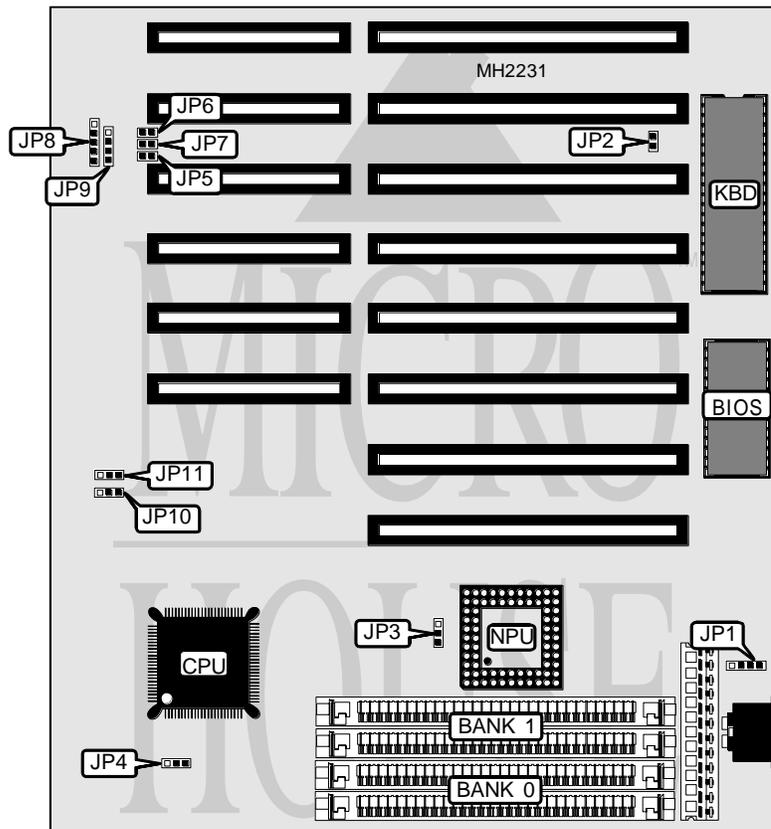


UNIDENTIFIED 386SX-S NON CACHE MAINBOARD MODEL 2

Processor	CX486SLC
Processor Speed	25/33/40MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB
Cache	None
BIOS	Unidentified
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo LED	JP7
Turbo switch	JP5	Power LED & keylock	JP8
Reset switch	JP6	Speaker	JP9

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386SX-S NON CACHE MAINBOARD MODEL 2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í NPU synchronous with CPU	JP3	pins 1 & 2 closed
NPU asynchronous with CPU	JP3	pins 2 & 3 closed
í Pipeline mode disabled	JP4	pins 1 & 2 closed
Pipeline mode enabled	JP4	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CPU SPEED CONFIGURATION		
Speed	JP10	JP11
25MHz	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 2 & 3 closed	pins 1 & 2 closed
40MHz	pins 2 & 3 closed	pins 2 & 3 closed