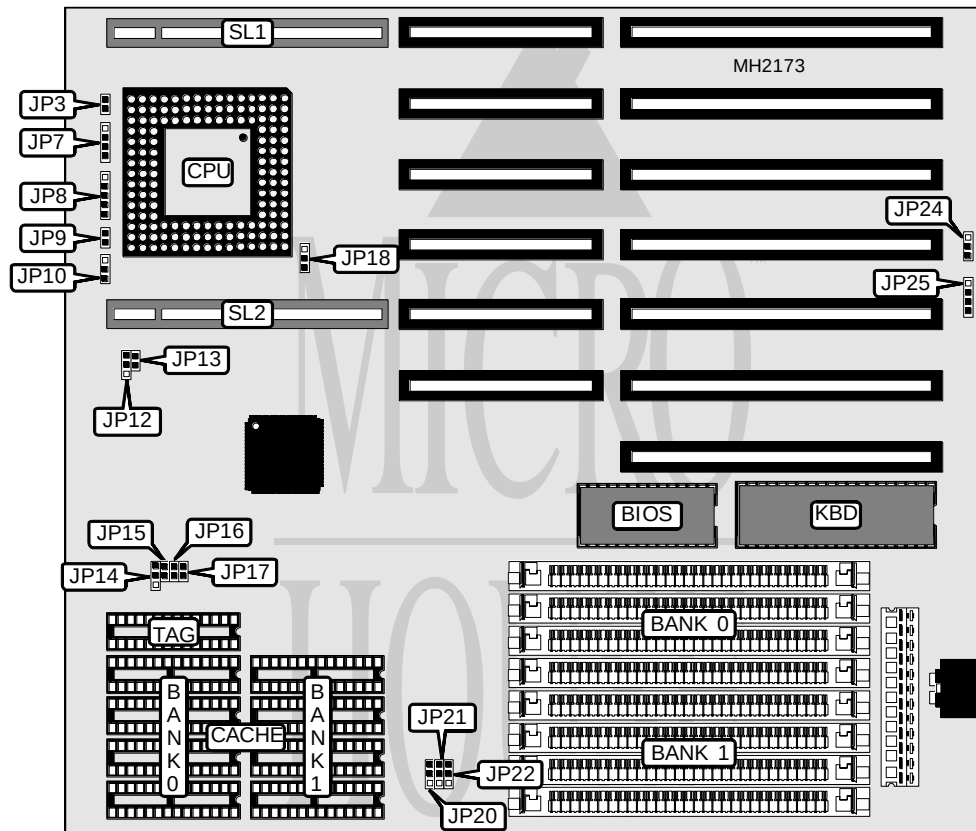


ROBOTECH, INC.

VL-BUS 486

Processor	80486SX/80486DX/80486DX2
Processor Speed	20/25/33/50(internal)/50/66(internal)MHz
Chip Set	Toshiba
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP3	Turbo switch	JP10
Speaker	JP7	External battery	JP25
Power LED & keylock	JP8	32-bit VESA local bus slots	SL1 & SL2
Reset switch	JP9		

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VL-BUS 486

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Turbo switch select turbo	JP10	Closed
Turbo switch select normal	JP10	Open
í CMOS memory normal operation	JP24	pins 2 & 3 closed
CMOS memory clear	JP24	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	JP14	JP15	JP16	JP17	JP20	JP21	JP22
32KB	1 & 2	Open	Open	Open	2 & 3	2 & 3	1 & 2
64KB	2 & 3	Open	Open	Closed	1 & 2	2 & 3	1 & 2
128KB	1 & 2	Closed	Open	Closed	1 & 2	1 & 2	2 & 3
256KB	2 & 3	Closed	Closed	Closed	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.							

CPU TYPE CONFIGURATION			
Type	JP12	JP13	JP18
80486SX	pins 2 & 3 closed	Open	pins 1 & 2 closed
80486DX/DX2	pins 1 & 2 closed	Closed	pins 2 & 3 closed