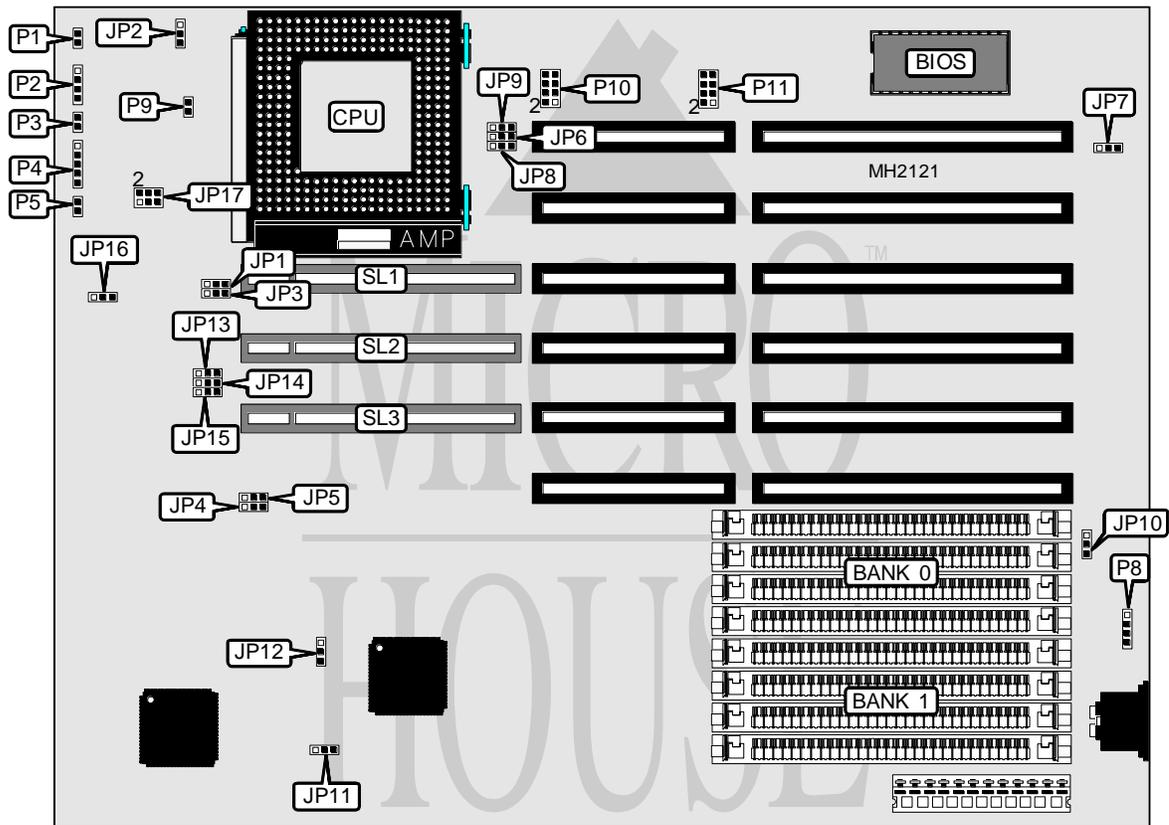


OCEAN INFORMATION SYSTEMS, INC.

HIPPO VL+ REV. 3

Processor	80486SX/80487SX/80486DX/80486DX2/80486DX4/Pentium Overdrive
Processor Speed	25/33/50(internal)/66(internal)/75(internal)/100(internal)MHz
Chip Set	Headland
Max. Onboard DRAM	128MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	P1	Turbo LED	P5
Speaker	P2	External battery	P8
Reset switch	P3	Chassis fan power	P9
Power LED & keylock	P4	32-bit VESA Local bus slots	SL1 - SL3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	pins 1 & 2 closed
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP3	pins 1 & 2 closed
í Monitor type select monochrome	JP7	pins 2 & 3 closed
Monitor type select color	JP7	pins 1 & 2 closed
í CMOS memory normal operation	JP10	pins 1 & 2 closed
CMOS memory clear	JP10	pins 2 & 3 closed
í Factory configured - do not alter	JP11	N/A
í Factory configured - do not alter	JP12	N/A
í Factory configured - do not alter	JP13	pins 2 & 3 closed
í Factory configured - do not alter	JP14	pins 2 & 3 closed
í Factory configured - do not alter	JP15	pins 2 & 3 closed
í Factory configured - do not alter	JP16	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
80MB	(4) 4M x 9	(4) 16M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CPU TYPE CONFIGURATION			
Type	JP6	JP8	JP9
80486SX	pins 2 & 3 closed	N/A	pins 1 & 2 closed
80487SX	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX/DX2/DX4	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION		
Type	P10	P11
80486SX/DX/DX2	pins 3 & 4, 5 & 6, 7 & 8 closed	pins 3 & 4, 5 & 6, 7 & 8 closed
80486DX4	Open	Open

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CPU SPEED CONFIGURATION		
Speed	JP4	JP5
25MHz	pins 2 & 3 closed	pins 2 & 3 closed
33MHz	pins 1 & 2 closed	pins 1 & 2 closed
50iMHz	pins 2 & 3 closed	pins 2 & 3 closed
66iMHz	pins 1 & 2 closed	pins 1 & 2 closed
75iMHz	pins 2 & 3 closed	pins 2 & 3 closed
100iMHz	pins 1 & 2 closed	pins 1 & 2 closed

CPU CLOCK CONFIGURATION	
Speed	JP17
2x	pins 3 & 4 closed
2.5x	pins 5 & 6 closed
3x	pins 1 & 2 closed