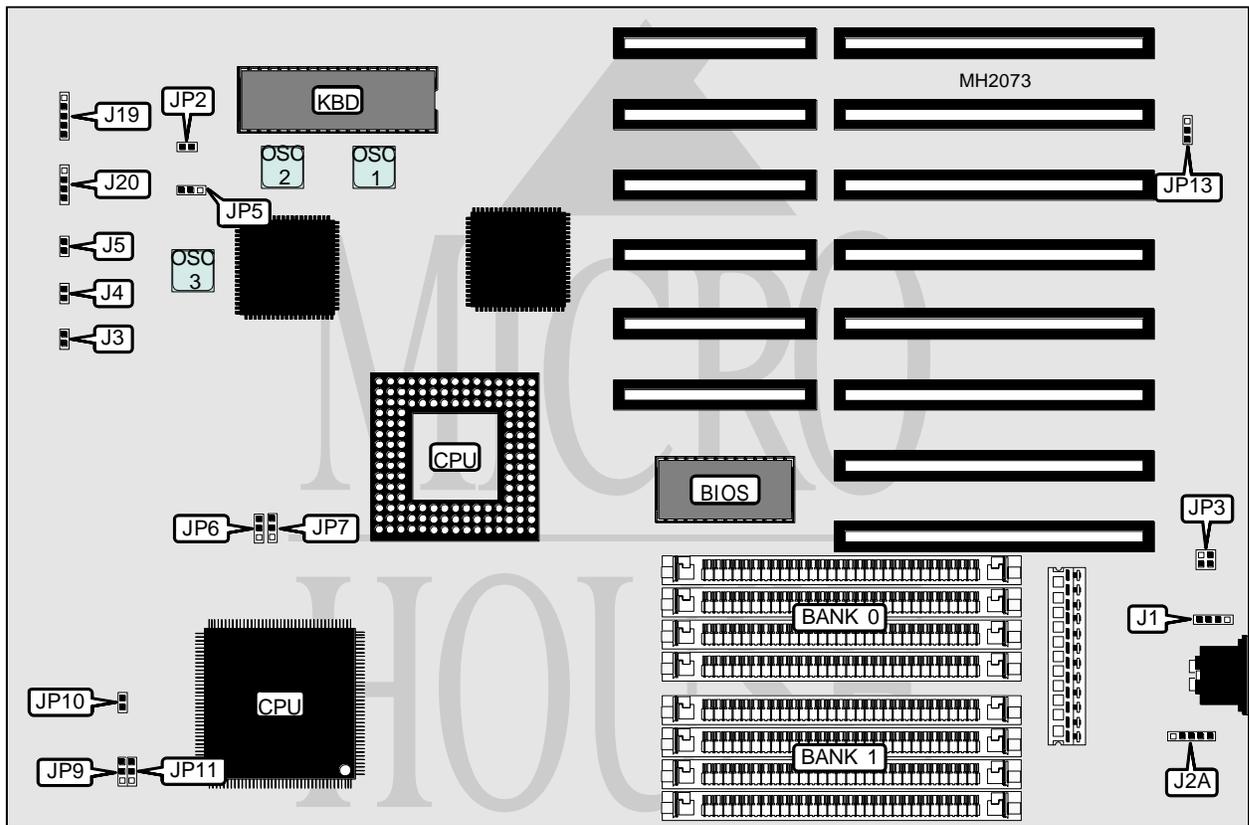


INFORMTECH INTERNATIONAL, INC.

IT486SQ

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/50(internal)/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	None
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	J5
Keyboard connector	J2A	Power LED & keylock	J19
Reset switch	J3	Speaker	J20
Turbo switch	J4		

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IT486SQ

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select - CGA, EGA, VGA	JP2	Closed
Monitor type select - monochrome	JP2	Open
í I/O recovery time enabled	JP13	pins 2 & 3 closed
I/O recovery time disabled	JP13	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CPU TYPE CONFIGURATION			
CPU Type	JP9	JP10	JP11
80486SX	pins 1 & 2 closed	Open	Open
80486DX	pins 2 & 3 closed	Closed	pins 2 & 3 closed
P24	pins 2 & 3 closed	Closed	pins 1 & 2 closed

SYSTEM CLOCK CONFIGURATION	
System Clock	JP5
20MHz	pins 1 & 2 closed
Half of alternative oscillator frequency at OSC3	pins 2 & 3 closed

UPGRADE PROCESSOR TYPE CONFIGURATION		
Upgrade processor	JP6	JP7
80486DX	pins 2 & 3 closed	pins 2 & 3 closed
80487SX or other	pins 1 & 2 closed	pins 1 & 2 closed

EXTERNAL BATTERY CONFIGURATION	
Battery type	JP3
3.6V Lithium battery **	pins 1 & 2 closed
4.5V Alkaline Battery	pins 2 & 4 closed
6V (4 AA) Battery pack	pins 3 & 4 closed
6V Battery pack **	pins 1 & 3 closed

Note: ** with internal series current limiting resistor.