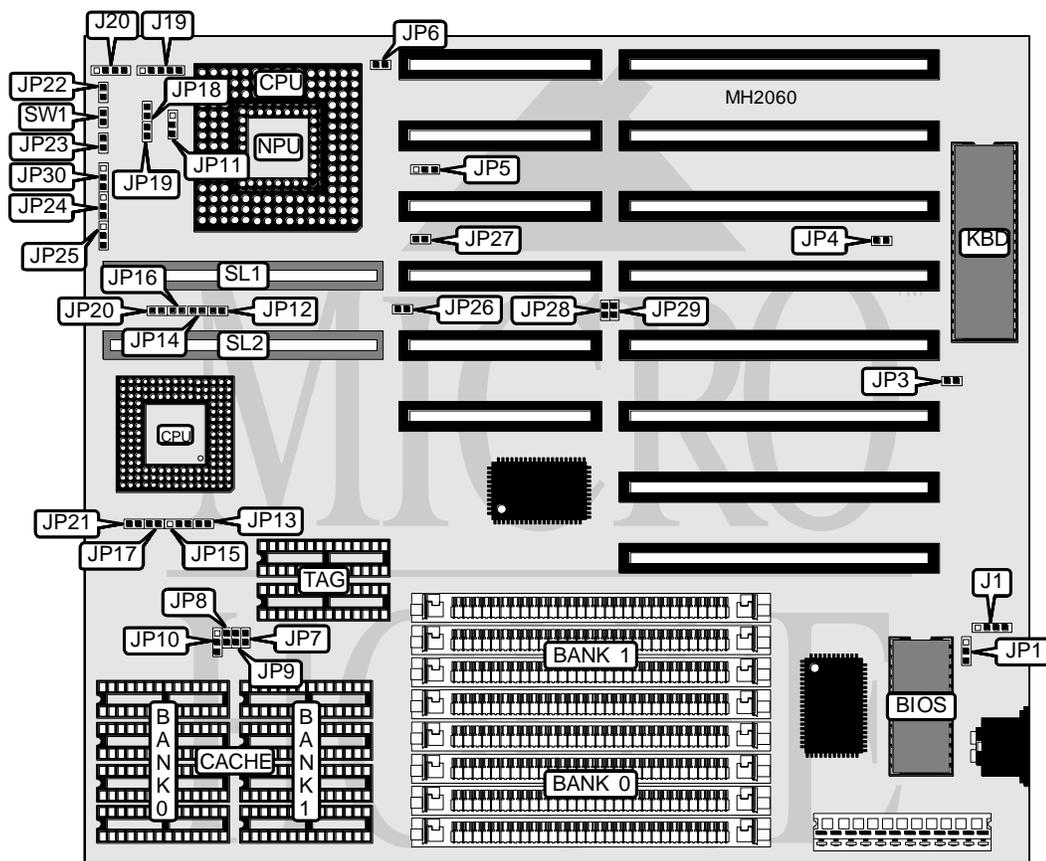


EDOM INTERNATIONAL CORPORATION

3486VL2M (MV008)

Processor	AMD386DX/AMD386DXL/80386DX/80486SX/80487SX/80486DX/80486DX2/ Pentium Overdrive
Processor Speed	25/33/40(internal)/50(internal)/50/66(internal)/80(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	80387DX/CX487DLC



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	JP23
Power LED & keylock	J19	32-bit VESA Local bus slots	SL1 & SL2
Speaker	J20	Reset switch	SW1
Turbo switch	JP22		

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3486VL2M (MV008)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í I/O bus speed select ATCLK21/5 (80386 only)	JP3	Open
I/O bus speed select ATCLK21/4 (80386 only)	JP3	Closed
í Monitor type select monochrome	JP4	Open
Monitor type select color	JP4	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U39)	TAG (U40)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	NONE
128KB	(4) 32K x 8	NONE	(1) 8K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 8K x 8	(1) 8K x 8

Note: The relative position of U39 & U40 is unidentified.

CACHE JUMPER CONFIGURATION				
Size	JP7	JP8	JP9	JP10
64KB	Open	Open	Open	pins 2 & 3 closed
128KB	Open	Closed	Closed	pins 1 & 2 closed
256KB	Closed	Closed	Closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION			
Type	JP11	JP13	JP15
80486SX	Open	Open	pins 2 & 3 closed
80487SX	pins 1 & 2 closed	Closed	pins 1 & 2 closed
80486DX	pins 2 & 3 closed	Closed	pins 1 & 2 closed

CPU TYPECONFIGURATION		
Type	JP5	JP6
80386	pins 2 & 3 closed	Open
80486	pins 1 & 2 closed	Closed

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CPU TYPE CONFIGURATION (80386)		
Type	JP17	JP21
Cyrix	Closed	Closed
Intel/AMD	Open	Open

CPU SPEED CONFIGURATION		
Speed	JP18	JP19
25MHz	Closed	Open
33MHz	Open	Closed
40MHz	Open	Open
50MHz	Closed	Open
66MHz	Open	Closed
80MHz	Open	Open

CPU CLOCK CONFIGURATION		
Type	JP24	JP25
80386 double frequency	pins 1 & 2 closed	pins 2 & 3 closed
80486 single frequency	pins 1 & 2 closed	pins 1 & 2 closed
80486 double frequency	pins 2 & 3 closed	pins 2 & 3 closed

VESA CPU TYPE CONFIGURATION		
CPU Type	JP16 (ID1)	JP20 (ID0)
80386	Closed	Open
80486	Open	Closed

VESA BUS SPEED CONFIGURATION	
Speed	JP12 (ID3)
<= 33MHz	Open
> 33MHz	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP14 (ID2)
0 wait states	Open
1 wait state	Closed

VESA PAL CONFIGURATION				
PAL	JP26	JP27	JP28	JP29
Enabled	Open	Open	Open	Open
Disabled	Closed	Closed	Closed	Closed

Note: PAL disabled - slave mode function only. PAL enabled - master/slave mode.

VESA BUS SPEED MASTER/SLAVE CONFIGURATION	
Speed	JP30
33MHz	pins 1 & 2 closed
50MHz	pins 2 & 3 closed