UNIDENTIFIED
80486DLC

Processor	80386DX/CX486DLC
Processor Speed	33/40MHz
Chip Set	Unidentified
Max. Onboard DRAM	32MB
Cache	32/128KB
BIOS	AMI
Dimensions	230mm x 218mm
I/O Options	None
NPU Options	80387DX
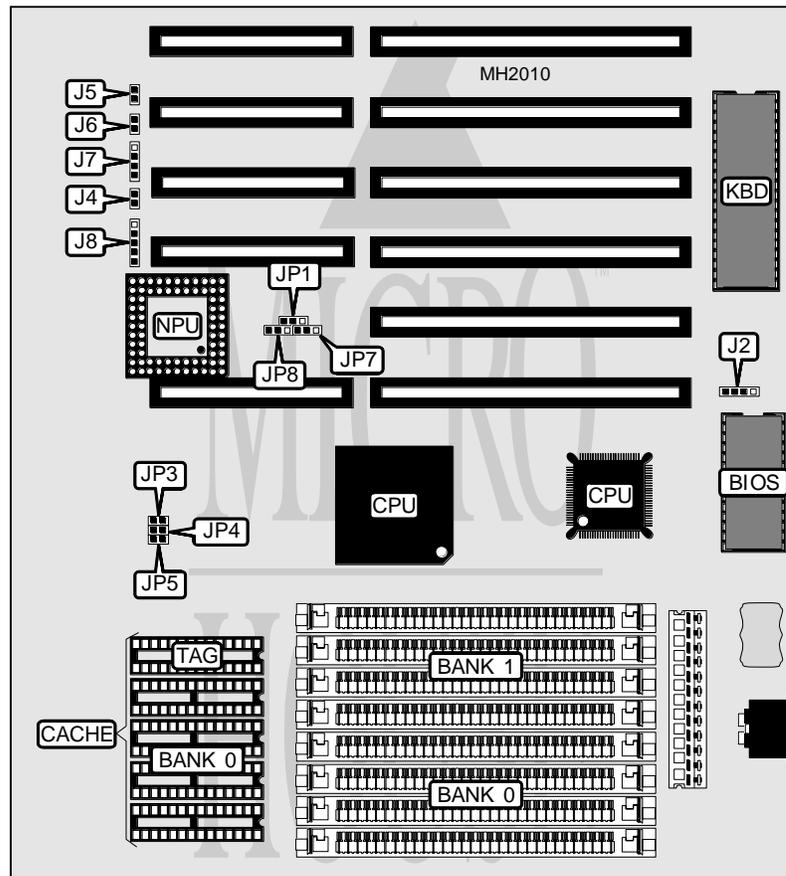
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Turbo LED	J6
Reset switch	J4	Speaker	J7
Turbo switch	J5	Power LED & keylock	J8

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU disabled	JP1	pins 2 & 3 closed
NPU enabled	JP1	pins 1 & 2 closed
í Factory configured - do not alter	JP7	N/A
í Factory configured - do not alter	JP8	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
32KB	(4) 16K x 4	(1) 16K x 4
128KB	(4) 64K x 4	(1) 64K x 4

CACHE JUMPER CONFIGURATION			
Size	JP3	JP4	JP5
32KB	Open	Open	Open
128KB	Closed	Closed	Closed