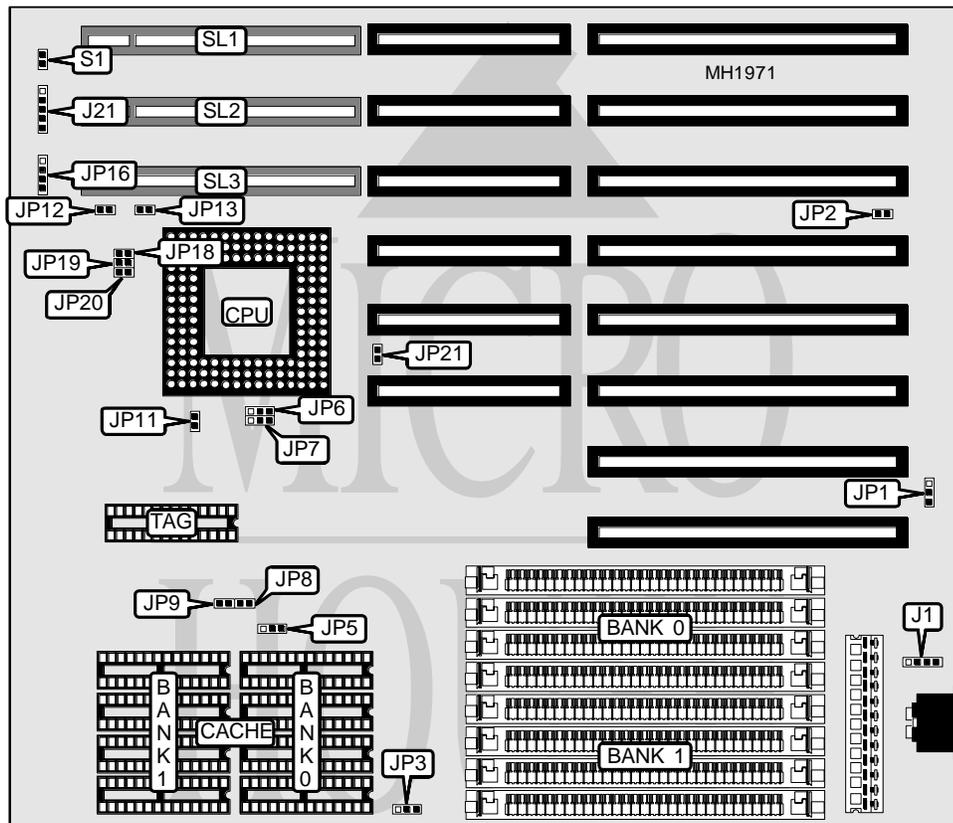


MAGITRONICS

486SX/486DX/486DX2 (VER. 2)

Processor	80486SX/80486DX/80486DX2/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	64MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J1	Speaker	JP16
Power LED & keylock	J21	Reset switch	S1
Turbo LED	JP12	32-bit VESA local bus slots	SL1 - SL3
Turbo switch	JP13		

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MAGITRONICS

486SX/486DX/486DX2 (VER. 2)

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open

DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8 or (1) 32K x 8

CACHE JUMPER CONFIGURATION

Size	JP3	JP5	JP8	JP9
64KB	pins 2 & 3 closed	pins 2 & 3 closed	Open	Open
128KB	pins 1 & 2 closed	pins 1 & 2 closed	Closed	Open
256KB	pins 2 & 3 closed	pins 2 & 3 closed	Closed	Closed

CPU TYPE CONFIGURATION

Type	JP6	JP7	JP11
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	Open
80486DX/DX2	pins 1 & 2 closed	pins 1 & 2 closed	Closed

CPU SPEED CONFIGURATION

Speed	JP18	JP19	JP20
25MHz	Closed	Open	Closed
33MHz	Closed	Closed	Open
40MHz	Open	Open	Closed
50iMHz	Closed	Open	Closed
50MHz	Open	Closed	Open
66iMHz	Closed	Closed	Open

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MAGITRONICS**486SX/486DX/486DX2 (VER. 2)**

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CPU ADS DELAY CONFIGURATION	
Speed	JP21
33MHz	pins 2 & 3 closed
40MHz	pins 1 & 2 closed
50MHz	pins 1 & 2 closed
66MHz	pins 2 & 3 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP17
0 wait states	Open
1 wait state	Closed
Note: The location of JP17 is unknown.	

BUS SPEED CONFIGURATION	
CPU speed	JP14
<= 33MHz	Open
> 33MHz	Closed
Note: The location of JP14 is unknown.	

MISCELLANEOUS TECHNICAL NOTE
Note: The location of jumpers on diagram are for identification purposes and may not be the actual location.