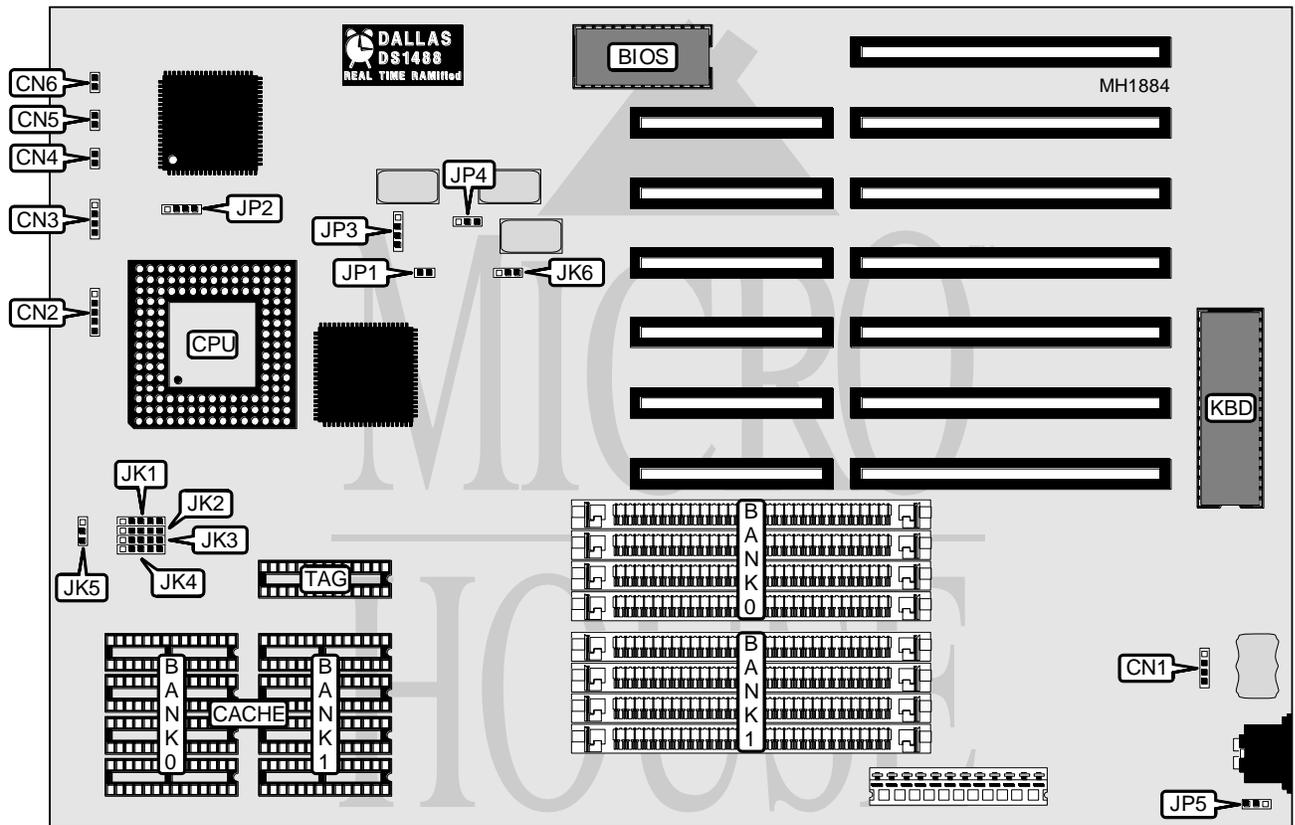


PC WARE INTERNATIONAL, INC.

MA405DC

Processor 80486SX/80487SX/80486DX
Processor Speed 25/33/40/50MHz
Chip Set ACC Micro
Max. Onboard DRAM 32MB
Cache 32/64/128/256KB
BIOS AMI
Dimensions 330mm x 218mm
I/O Options None
NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	CN1	Turbo switch	CN4
Power LED & keylock	CN2	Reset switch	CN5
Speaker	CN3	Turbo LED	CN6

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MA405DC

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Cache enabled	JP1	Closed
Cache disabled	JP1	Open
í 16mhz Oscillator not installed	JP4	pins 2 & 3 closed
16MHz Oscillator installed	JP4	pins 1 & 2 closed
í Monitor type select color	JP5	pins 2 & 3 closed
Monitor type select monochrome	JP5	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	NONE
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 32K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 32K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION						
Size	JK1	JK2	JK3	JK4	JK5	JK6
32KB	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4	pins 1 & 2	pins 1 & 2
64KB	1 & 2, 3 & 4	2 & 3, 4 & 5	1 & 2, 3 & 4	1 & 2, 3 & 4	pins 1 & 2	pins 1 & 2
128KB	2 & 3, 4 & 5	1 & 2, 4 & 5	1 & 2, 3 & 4	1 & 2, 3 & 4	pins 1 & 2	pins 1 & 2
256KB	2 & 3, 4 & 5	2 & 3, 4 & 5	2 & 3, 4 & 5	Closed	pins 1 & 2	pins 2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION		
Type	JP2	JP3
80486SX	pins 2 & 3 closed	pins 2 & 3 closed
80487SX	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed
80486DX	pins 3 & 4 closed	pins 1 & 2 and 3 & 4 closed