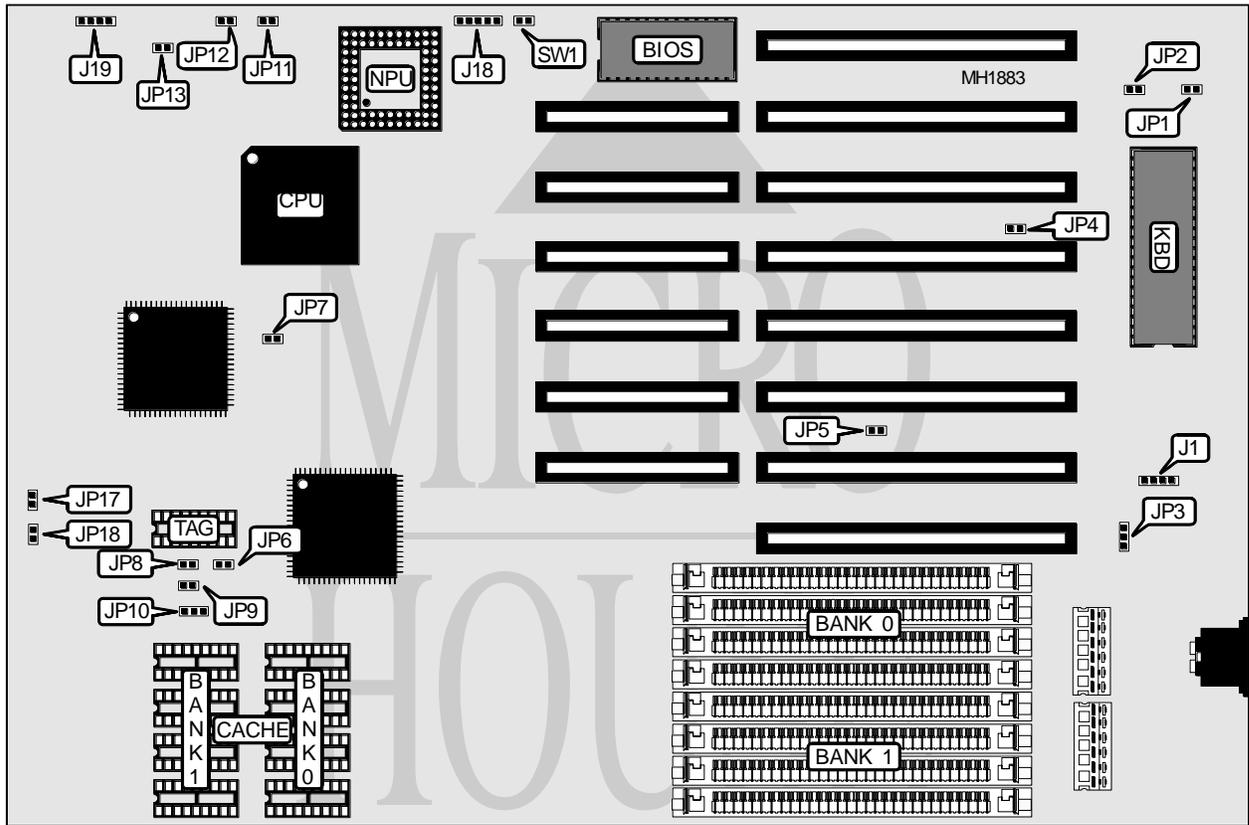


UNIDENTIFIED OPTI 386 DX

Processor 80386DX/CX486DLC
Processor Speed 33/40MHz
Chip Set Opti
Max. Onboard DRAM 32MB
Cache 64/128/256KB
BIOS AMI
Dimensions 330mm x 218mm
I/O Options None
NPU Options 80387DX/CX487DLC



| CONNECTIONS | | | |
|---------------------|----------|--------------|----------|
| Purpose | Location | Purpose | Location |
| External battery | J1 | Turbo LED | JP12 |
| Power LED & keylock | J18 | Reset switch | SW1 |
| Speaker | J19 | | |

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UNIDENTIFIED OPTI 386 DX

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| USER CONFIGURABLE SETTINGS | | |
|-----------------------------------|--------|-------------------|
| Function | Jumper | Position |
| í CMOS memory normal operation | JP3 | pins 2 & 3 closed |
| CMOS memory clear | JP3 | pins 1 & 2 closed |
| í Factory configured-do not alter | JP4 | N/A |
| í Factory configured-do not alter | JP7 | N/A |
| í Factory configured do not alter | JP11 | N/A |

| CPU TYPE SELECT | | |
|-----------------|--------|--------|
| Type | JP5 | JP13 |
| CX486DLC | Closed | Closed |
| 80386DX | Open | Open |

| CLOCK SPEED SELECT | | |
|--------------------|--------|--------|
| Speed | JP1 | JP2 |
| 33MHz | Closed | Closed |
| 40MHz | Open | Open |

| CACHE JUMPER CONFIGURATION | | | | | | |
|----------------------------|--------|--------|--------|------------|--------|--------|
| Size | JP6 | JP8 | JP9 | JP10 | JP17 | JP18 |
| 64KB | Open | Open | Open | Pins 2 & 3 | Open | Open |
| 128KB | Closed | Closed | Open | Pins 1 & 2 | Open | Closed |
| 256KB | Closed | Closed | Closed | Pins 2 & 3 | Closed | Closed |

Note: All pins designated should be in the closed position.

| DRAM CONFIGURATION | | |
|--------------------|--------------|--------------|
| Size | Bank 0 | Bank 1 |
| 1MB | (4) 256K x 9 | NONE |
| 2MB | (4) 256K x 9 | (4) 256K x 9 |
| 4MB | (4) 1M x 9 | NONE |
| 8MB | (4) 1M x 9 | (4) 1M x 9 |
| 16MB | (4) 4M x 9 | NONE |
| 32MB | (4) 4M x 9 | (4) 4M x 9 |