MICRONICS COMPUTERS, INC.
4 8 6 / 2 5

Processor	80486DX
Processor Speed	25MHz
Chip Set	Micronics
Max. Onboard DRAM	16MB
Cache	64/128/256KB
BIOS	Phoenix
Dimensions	355mm x 304mm
I/O Options	None
NPU Options	4167
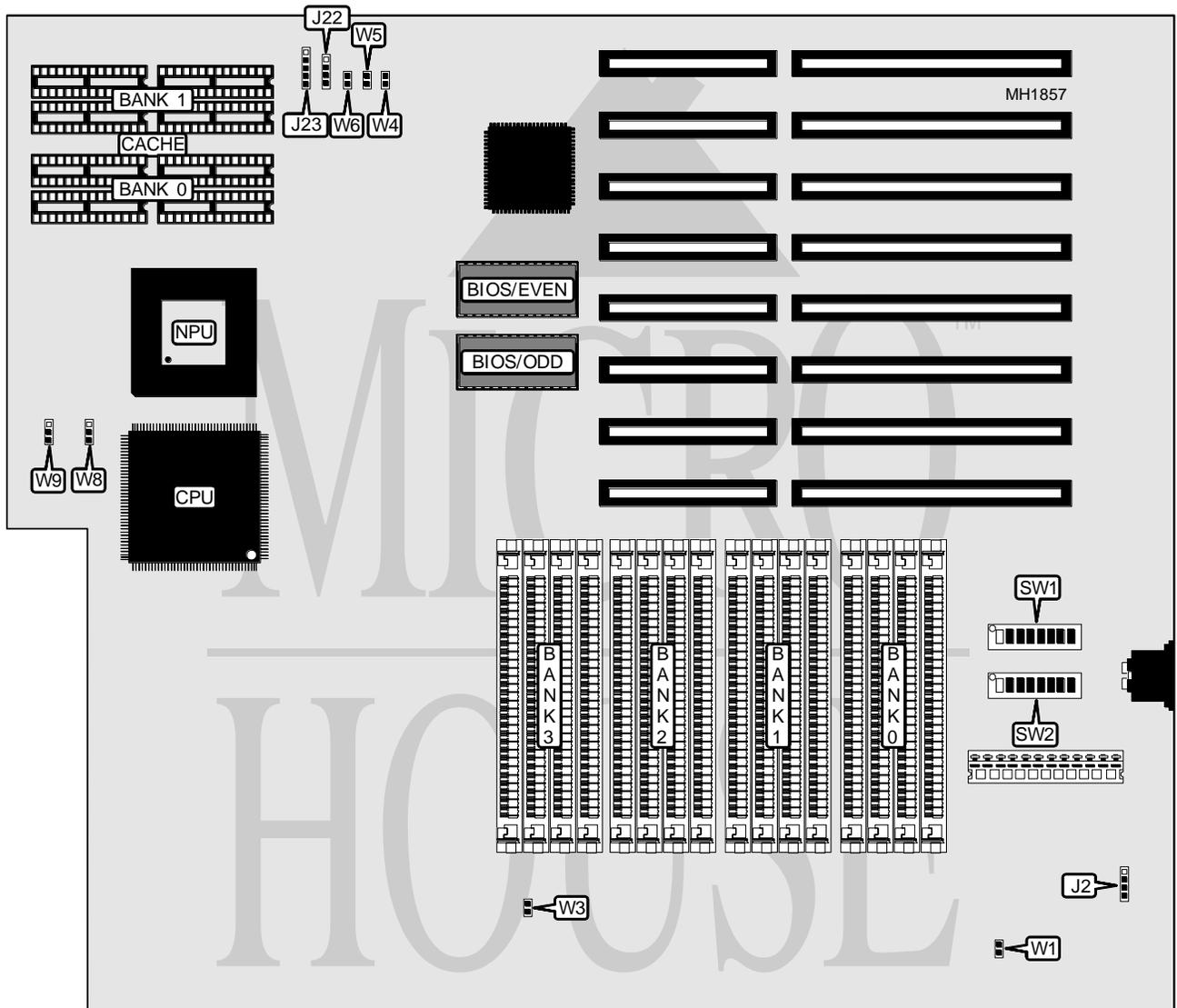
Continued next page...

MICRONICS COMPUTERS, INC.
4 8 6 / 2 5

CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Reset switch	W4
Speaker	J22	Turbo LED	W5
Power LED & keylock	J23	Turbo switch	W6

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Internal cache enabled	SW1/1	On
Internal cache disabled	SW1/1	Off
í On board BIOS enabled	SW1/2	Off
External BIOS enabled	SW1/2	On
í Factory configured - do not alter	SW1/3	On
í External cache enabled	SW1/4	On
External cache disabled	SW1/4	Off
í BIOS relocation disabled	SW1/5	Off
BIOS relocation enabled	SW1/5	On
í Factory configured - do not alter	SW1/6	Off
í Monitor type select color, CGA, EGA, or VGA	SW1/7	On
Monitor type select monochrome	SW1/7	Off
í Factory configured - do not alter	SW1/8	Off
í Factory configured - do not alter	SW2/5	Off
í D00000-EFFFFF cacheable	SW2/6	Off
D00000-EFFFFF non-cacheable	SW2/6	On
í Factory configured - do not alter	W1	N/A
í Factory configured - do not alter	W2	pins 2 & 3 closed
í Factory configured - do not alter	W3	N/A
Note: The exact location of W2 is unknown.		

DRAM CONFIGURATION							
Size	Bank 0	Bank 1	Bank 2	Bank 3	SW1/1	SW1/2	SW1/3	SW1/4
1MB	(4)256K x 9	NONE	NONE	NONE	On	On	On	On
2MB	(4)256K x 9	(4)256K x 9	NONE	NONE	Off	On	On	On
3MB	(4)256K x 9	(4)256K x 9	(4)256K x 9	NONE	On	Off	On	On
4MB	(4)256K x 9	(4)256K x 9	(4)256K x 9	(4)256K x 9	Off	Off	On	On
4MB	(4)1M x 9	NONE	NONE	NONE	On	On	Off	On
5MB	(4)256K x 9	(4)1M x 9	NONE	NONE	On	On	Off	Off
6MB	(4)256K x 9	(4)256K x 9	(4)1M x 9	NONE	On	Off	On	Off
7MB	(4)256K x 9	(4)256K x 9	(4)256K x 9	(4)1M x 9	Off	On	On	Off
8MB	(4)1M x 9	(4)1M x 9	NONE	NONE	Off	On	Off	On
9MB	(4)256K x 9	(4)1M x 9	(4)1M x 9	NONE	On	Off	Off	Off
10MB	(4)256K x 9	(4)256K x 9	(4)1M x 9	(4) 1M x 9	Off	Off	On	Off
12MB	(4)1M x 9	(4)1M x 9	(4)1M x 9	NONE	On	Off	Off	On
13MB	(4)256K x 9	(4)1M x 9	(4)1M x 9	(4)1M x 9	Off	Off	Off	Off
16MB	(4)1M x 9	(4)1M x 9	(4)1M x 9	(4)1M x 9	Off	Off	Off	On

MICRONICS COMPUTERS, INC.
4 8 6 / 2 5

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 32K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
Note: The exact location of the TAG chip is unknown.			

CACHE JUMPER CONFIGURATION				
Size	SW2/7	SW2/8	W8	W9
64KB	Off	Off	pins 1 & 2 closed	pins 1 & 2 closed
128KB	On	On	pins 2 & 3 closed	pins 1 & 2 closed
256KB	Off	On	pins 2 & 3 closed	pins 2 & 3 closed