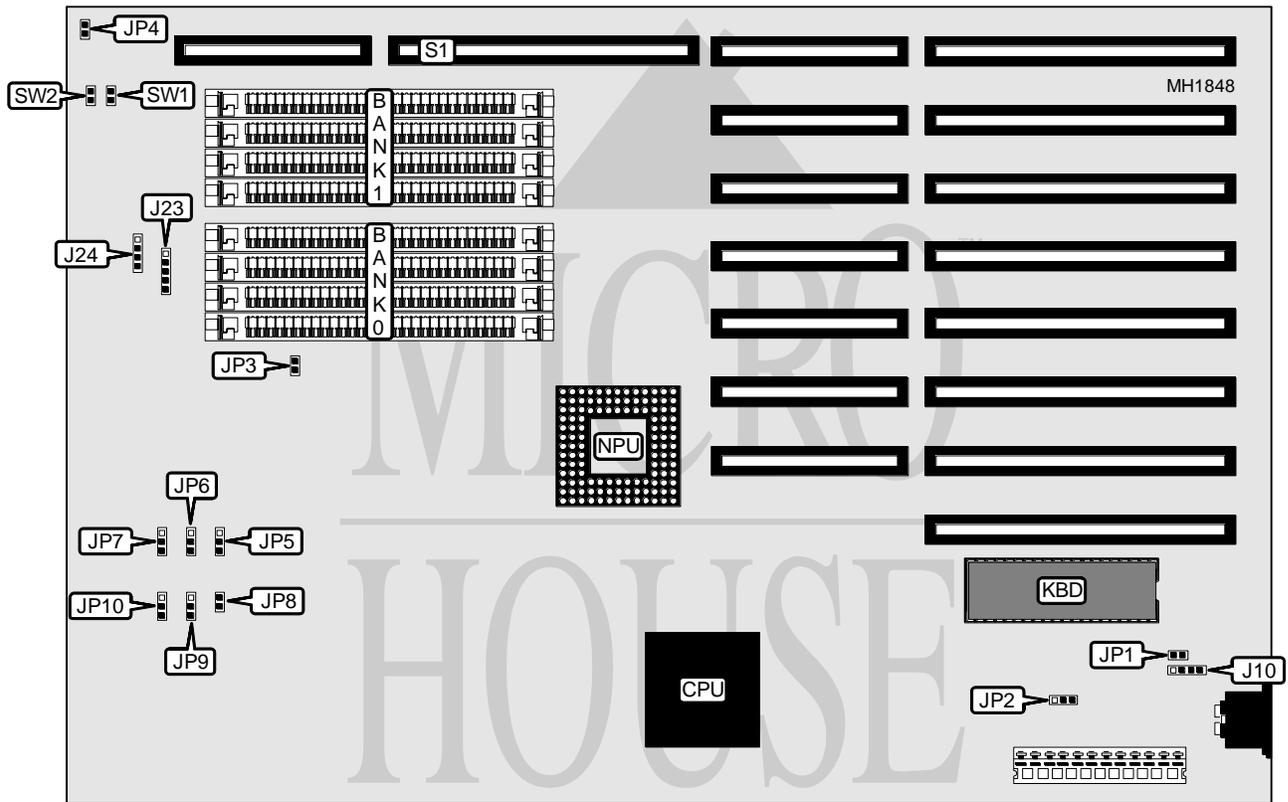


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OPTi 386-33/40WB

Processor	80386DX (exact location is unknown)
Processor Speed	33/40MHz
Chip Set	OPTi
Max. Onboard DRAM	40MB
Cache	64KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J10	32-bit external memory card slot	S1
Power LED & keylock	J23	Reset switch	SW1
Speaker	J24	Turbo switch	SW2
Turbo LED	JP4		

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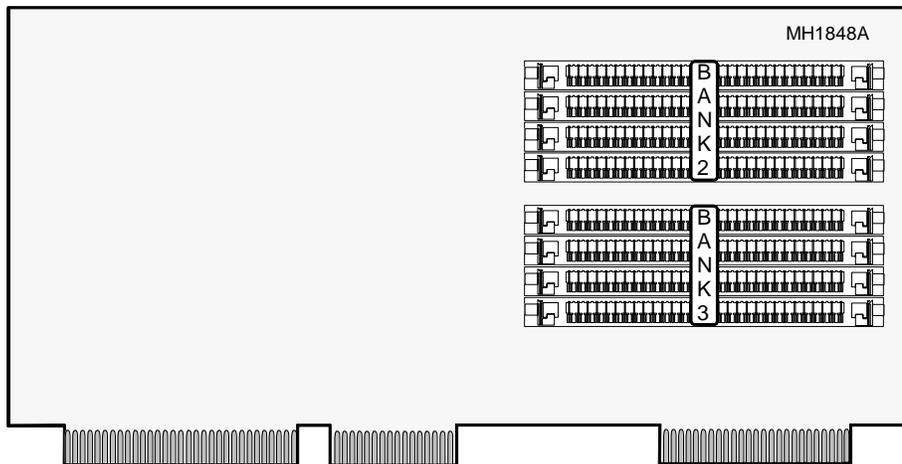
OPTi 386-33/40WB

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
20MB	(4) 4M x 9	(4) 1M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9	NONE
24MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	NONE
28MB	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
28MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	NONE
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9

Note: The locations of Bank 2 & Bank 3 can be found on the external memory card.



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CACHE CONFIGURATION				
Size	Max. Cacheable	Bank 0	Bank 1	TAG
32KB	8MB	(4) 8K x 8	(4) 8K x 8	(4) 4K x 4
64KB	16MB	(4) 8K x 8	(4) 8K x 8	(4) 4K x 4
128KB	32MB	(4) 32K x 8	(4) 32K x 8	(4) 16K x 8
256KB	64MB	(4) 32K x 8	(4) 32K x 8	(4) 16K x 8

CACHE JUMPER CONFIGURATION						
Size	JP5	JP6	JP7	JP8	JP9	JP10
32KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	Open	pins 2 & 3	pins 2 & 3
64KB	pins 2 & 3	pins 1 & 2	pins 1 & 2	Open	pins 2 & 3	pins 1 & 2
128KB	pins 2 & 3	pins 1 & 2	pins 2 & 3	Closed	pins 2 & 3	pins 1 & 2
256KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	Closed	pins 1 & 2	pins 1 & 2

Note: Pins designated should be in the closed position.

AT CLK SELECT	
AT Clock	JP3
CLK2IN/6	Open
CLK2IN/8	Closed