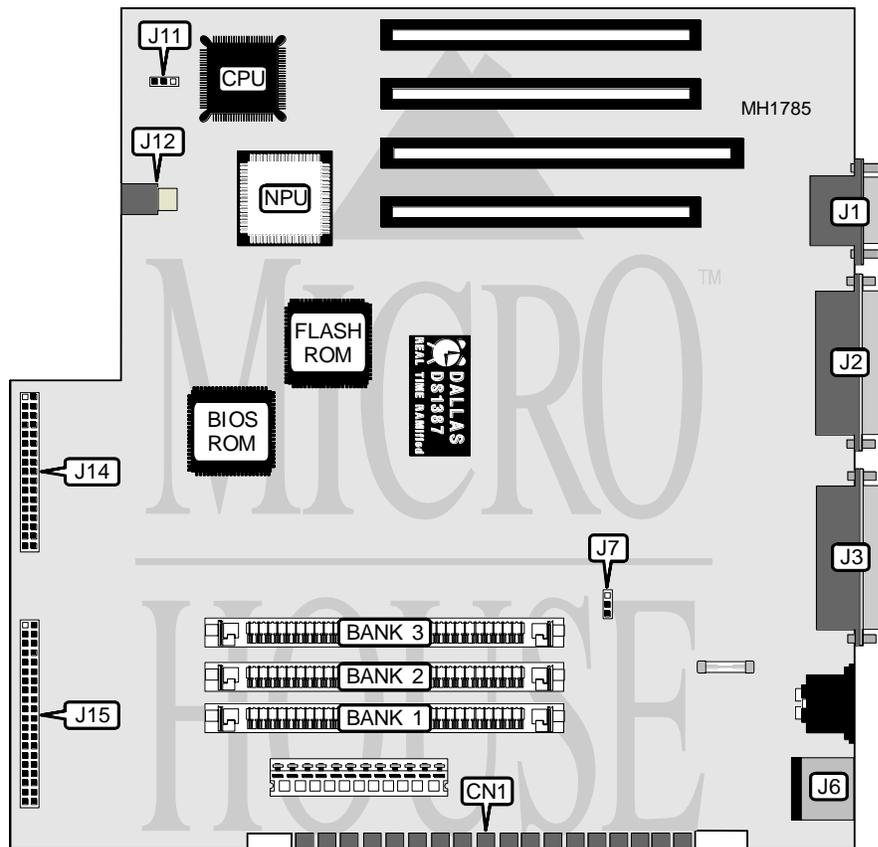


REPLY CORPORATION PS/2 MODEL 50/50Z

Processor	80386SLC/80486SLC2
Processor Speed	25/50MHz
Chip Set	Galaxy
Max. Onboard DRAM	16MB
Cache	None
BIOS	Reply
Dimensions	330mm x 218mm
I/O Options	32-bit MCA bus slot with video extension, Floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial port, VGA port
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Power supply connector	CN1	Chassis fan power	J7
VGA port	J1	Speaker connector	J12
Serial port	J2	Floppy drive interface	J14
Parallel port	J3	AT IDE interface connector	J15
PS/2 mouse port	J6	32-bit MCA bus slot	S1

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REPLY CORPORATION

PS/2 MODEL 50/50Z

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í CMOS memory normal operation	J11	pins 1 & 2 closed
CMOS memory clear	J11	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 1	Bank 2	Bank 3
4MB	(1) 4M x 9	NONE	NONE
8MB	(1) 4M x 9	(1) 4M x 9	NONE
12MB	(1) 4M x 9	(1) 4M x 9	(1) 4M x 9
12MB	(1) 4M x 9	(1) 8M x 9	NONE
16MB	(1) 4M x 9	(1) 4M x 9	(1) 8M x 9