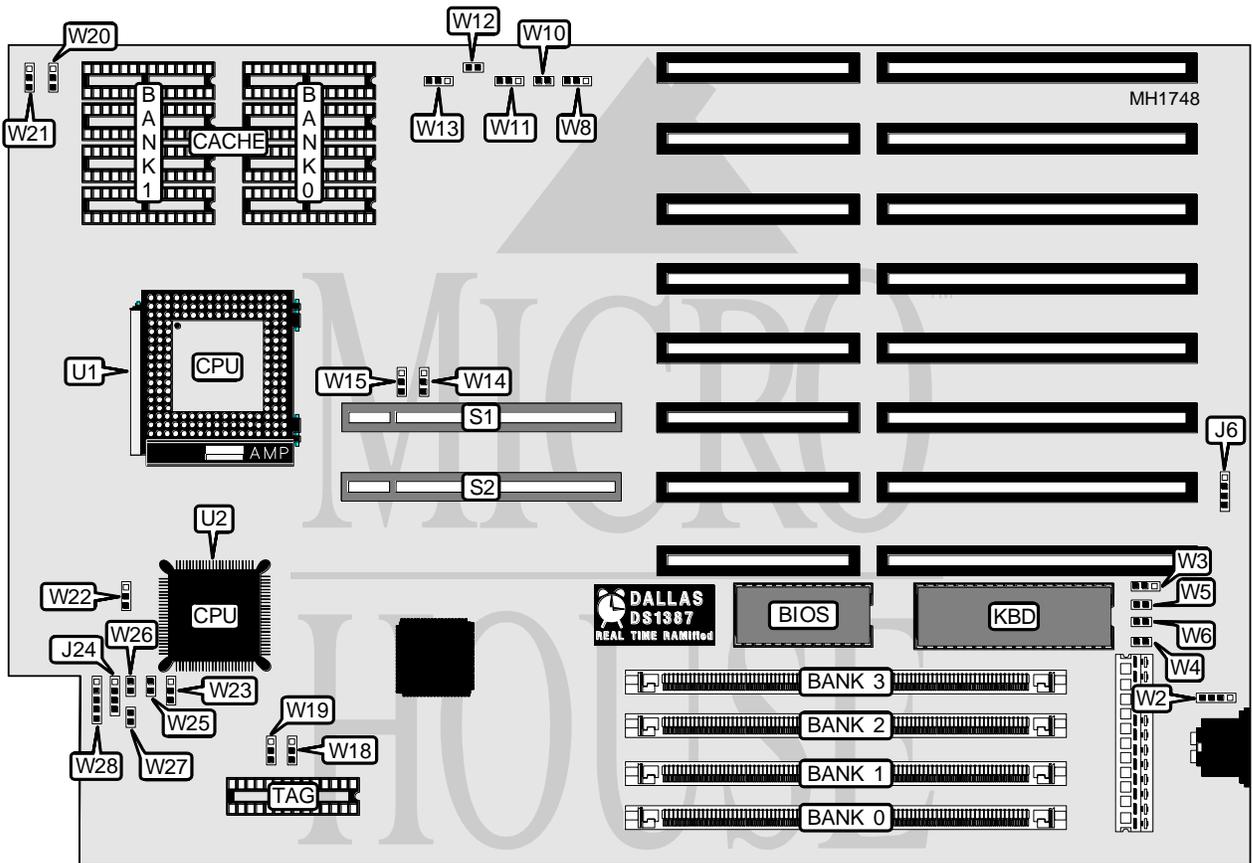


MICRONICS COMPUTERS, INC.

MX30 VL-BUS

| | |
|--------------------------|------------------------------------|
| Processor | 80486SX/80486DX/ODP486SX/80486DX2 |
| Processor Speed | 25/33/50(internal)/66(internal)MHz |
| Chip Set | Micronics |
| Max. Onboard DRAM | 64MB |
| Cache | 64/128/256KB |
| BIOS | Phoenix |
| Dimensions | 330mm x 218mm |
| I/O Options | 32-bit VESA local bus slots (2) |
| NPU Options | None |



| CONNECTIONS | | | |
|--|----------|---------------------|----------|
| Purpose | Location | Purpose | Location |
| External Battery | J6 | Reset switch | W25 |
| Speaker | J24 | Turbo switch | W26 |
| 32-bit VESA Local bus slots | S1 & S2 | Turbo LED | W27 |
| External keyboard connector | W2 | Power LED & keylock | W28 |
| Note: MX30 VL-Bus will come standard with a Dallas Real Time Clock or with the external battery connector, J6. | | | |

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| USER CONFIGURABLE SETTINGS | | |
|--|--------|-------------------|
| Function | Jumper | Position |
| í Parity enabled | W3 | pins 1 & 2 closed |
| Parity disabled | W3 | pins 2 & 3 closed |
| í Factory configured - do not alter | W4 | Open |
| í Dallas real time clock installed | W5 | Closed |
| Other manufacturer's real time clock installed | W5 | Open |
| í Monitor type select color | W6 | Open |
| Monitor type select monochrome | W6 | Closed |
| í Factory configured - do not alter | W10 | Open |
| í Factory configured - do not alter | W12 | Open |
| í Factory configured - do not alter | W14 | pins 1 & 2 closed |
| í Factory configured - do not alter | W15 | pins 1 & 2 closed |

| DRAM CONFIGURATION | | | | |
|--------------------|---------------|---------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 1MB | (1) 256K x 36 | NONE | NONE | NONE |
| 2MB | (1) 256K x 36 | (1) 256K x 36 | NONE | NONE |
| 4MB | (1) 1M x 36 | NONE | NONE | NONE |
| 6MB | (1) 256K x 36 | (1) 256K x 36 | (1) 1M x 36 | NONE |
| 8MB | (1) 1M x 36 | (1) 1M x 36 | NONE | NONE |
| 10MB | (1) 256K x 36 | (1) 256K x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 12MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | NONE |
| 16MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 16MB | (1) 4M x 36 | NONE | NONE | NONE |
| 18MB | (1) 256K x 36 | (1) 256K x 36 | (1) 4M x 36 | NONE |
| 20MB | (1) 1M x 36 | (1) 4M x 36 | NONE | NONE |
| 24MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | NONE |
| 32MB | (1) 4M x 36 | (1) 4M x 36 | NONE | NONE |
| 36MB | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 | NONE |
| 40MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 48MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | NONE |
| 64MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |

| CACHE CONFIGURATION | | | |
|---------------------|-------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | TAG |
| 64KB | (4) 8K x 8 | (4) 8K x 8 | (1) 8K x 8 |
| 128KB | (4) 32K x 8 | NONE | (1) 8K x 8 |
| 256KB | (4) 32K x 8 | (4) 32K x 8 | (1) 32K x 8 |

| CACHE JUMPER CONFIGURATION | | | | | |
|----------------------------|------------|------------|------------|------------|------------|
| Size | W8 | W11 | W13 | W18 | W19 |
| 64KB | pins 2 & 3 | pins 2 & 3 | pins 1 & 2 | pins 1 & 2 | pins 1 & 2 |
| 128KB | pins 1 & 2 | pins 1 & 2 | pins 2 & 3 | pins 2 & 3 | pins 1 & 2 |
| 256KB | pins 2 & 3 |

Note: Pins designated should be in the closed position.

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| CPU TYPE CONFIGURATION | | | |
|------------------------|---------|-------------------|-------------------|
| U1 | U2 | W22 | W23 |
| None | 80486SX | pins 1 & 2 closed | pins 1 & 2 closed |
| 80486SX | None | pins 1 & 2 closed | pins 1 & 2 closed |
| 80486DX/80486DX2 | 80486SX | pins 2 & 3 closed | pins 2 & 3 closed |
| 80486DX/80486DX2 | None | pins 2 & 3 closed | pins 2 & 3 closed |
| ODP486SX | 80486SX | pins 2 & 3 closed | pins 2 & 3 closed |

| CPU SPEED CONFIGURATION | | |
|-------------------------|-------------------|-------------------|
| Speed | W20 | W21 |
| 25MHz | pins 2 & 3 closed | pins 2 & 3 closed |
| 33MHz | pins 1 & 2 closed | pins 2 & 3 closed |
| 50i MHz | pins 2 & 3 closed | pins 2 & 3 closed |
| 66i MHz | pins 1 & 2 closed | pins 2 & 3 closed |