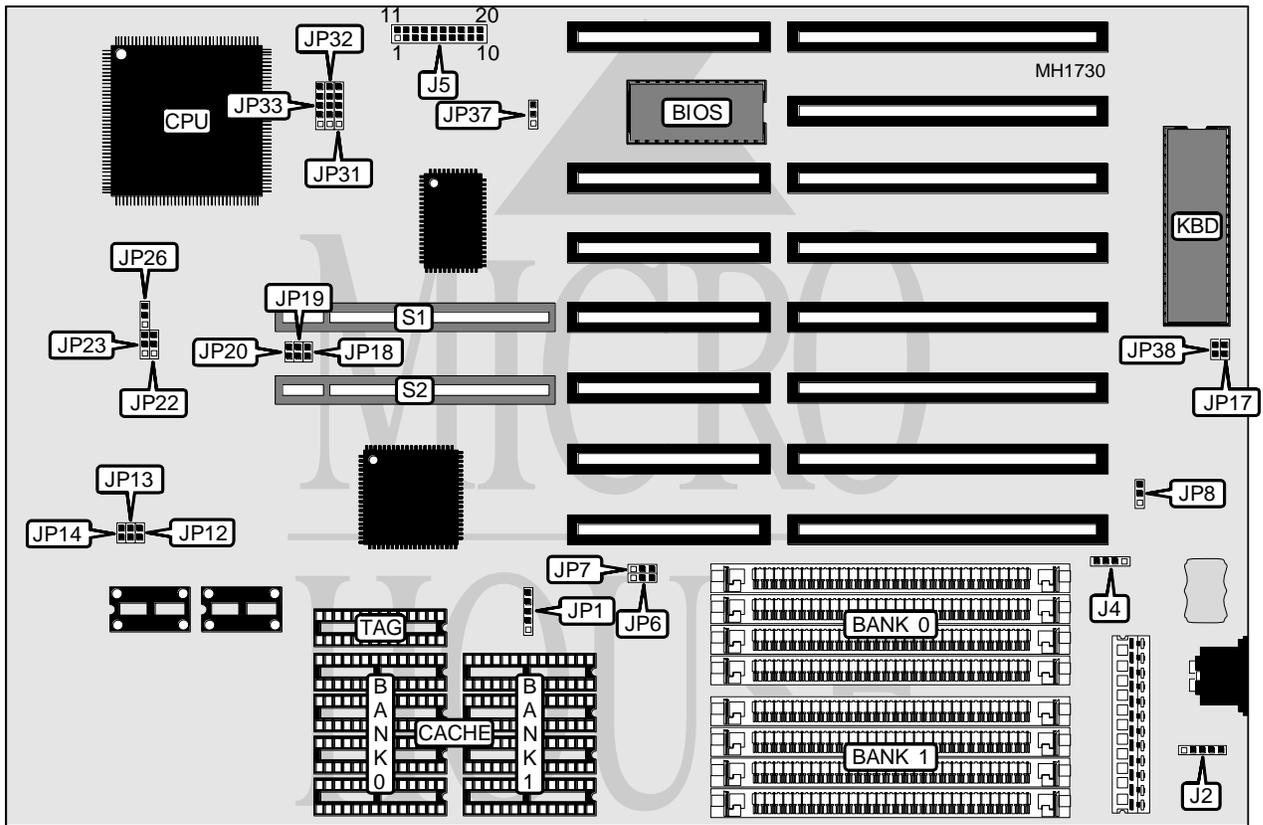


# MICRO-STAR INTERNATIONAL CO., LTD.

## VLB 486CT1

<b>Processor</b>	80486SX/80487SX/80486DX/ODP486SX/80486DX2/Pentium Overdrive
<b>Processor Speed</b>	20/25/33/40/50(internal)/50/66(internal)MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit VESA local bus slots (2)
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External keyboard	J2	Green PC	JP38
External battery	J4	32 Bit VESA slots	S1 & S2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP17	Open
Monitor type select color	JP17	Closed
í Factory configured - do not alter	JP18	Closed
í Factory configured - do not alter	JP22	pins 1 & 2 closed
í Factory configured - do not alter	JP23	pins 1 & 2 closed
í Factory configured - do not alter	JP26	pins 1 & 2 closed
í Factory configured - do not alter	JP37	pins 1 & 2 closed

PIN OUT CONFIGURATION	
Function	J5
Keylock	pins 3 & 5 closed
Power LED	pins 1 & 2 closed
Speaker	pins 7, 8, 9 & 10 closed
Turbo LED	pins 12 & 13 closed
Turbo switch	pins 15, 16 & 17 closed
Reset switch	pins 19 & 20 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 3
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP1	JP6	JP7
64KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
128KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
256KB	pins 4 & 5 closed	pins 1 & 2 closed	pins 1 & 2 closed

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CPU TYPE CONFIGURATION			
Type	JP31	JP32	JP33
80486SX	pins 3 & 4 closed	Open	pins 2 & 3 , 4 & 5 closed
80486SX (PQFP)	Open	Open	pins 1 & 2 closed
80487SX	pins 2 & 3 , 4 & 5 closed	pins 1 & 2 closed	pins 2 & 3 , 4 & 5 closed
80486DX	pins 2 & 3 , 4 & 5 closed	pins 2 & 3 closed	pins 2 & 3 , 4 & 5 closed
80486DX (PQFP)	pins 1 & 2 closed	pins 4 & 5 closed	pins 1 & 2 closed
ODP486SX	pins 2 & 3 , 4 & 5 closed	pins 1 & 2 closed	pins 2 & 3 , 4 & 5 closed
80486DX2	pins 2 & 3 , 4 & 5 closed	pins 2 & 3 closed	pins 2 & 3 , 4 & 5 closed
Pentium Overdrive	pins 2 & 3 , 4 & 5 closed	pins 1 & 2 closed	pins 2 & 3 , 4 & 5 closed

CPU SPEED CONFIGURATION					
Speed	OSC.	JP8	JP12	JP13	JP14
20MHz	40MHz	pins 2 & 3 closed	Closed	Closed	Open
25MHz	25MHz	pins 2 & 3 closed	Open	Open	Closed
33MHz	33MHz	pins 1 & 2 closed	Closed	Open	Open
50i MHz	25MHz	pins 2 & 3 closed	Open	Open	Closed
50MHz	50MHz	pins 1 & 2 closed	Open	Closed	Open
66i MHz	33MHz	pins 1 & 2 closed	Closed	Open	Open

VESA WAIT STATE/BUS SPEED CONFIGURATION			
CPU speed	Wait states	JP19	JP20
≤ 33MHz	0 wait states	Open	Open
> 33MHz	1 wait state	Closed	Closed