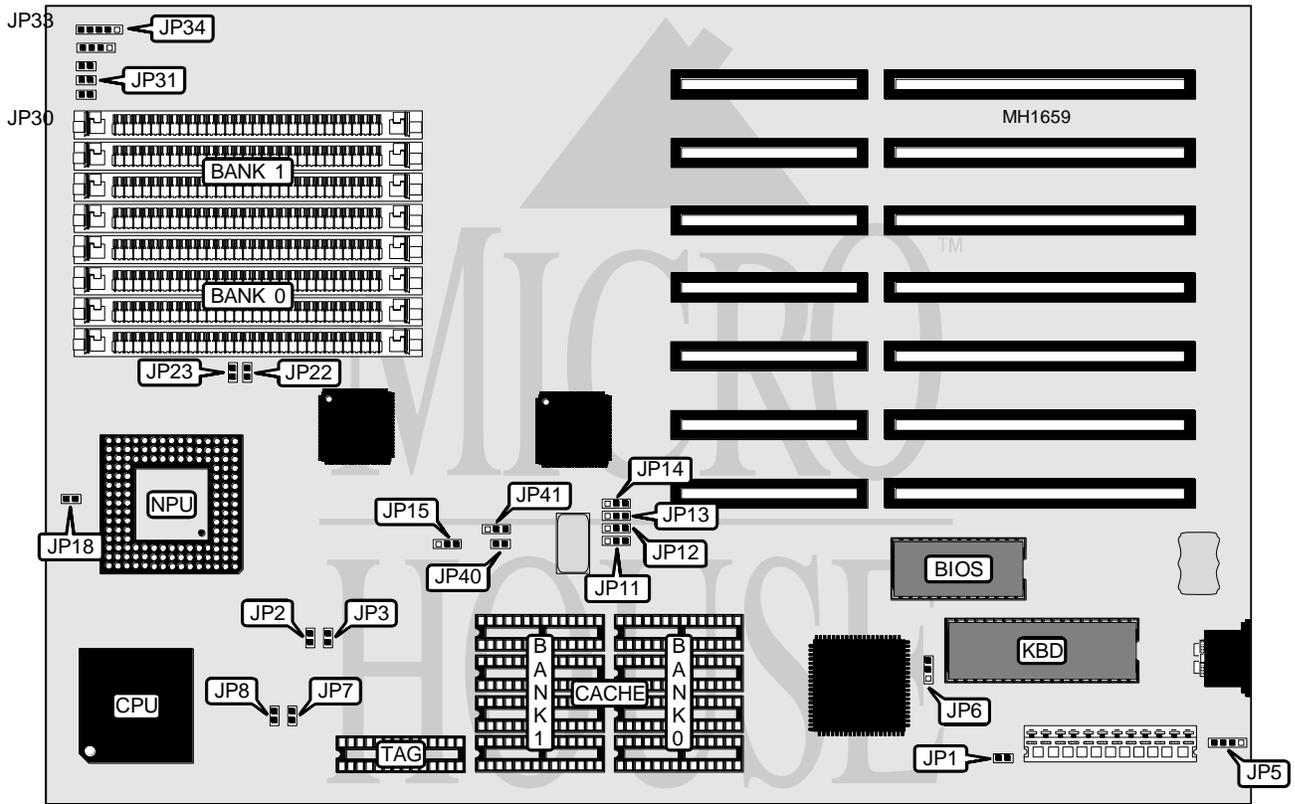


AIM MOTHERBOARD COMPANY

AIM 486 C Q

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/50MHz
Chip Set	VLSI
Max. onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP5	Reset switch	JP32
Turbo switch	JP30	Speaker	JP33
Turbo LED	JP31	Power LED & keylock	JP34

Continued on next page . . .

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AIM486CQ

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í CMOS memory normal operation	JP6	pins 1 & 2 closed
CMOS memory clear	JP6	pins 2 & 3 closed
í NPU select 0 wait states	JP18	Open
NPU select 1 wait state	JP18	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 4
128KB	(4) 32K x 8	NONE	(1) 8K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 4

CACHE JUMPER CONFIGURATION						
Size	JP11	JP12	JP13	JP14	JP40	JP41
64KB	Open	Open	Open	Open	Open	pins 2 & 3
128KB	Open	pins 1 & 2	pins 2 & 3	Closed	Open	pins 1 & 2
256KB	Closed	pins 1 & 2	pins 1 & 2	Closed	Closed	pins 2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION			
Speed	JP15	JP22	JP23
20MHz	pins 2 & 3 closed	Open	Open
25MHz	pins 1 & 2 closed	Closed	Open
33MHz	pins 1 & 2 closed	Open	Closed
50MHz	pins 1 & 2 closed	Closed	Closed

CPU TYPE CONFIGURATION				
TYPE	JP2	JP3	JP7	JP8
80486SX	Open	pins 2 & 3 closed	Open	Closed
80487SX	pins 1 & 2 closed	pins 1 & 2 closed	Closed	Open
80486DX	pins 2 & 3 closed	pins 2 & 3 closed	Open	Closed