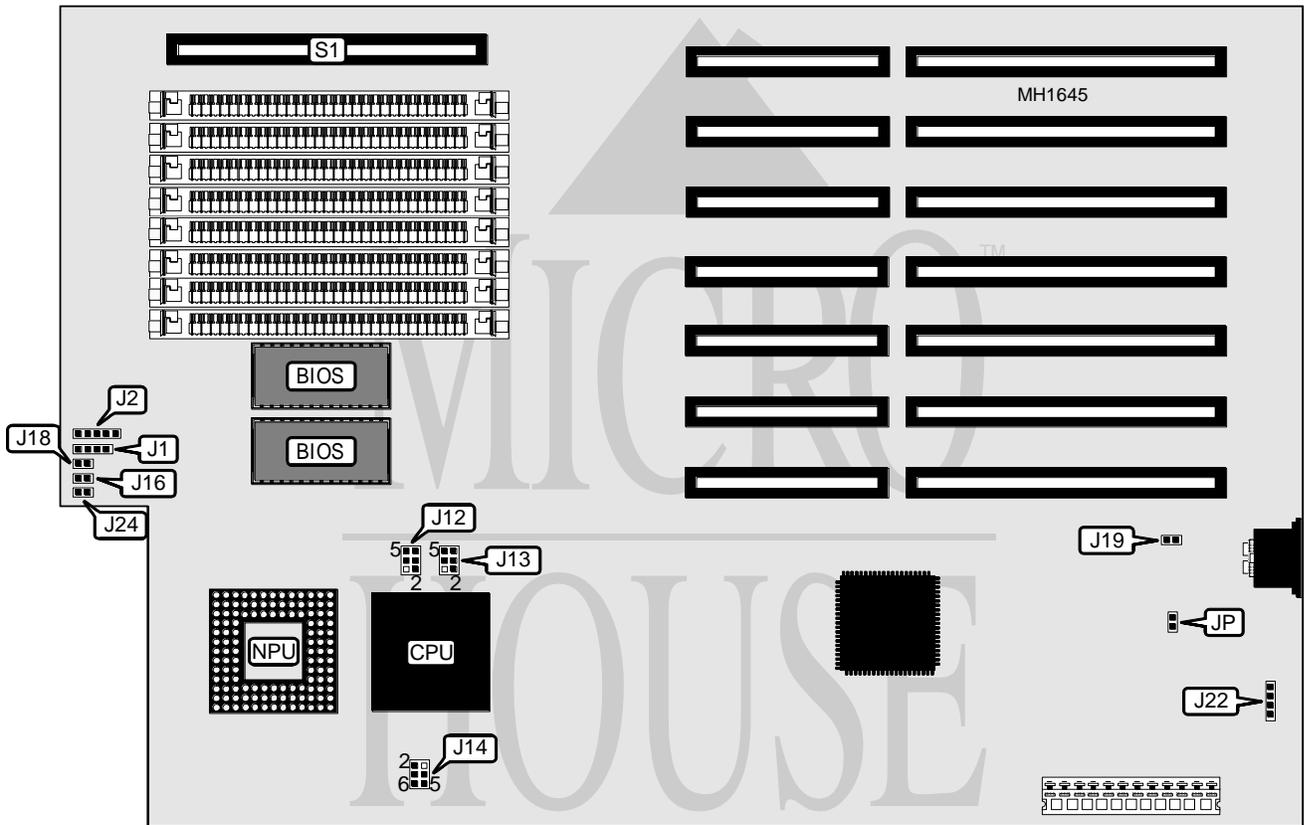


UNIDENTIFIED

TTL 386-33/25

Processor	80386DX
Processor Speed	25/33MHz
Chip Set	Unknown
Max. onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	325mm x 308mm
I/O Options	32-bit external memory card
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	External battery	J22
Power LED & keylock	J2	Turbo Switch	J24
Turbo LED	J16	32-bit external memory card	S1
Reset switch	J18		
Note: The exact location of J24 is unknown.			

Continued on next page . . .

UNIDENTIFIED
TTL 386-33/25

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Power good signal detect from power supply	JP	pins 1 & 2 closed
Power good signal detect from board	JP	pins 2 & 3 closed
í Factory configured - do not alter	JP13	pins 1 & 3, 2 & 4 closed, 5 & 6 open
í Monitor type select color	JP19	Closed
Monitor type select monochrome	JP19	Open

DRAM CONFIGURATION		
Size	Bank 0 (ML1, ML2, ML3, ML4)	Bank 1 (MH1, MH2, MH3, MH4)
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9

DRAM JUMPER CONFIGURATION	
Size	J12
1MB	pins 1 & 2, 3 & 4 closed, pins 5 & 6 open
2MB	pins 1 & 2, 5 & 6 open, pins 3 & 4 closed
4MB	pins 3 & 4, 5 & 6, open, pins 1 & 2 closed
8MB	pins 1 & 2, 3 & 4, 5 & 6 open

NPU CONFIGURATION	
NPU	J14
Enabled	Pins 1 & 2 open, pins 3 & 5, 4 & 6 closed
Disabled	pins 1 & 3, 2 & 4 closed, pins 5 & 6 open