UNIDENTIFIED
INFO 286

Processor	80286
Processor Speed	6/8/10/12MHz
Chip Set	C & T
Max. onboard DRAM	512KB
Cache	None
BIOS	Unknown
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287 (Exact location unknown)
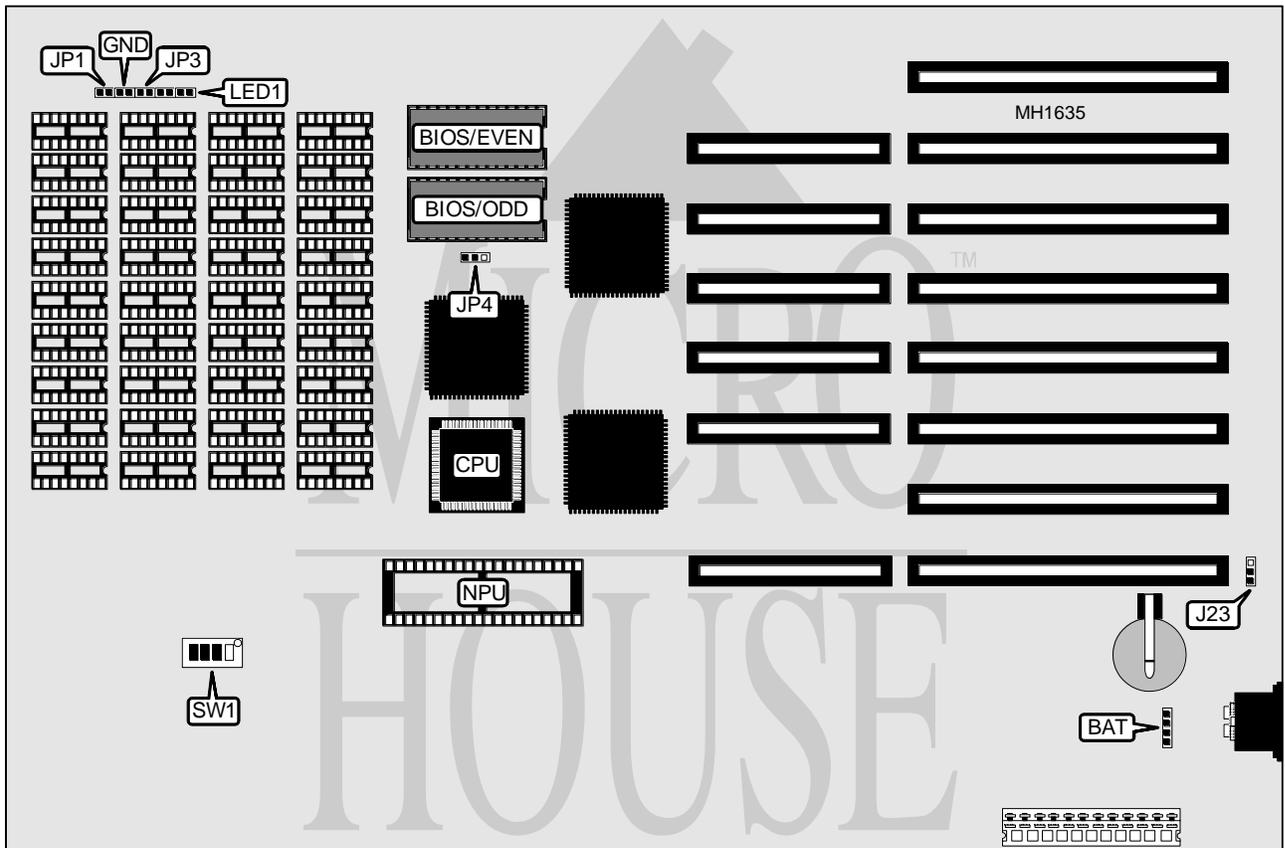
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	BAT	Reset switch	JP1
Power LED & keylock	J21	Turbo Switch	JP3
Speaker	J22	Turbo LED	LED1
Note: The locations of jumpers J21 and J22 are unknown.			

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	GND	N/A
í BIOS type select 27256	JP4	pins 1 & 2 closed
BIOS type select 27128	JP4	pins 2 & 3 closed
í Factory configured - do not alter	SW1/switch 1	N/A
í Monitor type select color	SW1/switch 2	On
Monitor type select monochrome	SW1/switch 2	Off

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	SW1/switch 3	SW1/switch 4
256KB	(9) 41256	NONE	On	On
512KB	(18) 41256	NONE	Off	On
640KB	(18) 41256	(4) 41256	On	Off
1024KB	(18) 41256	(18) 41256	Off	Off