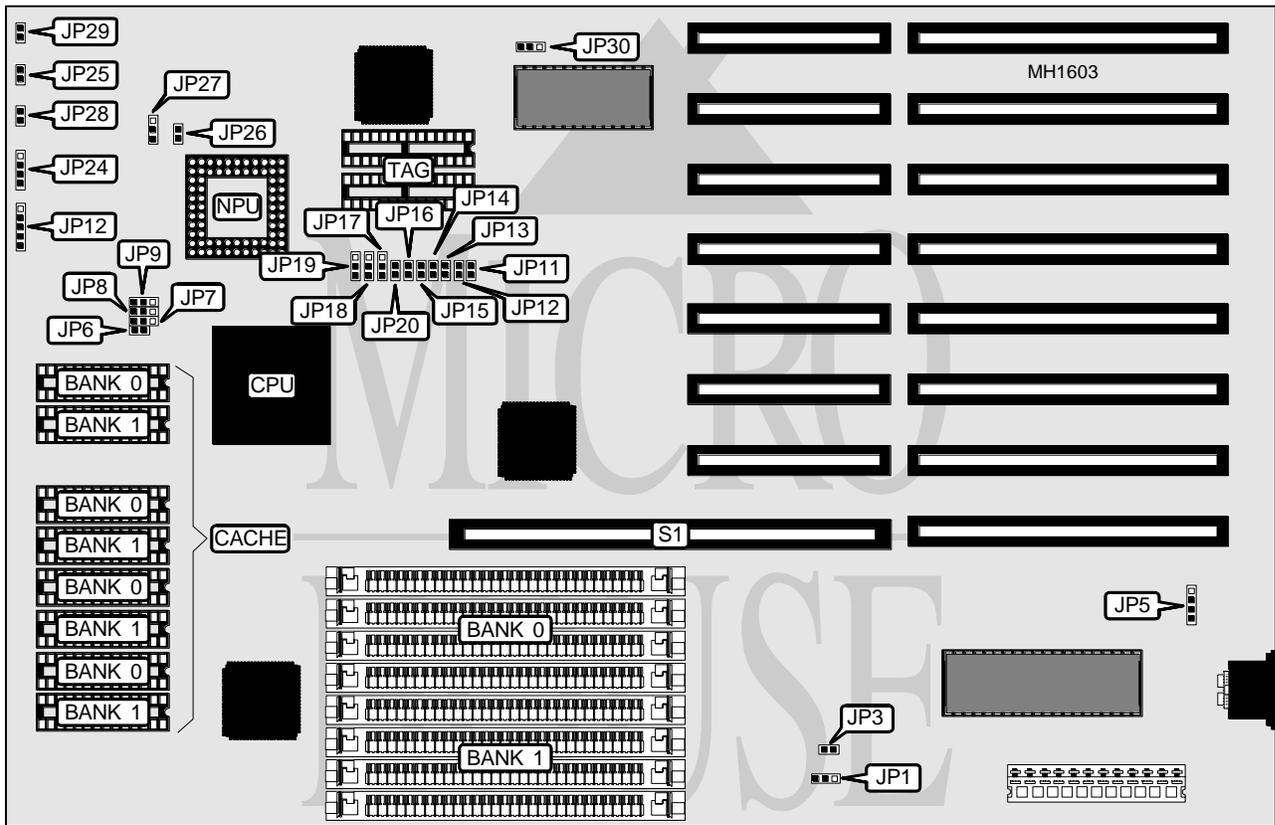


UNIDENTIFIED NT-386/CT

Processor	80386DX
Processor Speed	16/20/25/33MHZ
Chip Set	C & T Peak/DM
Max. onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	Unknown
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP5	Turbo switch	JP28
Power LED & keylock	JP21	Reset switch	JP29
Speaker	JP24	32-bit external memory card	S1
Turbo LED	JP25		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select color	JP3	Closed
Monitor type select monochrome	JP3	Open
í NPU synchronous with CPU	JP26	Open
NPU asynchronous	JP26	Closed
í Power good signal detect from board	JP27	pins 2 & 3 closed
Power good signal detect from power supply	JP27	pins 1 & 2 closed
í CMOS memory normal operation	JP30	pins 2 & 3 closed
CMOS memory clear	JP30	pins 1 & 2 closed

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	JP1
1MB	(4) 256K x 9	NONE	NONE	NONE	pins 1 & 2
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE	pins 1 & 2
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE	pins 1 & 2
4MB	(4) 256K x 9	pins 1 & 2			
4MB	(4) 1M x 9	NONE	NONE	NONE	pins 1 & 2
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE	pins 2 & 3
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE	pins 2 & 3
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE	pins 1 & 2
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	pins 2 & 3
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE	pins 1 & 2
16MB	(4) 1M x 9	pins 1 & 2			
16MB	(4) 4M x 9	NONE	NONE	NONE	pins 1 & 2
17MB	(4) 256K x 9	(4) 4M x 9	NONE	NONE	pins 2 & 3
18MB	(4) 256K x 9	(4) 256K x 9	(4) 4M x 9	NONE	pins 2 & 3
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE	pins 2 & 3
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE	pins 2 & 3
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE	pins 1 & 2
33MB	(4) 4M x 9	(4) 4M x 9	(4) 256K x 9	NONE	pins 2 & 3
34MB	(4) 4M x 9	(4) 4M x 9	(4) 256K x 9	(4) 256K x 9	pins 2 & 3
36MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	NONE	pins 2 & 3
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	pins 2 & 3
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE	pins 1 & 2
64MB	(4) 4M x 9	pins 1 & 2			

Note: Banks 2 & 3 are located on an optional 32-bit external memory card.
Pins designated should be in the closed position.

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(2) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(2) 16K x 4
128KB	(4) 32K x 8	NONE	(2) 64K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(2) 64K x 4

CACHE JUMPER CONFIGURATION				
Size	JP6	JP7	JP8	JP9
32KB	Open	Closed	Closed	Closed
64KB	Open	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
128KB	Closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
256KB	Closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

CACHE JUMPER CONFIGURATION										
Size	JP11	JP12	JP13	JP14	JP15	JP16	JP17	JP18	JP19	JP20
32KB	Open	Open	Closed	Open	Closed	Open	2 & 3	Open	2 & 3	Open
64KB	Open	Open	Closed	Closed	Closed	Open	2 & 3	Open	Open	Closed
128KB	Open	Closed	Open	Closed	Closed	Open	1 & 2	1 & 2	Open	Closed
256KB	Closed	Closed	Open	Closed	Open	Closed	1 & 2	1 & 2	1 & 2	Closed