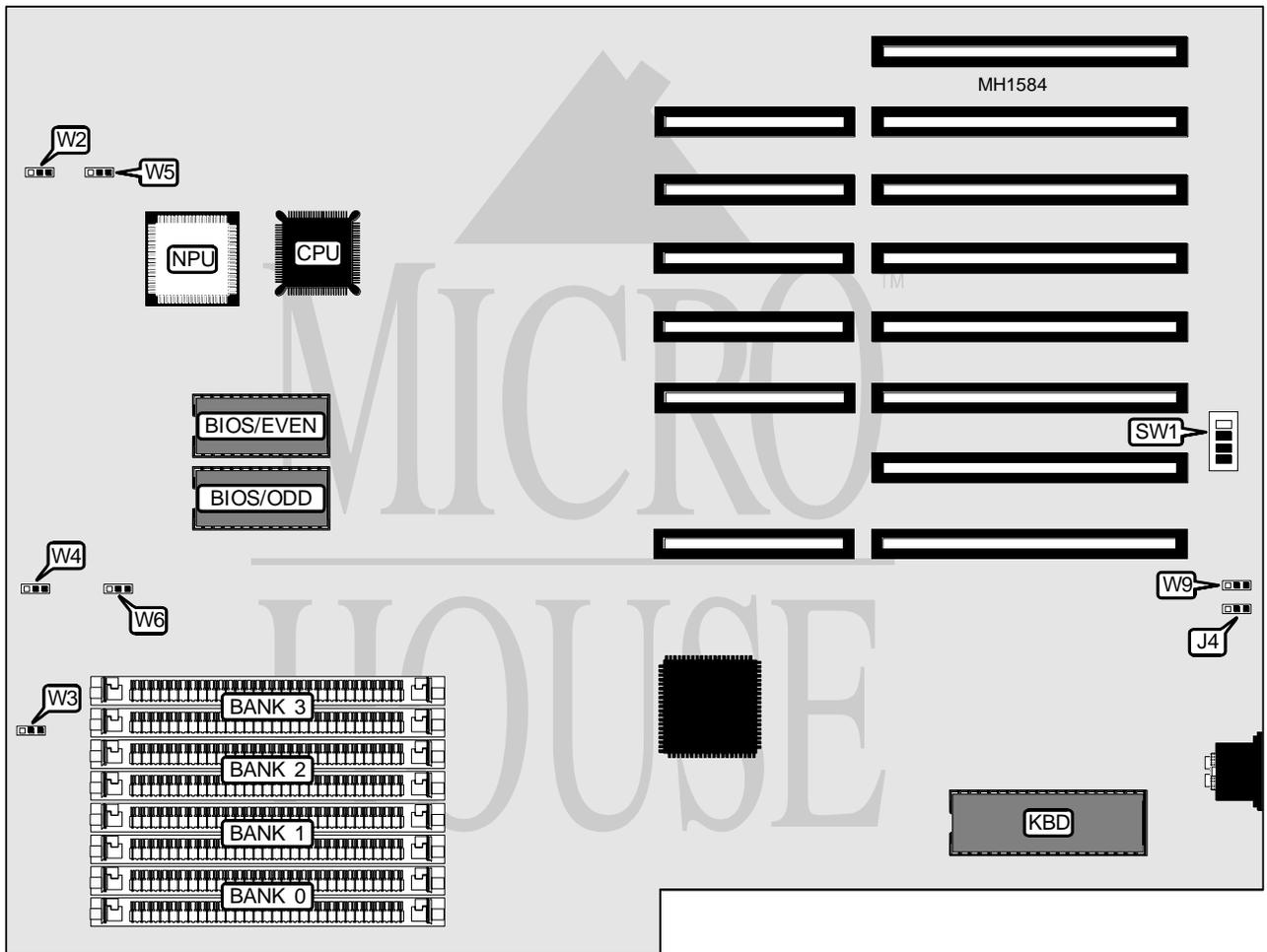


# EVEREX SYSTEMS, INC.

## 3000B 386/16/20 (EV-1864)

<b>Processor</b>	80386SX
<b>Processor Speed</b>	20MHz
<b>Chip Set</b>	C & T
<b>Max. onboard DRAM</b>	8MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	355mm x 304mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387SX



CONNECTIONS	
Purpose	Location
External battery	J4

Continued on next page . . .

## EVEREX SYSTEMS, INC.

## 3000B 386/16/20 (EV-1864)

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	W1	Closed
í8-bit-select 5 wait state	W2	pins 2 & 3 closed
8-bit-select 4 wait state	W2	pins 1 & 2 closed
í CPU speed select normal	W3	pins 2 & 3 closed
CPU speed select high	W3	Open
CPU speed select low	W3	pins 1 & 2 closed
í 16-bit select 2 wait state	W5	pins 1 & 2 closed
16-bit select 1 wait state	W5	pins 2 & 3 closed
í Factory configured - do not alter	W7	Open
í Factory configured - do not alter	W8	Closed
í Monitor type select color	W9	pins 1 & 2 closed
Monitor type select monochrome	W9	pins 2 & 3 closed
Note: Exact location of W1 & W8 is unknown.		

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3	W4	W6
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE	pins 1 & 2	pins 1 & 2
2MB	(2) 256K x 9	pins 1 & 2	pins 1 & 2			
2MB	(2) 1M x 9	NONE	NONE	NONE	pins 2 & 3	pins 2 & 3
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE	pins 2 & 3	pins 2 & 3
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE	pins 2 & 3	pins 2 & 3
8MB	(2) 1M x 9	pins 2 & 3	pins 2 & 3			
Note: Pins designated should be in the closed position.						