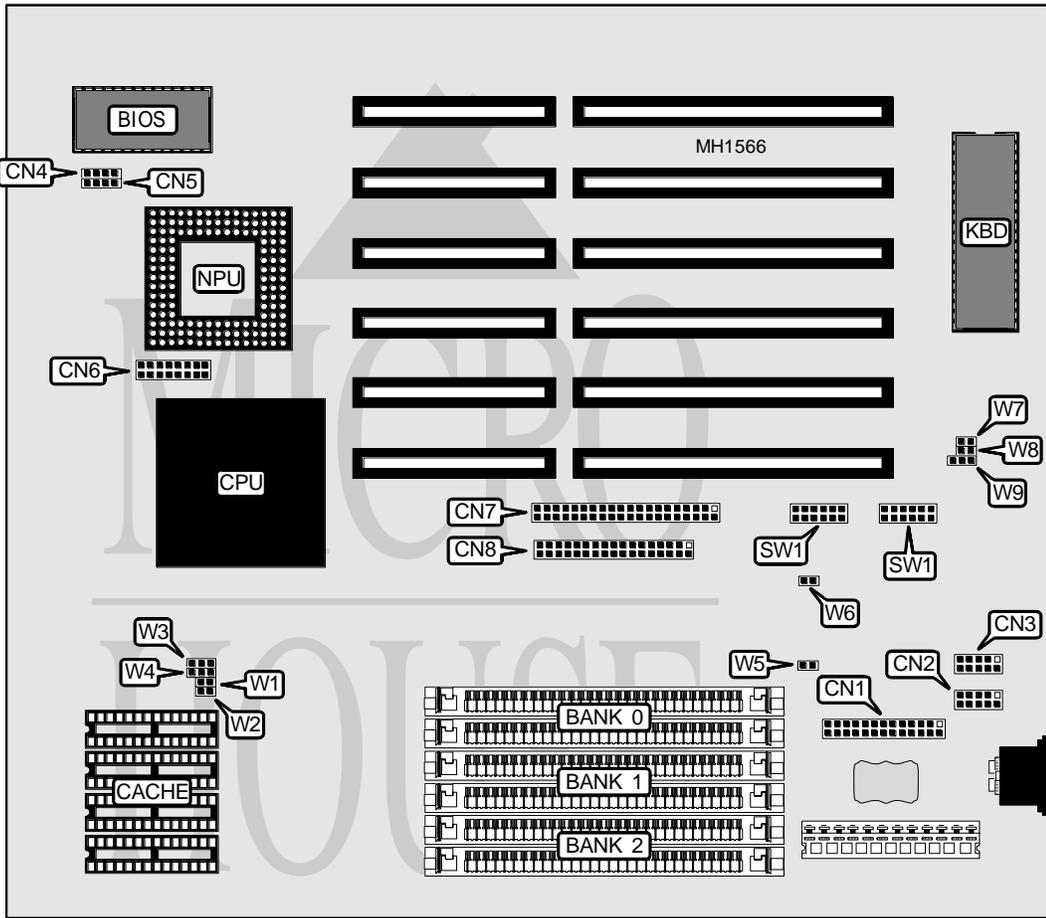


EVEREX SYSTEMS, INC.
TEMPO 486/25 SX EV-18186

Processor	CX486SLC
Processor Speed	25MHz
Chip Set	C & T
Max. onboard DRAM	24MB
SRAM Cache	16/64KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2)
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Parallel port	CN1	Speaker connector	CN5
Serial port (COM1)	CN2	LED connector	CN6
Serial port (COM2)	CN3	IDE interface	CN7
IDE interface LED	CN4	Floppy drive interface	CN8

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TEMPO 486/25 SX EV-18186

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í I/O output select unidirectional (printer)	W5	Closed
I/O output select bidirectional	W5	Open
í IDE interface enabled	W6	Closed
IDE interface disabled	W6	Open
í Normal setup	W7	Closed
Bypass setup	W7	Open
í Monitor select color	W8	Open
Monitor select monochrome	W8	Closed
í I/O bus speed select ATCLK/4	W9	pins 1 & 2 closed
I/O bus speed select ATCLK/2	W9	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
512KB	(2) 256K x 9	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 1M x 9	NONE	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE
24MB	(2) 4M x 9	(2) 4M x 9	(2) 4M x 9

CACHE CONFIGURATION

Size	CACHE
16KB	(4) 8K x 8
64KB	(4) 32K x 8

CACHE JUMPER CONFIGURATION

Size	W1	W2	W3	W4
16KB	Open	Open	pins 2 & 3 closed	pins 2 & 3 closed
64KB	Closed	Closed	pins 1 & 2 closed	pins 1 & 2 closed

PARALLEL PORT CONFIGURATION

LPT	IRQ	SW1/5 & 6	SW1/7 & 8	SW2/9 & 10	SW2/11 & 12
1	7	Open	Closed	Closed	Open
2	5	Closed	Open	Open	Closed
Disabled	N/A	Open	Open	Open	Open

SERIAL PORT CONFIGURATION

Port 1	Port 2	SW1/pins 1 & 2	SW1/pins 3 & 4
COM1	COM2	Closed	Closed
COM1	Disabled	Open	Closed
Disabled	COM2	Closed	Open
Disabled	Disabled	Open	Open