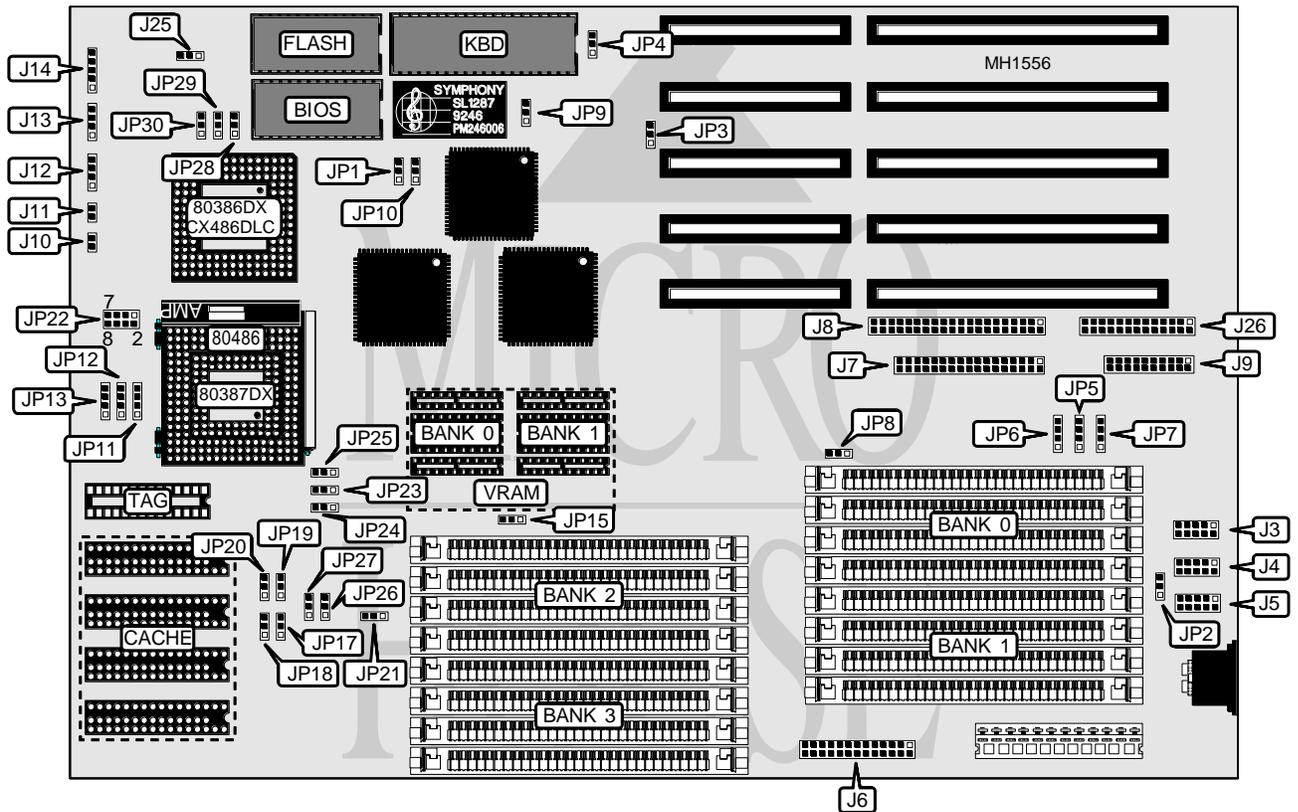


DEICO ELECTRONICS, INC. PREDATOR II

Processor	80386DX/CX486DLC/80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/50(internal)/40/50/66(internal)MHz
Chip Set	Symphony
Max. onboard DRAM	64MB
SRAM Cache	128/512KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, PS/2 mouse port serial ports (2), VGA feature connector, VGA port
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	J3	IDE interface LED	J11
Serial port 2	J4	Turbo switch	J12/pins 1 & 2
PS/2 mouse port	J5	Turbo LED	J12/pins 3 & 4
Parallel port	J6	Speaker	J13
Floppy drive interface	J7	Power LED & keylock	J14
IDE interface	J8	CPU clock speed display	J25
VGA port	J9	VGA feature connector	J26
Reset switch	J10		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í PS/2 mouse port enabled (IRQ12)	JP1	pins 1 & 2 closed
PS/2 mouse port disabled	JP1	pins 2 & 3 closed
í Factory configured - do not alter	JP2	pins 2 & 3 closed
í Flash BIOS program mode enabled	JP3	pins 1 & 2 closed
Flash BIOS program mode disabled	JP3	pins 2 & 3 closed
í BIOS select flash BIOS	JP4	pins 1 & 2 closed
BIOS select standard BIOS	JP4	pins 2 & 3 closed
í All main board I/O ports enabled	JP8	pins 1 & 2 closed
All main board I/O ports disabled	JP8	pins 2 & 3 closed
í Monitor type select color	JP9	pins 1 & 2 closed
Monitor type select monochrome	JP9	pins 2 & 3 closed
í CMOS memory normal operation	JP10	pins 1 & 2 closed
CMOS memory clear	JP10	pins 2 & 3 closed
í Onboard EGA disabled	JP21	pins 2 & 3 closed
Onboard EGA enabled	JP21	pins 1 & 2 closed
í Onboard VGA enabled	JP22	pins 1 & 2 closed
Onboard VGA disabled	JP22	pins 2 & 3 closed

Note: J8 does not disable the PS/2 mouse or the onboard VGA port.

SERIAL PORT 1 IRQ CONFIGURATION			
Address	COM Assignment	IRQ	JP5
Disabled	N/A	N/A	pins 3 & 4 closed
3F8	COM1	4	pins 1 & 2 closed
3F8	COM1	5	pins 2 & 3 closed
3E8	COM3	4	pins 1 & 2 closed
3E8	COM3	5	pins 2 & 3 closed

Note: If "AUTO" I/O port management is enabled in BIOS, serial port COM assignments will be auto-configuring. If "MANUAL" I/O port management is enabled, COM assignments will follow the table above.

SERIAL PORT 2 IRQ CONFIGURATION			
Address	COM Assignment	IRQ	JP6
Disabled	N/A	N/A	pins 3 & 4 closed
2F8	COM2	3	pins 1 & 2 closed
2F8	COM2	9	pins 2 & 3 closed
2E8	COM4	3	pins 1 & 2 closed
2E8	COM4	9	pins 2 & 3 closed

Note: If "AUTO" I/O port management is enabled in BIOS, serial port COM assignments will be auto-configuring. If "MANUAL" I/O port management is enabled, COM assignments will follow the table above.

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PARALLEL PORT IRQ CONFIGURATION			
Address	LPT Assignment	IRQ	JP7
Disabled	N/A	N/A	pins 3 & 4 closed
378	LPT1	7	pins 1 & 2 closed
378	LPT1	5	pins 2 & 3 closed
278	LPT2	7	pins 1 & 2 closed
278	LPT2	5	pins 2 & 3 closed
3BC	LPT3	7	pins 1 & 2 closed
3BC	LPT3	5	pins 2 & 3 closed

CACHE CONFIGURATION		
Size	Cache	TAG
128KB	(4) 32K x 8	(1) 32K x 8
512KB	(4) 128K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP17	JP18	JP19	JP20
128KB	pins 1 & 2 closed			
512KB	pins 2 & 3 closed			

VRAM CONFIGURATION		
Size	Location	Video RAM
512KB	Bank 0	(4) 256K x 4 ZIP
1MB	Banks 0 & 1	(4) 256K x 4 ZIP

CPU TYPE CONFIGURATION							
Jumper	80386DX	CX486DLC	80486SX	80487SX	ODP486SX	80486DX	80486DX2
JP11	pins 1 & 2	pins 1 & 2	pins 3 & 4	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2
JP12	pins 1 & 2	pins 1 & 2	pins 3 & 4	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2
JP13	pins 1 & 2	pins 1 & 2	pins 3 & 4	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2
JP14	pins 1 & 2	pins 1 & 2	pins 2 & 3				
JP15	pins 1 & 2	pins 2 & 3	pins 1 & 2				
JP23	pins 1 & 2	pins 2 & 3	pins 1 & 2				
JP24	pins 1 & 2	pins 2 & 3	pins 1 & 2				
JP25	pins 1 & 2	pins 1 & 2	pins 2 & 3				
JP26	pins 1 & 2	pins 1 & 2	pins 2 & 3				
JP27	pins 1 & 2	pins 1 & 2	pins 2 & 3				
JP28	pins 1 & 2	pins 2 & 3	pins 1 & 2				
JP29	pins 1 & 2	pins 2 & 3	pins 1 & 2				
JP30	pins 1 & 2	pins 2 & 3	pins 1 & 2				

Notes: Pins designated should be in the closed position.
Location of jumper JP14 is unknown. Later versions of the board lack JP14 altogether.

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CPU SPEED CONFIGURATION				
Speed	JP22/pins 1 & 2	JP22/pins 3 & 4	JP22/pins 5 & 6	JP22/pins 7 & 8
20MHz	Closed	Open	Open	Open
25/50i MHz	Open	Closed	Open	Open
33/66i MHz	Open	Open	Closed	Open
40MHz	open	Open	Open	Closed
50MHz	Open	Closed	Open	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9			
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 1M x 9	(4) 256K x 9	NONE	NONE
6MB	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	NONE
7MB	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	NONE
10MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9			
16MB	(4) 4M x 9	NONE	NONE	NONE
17MB	(4) 4M x 9	(4) 256K x 9	NONE	NONE
18MB	(4) 4M x 9	(4) 256K x 9	(4) 256K x 9	NONE
19MB	(4) 4M x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 256K x 9	NONE	NONE
21MB	(4) 4M x 9	(4) 1M x 9	(4) 256K x 9	NONE
22MB	(4) 4M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
24MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	NONE
28MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
33MB	(4) 4M x 9	(4) 4M x 9	(4) 256K x 9	NONE
34MB	(4) 4M x 9	(4) 4M x 9	(4) 256K x 9	(4) 256K x 9
36MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	NONE
37MB	(4) 256K x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
49MB	(4) 256K x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
52MB	(4) 4M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 4M x 9			