

ICL

AC41644/41647 MODELS E450/X450

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| USER CONFIGURABLE SETTINGS | | |
|-------------------------------------|------------------|----------|
| Function | Jumper | Position |
| í Factory configured - do not alter | JP1 pins 9 & 10 | Closed |
| í Password disabled | JP1 pins 15 & 16 | Open |
| Password enabled | JP1 pins 15 & 16 | Closed |
| í Factory configured - do not alter | JP1 pins 17 & 18 | Closed |
| í BIOS write protect enabled | JP1 pins 19 & 20 | Open |
| BIOS write protect disabled | JP1 pins 19 & 20 | Closed |

| DRAM CONFIGURATION | | |
|--------------------|-------------|-------------|
| Size | Bank 0 | Bank 1 |
| 8MB | (2) 1M x 36 | NONE |
| 16MB | (2) 1M x 36 | (2) 1M x 36 |
| 16MB | (2) 2M x 36 | NONE |
| 24MB | (2) 1M x 36 | (2) 2M x 36 |
| 32MB | (2) 2M x 36 | (2) 2M x 36 |
| 32MB | (2) 4M x 36 | NONE |
| 40MB | (2) 1M x 36 | (2) 4M x 36 |
| 48MB | (2) 2M x 36 | (2) 4M x 36 |
| 64MB | (2) 4M x 36 | (2) 4M x 36 |
| 72MB | (2) 1M x 36 | (2) 8M x 36 |
| 80MB | (2) 2M x 36 | (2) 8M x 36 |
| 96MB | (2) 4M x 36 | (2) 8M x 36 |
| 128MB | (2) 8M x 36 | (2) 8M x 36 |

| CMOS MODE CONFIGURATION | |
|-------------------------|---------------------|
| Mode | JP1 |
| Normal | pins 11 & 12 closed |
| Clear | pins 13 & 14 closed |

| CPU SPEED CONFIGURATION | | | |
|-------------------------|----------------|----------------|----------------|
| Speed | JP1 pins 3 & 4 | JP1 pins 5 & 6 | JP1 pins 7 & 8 |
| 75MHz | Closed | Closed | Open |
| 90MHz | Closed | Open | Closed |
| 100MHz | Open | Closed | Open |