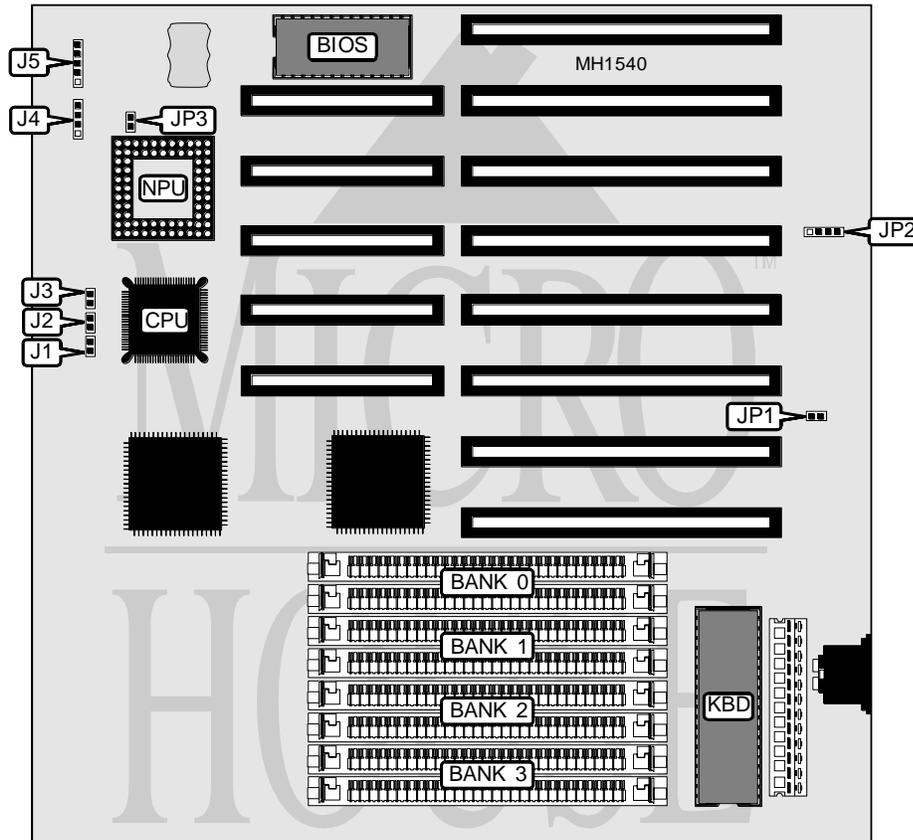


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Processor	80386SX
Processor Speed	16/20MHz
Chip Set	OPTI
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J1	Speaker	J4
Turbo LED	J2	Power LED & keylock	J5
Reset switch	J3	External battery	JP2

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Turbo mode enabled	J1	Open
Normal mode enabled CLK2/6 OR CLK2/4	J1	Closed
í Monitor type select monochrome	JP1	Open
Monitor type select color	JP1	Closed
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 3 & 4 closed
í NPU asynchronous with CPU	JP3	Closed
NPU synchronous with CPU	JP3	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9			
2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
9MB	(2) 256K x 9	(2) 256K x 9	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
12MB	(2) 1M x 9	(2) 1M x 9	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

DRAM CONFIGURATION								
Size	Bank 0	Parity 0	Bank 1	Parity 1	Bank 2	Parity 2	Bank 3	Parity 3
256KB								
256KB								
512KB								
512KB								
576KB								
576KB								
640KB								
640KB								

Note: When using 44256 or 4464 chips, insert the chips into the dual-size sockets.

CACHE CONFIGURATION				
Size	Max Cachable	Bank 0	Bank 1	TAG
64KB	8MB	(4) 8K x 8	(4) 8K x 8	(2) 16K x 4
128KB	16MB	(4) 32K x 8	NONE	(2) 16K x 4
256KB	32MB	(4) 32K x 8	(4) 32K x 8	(2) 16K x 4

CACHE JUMPER CONFIGURATION				
Size	W3	W4	W5	W6
64KB	pins 1 & 2 closed			
128KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed			

VESA CPU TYPE (ID0 & ID1) CONFIGURATION		
CPU Type	JP1 (ID0)	JP2 (ID1)
80386	pins 2 & 3 closed	pins 1 & 2 closed
80486	pins 1 & 2 closed	pins 2 & 3 closed

VESA WAIT STATE/BUS SPEED (ID2 & ID3) CONFIGURATION			
CPU speed	Wait states	JP4 (ID3)	JP5 (ID2)
< 33MHz	0 wait states	pins 1 & 2 closed	pins 1 & 2 closed
> 33MHz	1 wait state	pins 2 & 3 closed	pins 2 & 3 closed

VESA ID4 CONFIGURATION	
Setting	JP6 (ID4)
Reserved for future use	pins 2 & 3 closed

MISCELLANEOUS TECHNICAL NOTE
Note: Pins designated should be in the closed position.