# PACKARD BELL
# P B  3 8 6 - 2 5  ( R E V I S I O N  D )

| | |
|---|---|
| **Processor** | 80386DX |
| **Processor Speed** | 25MHz |
| **Chip Set** | Intel |
| **Max. onboard DRAM** | 8MB |
| **Cache** | None |
| **BIOS** | Phoenix |
| **Dimensions** | 330mm x 240mm |
| **I/O Options** | 32-bit expansion slots (2), parallel port, serial ports (2) |
| **NPU Options** | 80387/3167 |



| CONNECTIONS | | | |
|---|---|---|---|
| Purpose | Location | Purpose | Location |
| Parallel port | J10A | Reset switch | J18 |
| Serial port 1 (9 pin) | J10B/pins 1-10 | Speaker | J19 |
| Serial port 2 (9 pin) | J10B/pins 11-20 | Keylock | J20 |
| Serial port 2 (25 pin) | J10C | 32-bit expansion slots | S1 & S2 |
| Note:   J10C is a 25 pin version of serial port 2 (J10B pins 11-20).  Either connector may be used, but not both. | | | |
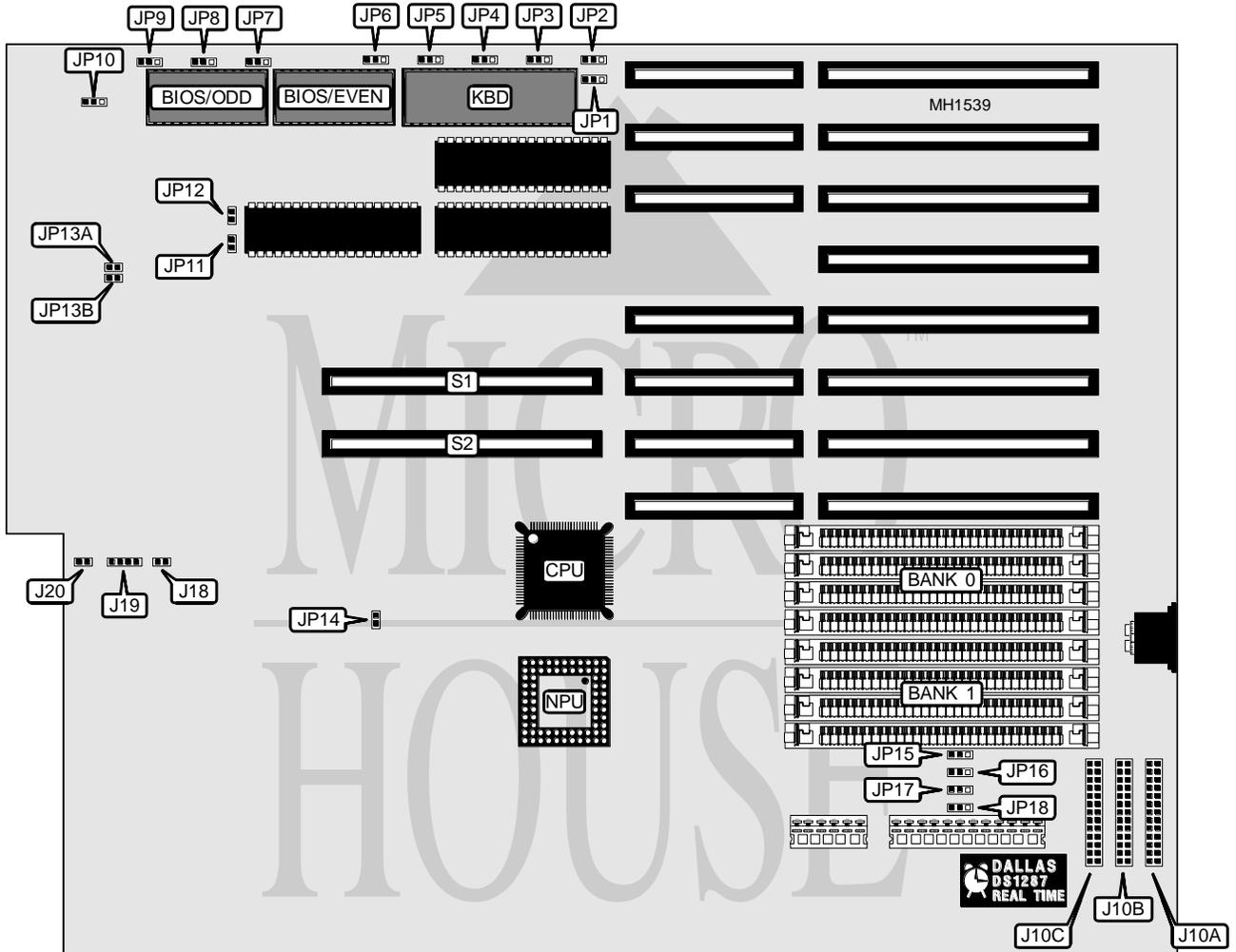
# PACKARD BELL
# PB 386-25 (REVISION D)

| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| Function | Jumper | Position |
| í  Printer acknowledge trailing edge | JP1 | pins 2 & 3 closed |
| Printer acknowledge leading edge | JP1 | pins 1 & 2 closed |
| í  NPU disabled | JP6 | pins 2 & 3 closed |
| NPU enabled | JP6 | pins 1 & 2 closed |
| í  Keyboard reset disabled | JP7 | pins 2 & 3 closed |
| Keyboard reset enabled | JP7 | pins 1 & 2 closed |
| í  System type select DOS | JP8 | pins 2 & 3 closed |
| System type select UNIX | JP8 | pins 1 & 2 closed |
| í  Monitor type select color | JP9 | pins 2 & 3 closed |
| Monitor type select monochrome | JP9 | pins 1 & 2 closed |
| í  BIOS type select 2764 | JP10 | pins 1 & 2 closed |
| BIOS type select 27128 | JP10 | pins 2 & 3 closed |

| SERIAL PORT CONFIGURATION | | | |
|---|---|---|---|
| Port 1 (J10B/1-10) | Port 2 (J10B/11-20 or J10C) | JP4 | JP5 |
| í  COM1/IRQ4 | COM2/IRQ3 | pins 1 & 2 | pins 1 & 2 |
| COM1/IRQ4 | Disabled | pins 1 & 2 | pins 2 & 3 |
| Disabled | COM2/IRQ3 | pins 2 & 3 | pins 1 & 2 |
| Disabled | Disabled | pins 2 & 3 | pins 2 & 3 |
| Note:    Pins designated should be in the closed position. | | | |

| PARALLEL PORT (J10A) CONFIGURATION | | |
|---|---|---|
| LPT | JP2 | JP3 |
| í  LPT1 | pins 1 & 2 closed | pins 2 & 3 closed |
| LPT2 | pins 2 & 3 closed | pins 1 & 2 closed |
| Disabled | pins 2 & 3 closed | pins 2 & 3 closed |

| DRAM CONFIGURATION | | | | | | |
|---|---|---|---|---|---|---|
| Size | Bank 0 | Bank 1 | JP17 | JP18 | JP13A | JP13B |
| 1MB | (4) 256K x 9 | NONE | pins 1 & 2 | pins 1 & 2 | Open | Open |
| 2MB | (4) 256K x 9 | (4) 256K x 9 | pins 1 & 2 | pins 1 & 2 | Open | Closed |
| 4MB | (4) 1M x 9 | NONE | pins 2 & 3 | pins 2 & 3 | Closed | Open |
| 8MB | (4) 1M x 9 | (4) 1M x 9 | pins 2 & 3 | pins 2 & 3 | Closed | Closed |
| Note:    Pins designated should be in the closed position. | | | | | | |

# PACKARD BELL
# PB 386-25 (REVISION D)

| RAM TYPE CONFIGURATION | | |
|---|---|---|
| Type | JP11 | JP12 |
| RAS/CAS | Open | Open |
| í  Fast Paged | Closed | Closed |
| Static Column | Open | Closed |

| RAM SPEED CONFIGURATION | | |
|---|---|---|
| Speed | JP15 | JP16 |
| 100ns or faster | pins 1 & 2 closed | pins 1 & 2 closed |
| Slower than 100ns | pins 2 & 3 closed | pins 2 & 3 closed |