UNIDENTIFIED
C 6 3 3 2

Processor	80386SX/CX486SLC
Processor Speed	25/33/40MHz
Chip Set	UNI
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	220mm x 163mm
I/O Options	None
NPU Options	80387SX
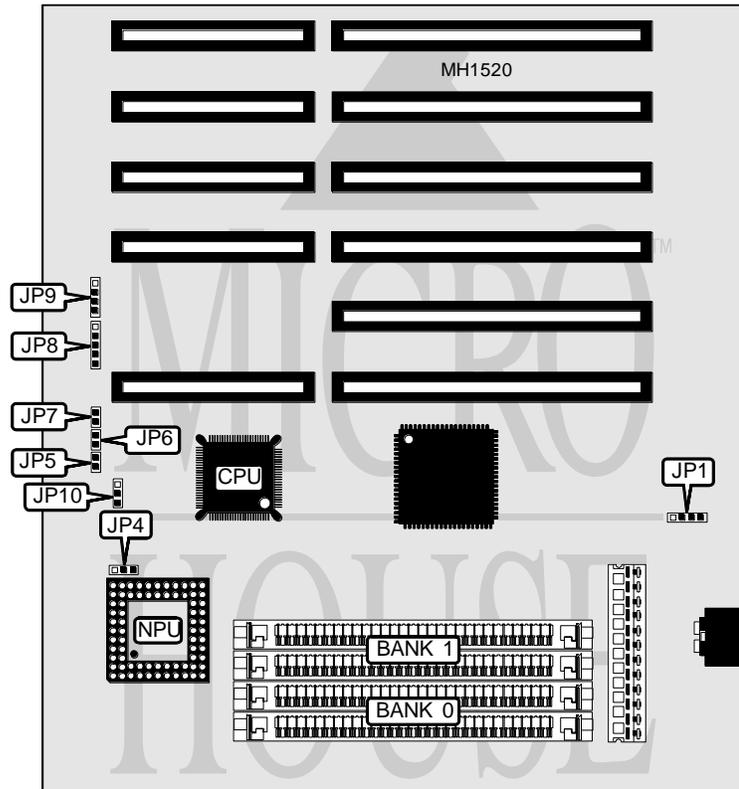
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo LED	JP7
Turbo switch	JP5	Power LED & keylock	JP8
Reset switch	JP6	Speaker	JP9

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CPU (80386) clock type select synchronous (CLK2)	JP4	pins 1 & 2 closed
CPU (80386) clock type select asynchronous (OSC2)	JP4	pins 2 & 3 closed
í Turbo enabled	JP5	Closed
Turbo disabled	JP5	Open

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DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
1MB	(2) 512K x 9	NONE
2MB	(2) 512K x 9	(2) 512K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CPU SPEED CONFIGURATION	
Speed	JP10
í 25/33MHz	pins 2 & 3 closed
40MHz	pins 1 & 2 closed