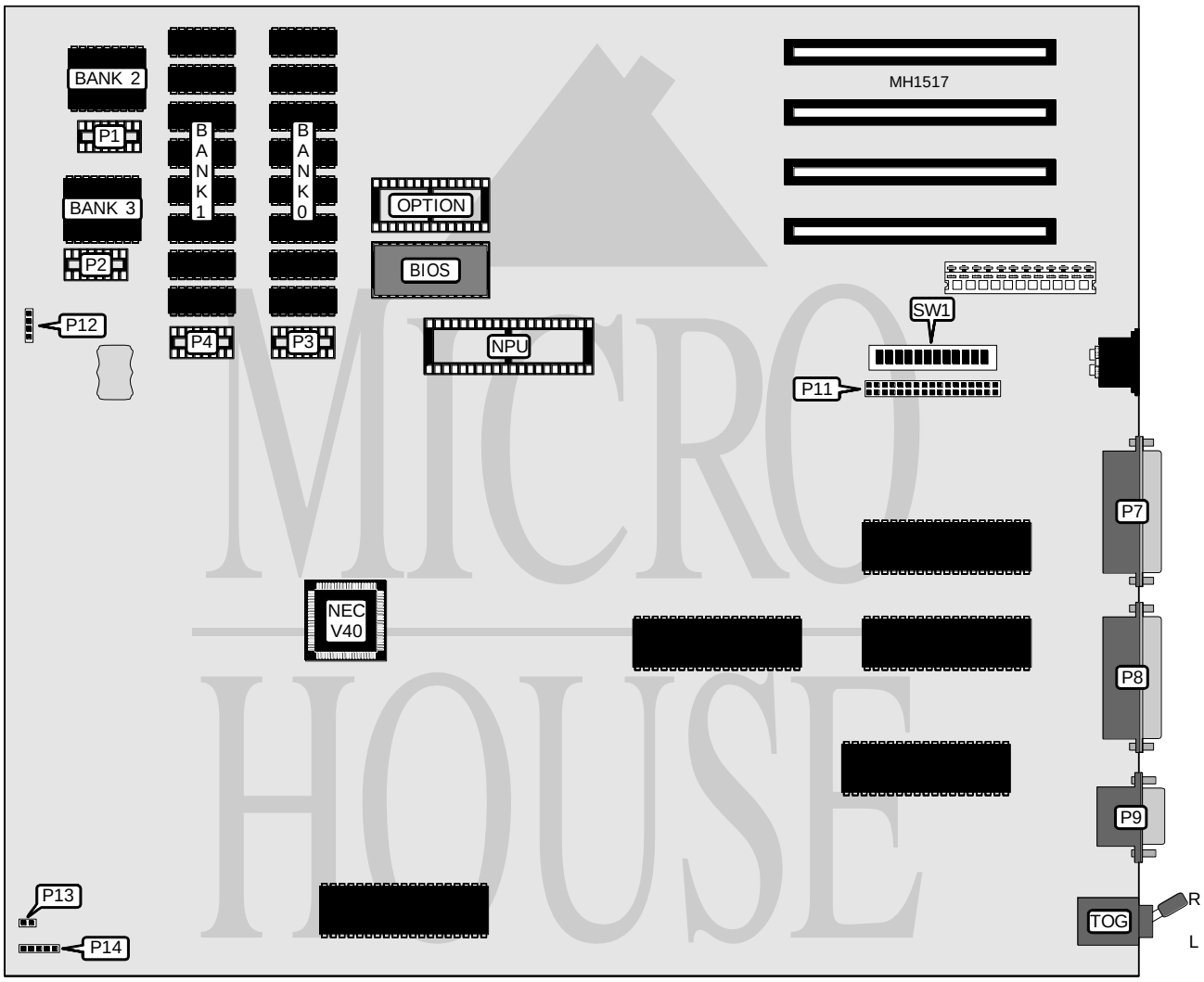
# PACKARD BELL
# P B V X 5 8 8

| | |
|---|---|
| **Processor** | NEC V40 (Integrated 8088-compatible CPU and system controller) |
| **Processor Speed** | 8MHz |
| **Chip Set** | NEC |
| **Max. Onboard DRAM** | 640KB |
| **SRAM Cache** | None |
| **BIOS** | Award |
| **Dimensions** | 355mm x 305mm |
| **I/O Options** | Parallel port, serial port, video port |
| **NPU Options** | 8087 |




| CONNECTIONS | | | |
|---|---|---|---|
| Purpose | Location | Purpose | Location |
| Parallel port | P7 | Speaker | P12 |
| Serial port | P8 | Reset | P13 |
| Video port | P9 | Keylock & power LED | P14 |
| Floppy drive interface | P11 | | |

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| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| Function | Jumper/Switch | Position |
| í Monitor type select color | TOG | Left |
| Monitor type select monochrome | TOG | Right |
| í Factory test mode disabled | SW1/1 | Off |
| Factory test mode enabled | SW1/1 | On |
| í NPU disabled | SW1/2 | On |
| NPU enabled | SW1/2 | Off |
| í System speed select fast (8MHz) | SW1/8 | On |
| System speed select slow (5.5 MHz) | SW1/8 | Off |
| í Serial port enabled | SW1/10 | On |
| Serial port disabled | SW1/10 | Off |
| í Parallel port enabled | SW1/11 | On |
| Parallel port disabled | SW1/11 | Off |

| FLOPPY DRIVE INTERFACE CONFIGURATION | | | |
|---|---|---|---|
| Mode | Drives Installed | SW1/7 | SW1/12 |
| í Enabled | A: | On | On |
| Enabled | A: & B: | Off | On |
| Disabled | N/A | N/A | Off |

| ONBOARD VIDEO CONFIGURATION | | | |
|---|---|---|---|
| Mode | SW1/5 | SW1/6 | SW1/9 |
| Monochrome | Off | Off | On |
| 40x25 CGA | Off | On | On |
| í 80x25 CGA | On | Off | On |
| External EGA card | On | On | Off |

| DRAM CONFIGURATION | | | | | | |
|---|---|---|---|---|---|---|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 | SW1/3 | SW1/4 |
| 256KB | (8) 41256 | NONE | NONE | NONE | On | On |
| 512KB | (8) 41256 | (8) 41256 | NONE | NONE | Off | On |
| 576KB | (8) 41256 | (8) 41256 | (2) 4464 | NONE | On | Off |
| 640KB | (8) 41256 | (8) 41256 | (2) 4464 | (2) 4464 | Off | Off |

Note: Parity sockets P1, P2, P3, and P4 are not used and should be left open. DRAM banks are soldered to the board, and should not be altered.

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