EVEREX SYSTEMS, INC.
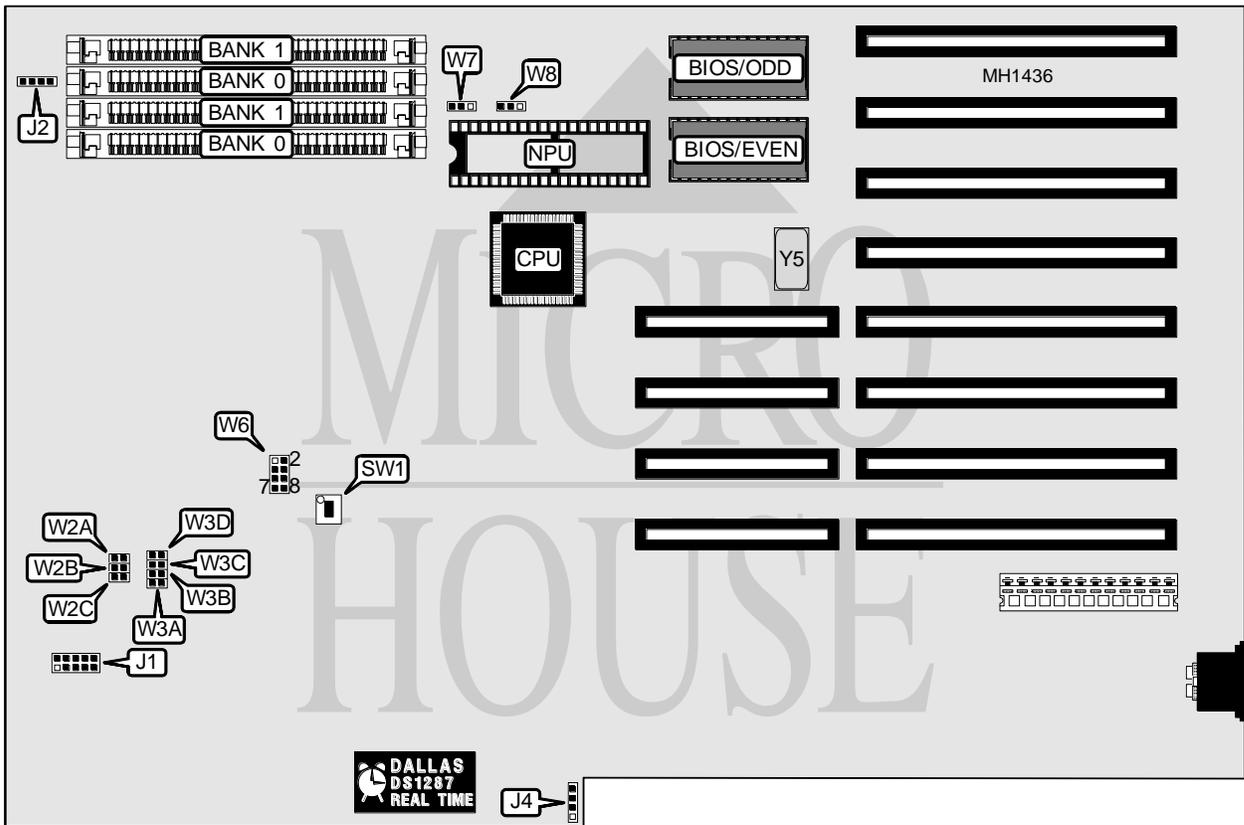# A G I 2 8 6 - 1 2 (E V - 1 8 1 5)

Processor   80286
Processor Speed   12MHz
Chip Set   C & T/Suntac
Max. onboard DRAM   1MB
Cache   None
BIOS   AMI
Dimensions   330mm x 218mm
I/O Options   None
NPU Options   80287

CONNECTIONS			
Purpose	Location	Purpose	Location
Front panel connector	J1	External battery connector	J4
Speaker	J2		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select color	SW1	Off
Monitor type select monochrome	SW1	On
í Factory configured - do not alter	W7	pins 2 & 3 closed
í Factory configured - do not alter	W8	pins 2 & 3 closed

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	W6/pins 3 & 4
512KB	(2) 256K x 9	NONE	Closed
1MB	(2) 256K x 9	(2) 256K x 9	Open

8-bit I/O BUS WAIT STATE CONFIGURATION			
Wait state	W2A	W2B	W2C
4	Closed	Open	Open
5	Open	Closed	Open
í 6	Open	Open	Closed

16-bit I/O BUS WAIT STATE CONFIGURATION			
Wait states	W3D	W3C	W3B	W3A
1	Open	Open	Open	Closed
í 2	Open	Open	Closed	Open
3	Open	Closed	Open	Open
4	Closed	Open	Open	Open