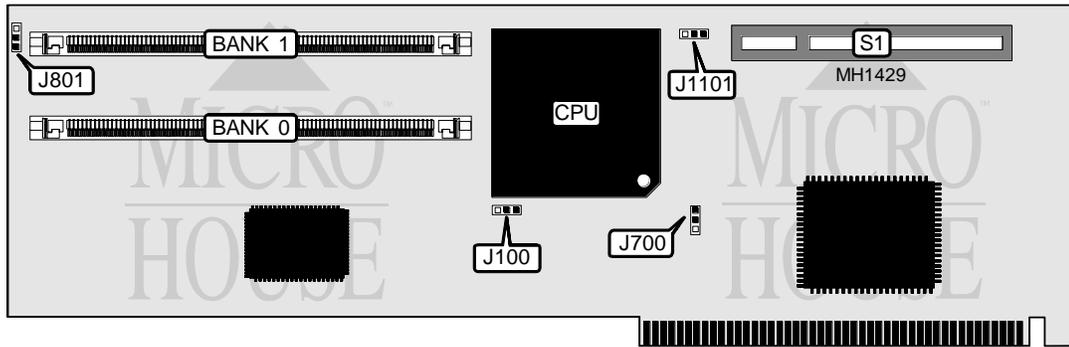


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MCMAS TER MC33PD/MC25PS

Processor	80486SX/80486DX
Processor Speed	25/33MHz
Chip Set	None (utilizes chipset on PS/2 main board)
Max. Onboard DRAM	64MB
SRAM Cache	128KB
BIOS	None (utilizes BIOS on PS/2 main board)
Dimensions	330mm x 100mm
I/O Options	32-bit VESA local processor slot
NPU Options	None



CONNECTIONS	
Purpose	Location
32-bit VESA local processor slot	S1

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CPU select 80486DX	J100	pins 1 & 2 closed
í CPU select 80486SX	J100	pins 2 & 3 closed
í Microchannel Ready timing standard	J700	pins 2 & 3 closed
í Microchannel Ready timing modified	J700	pins 1 & 2 closed
í Local bus fast write transfer mode enabled	J1101	pins 1 & 2 closed
í Local bus fast write transfer mode disabled	J1101	pins 2 & 3 closed

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MCMAS TER MC33PD/MC25PS

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DRAM CONFIGURATION			
Size	J801	Bank 0	Bank 1
4MB	pins 2 & 3 closed	(1) 1024K x 32	NONE
8MB	pins 2 & 3 closed	(1) 1024K x 32	(1) 1024K x 32
8MB	pins 2 & 3 closed	(1) 2048K x 32	NONE
12MB	pins 2 & 3 closed	(1) 2048K x 32	(1) 1024K x 32
16MB	pins 2 & 3 closed	(1) 2048K x 32	(1) 2048K x 32
16MB	pins 1 & 2 closed	(1) 4096K x 32	NONE
20MB	pins 1 & 2 closed	(1) 4096K x 32	(1) 1024K x 32
24MB	pins 1 & 2 closed	(1) 4096K x 32	(1) 2048K x 32
32MB	pins 1 & 2 closed	(1) 4096K x 32	(1) 4096K x 32
32MB	pins 1 & 2 closed	(1) 8192K x 32	NONE
36MB	pins 1 & 2 closed	(1) 8192K x 32	(1) 1024K x 32
40MB	pins 1 & 2 closed	(1) 8192K x 32	(1) 2048K x 32
48MB	pins 1 & 2 closed	(1) 8192K x 32	(1) 4096K x 32
64MB	pins 1 & 2 closed	(1) 8192K x 32	(1) 8192K x 32
Note: Where available, 36-bit SIMMs can be substituted for 32-bit SIMMs.			