UNIDENTIFIED
G W - X T I / 8 8 / V 2

Processor	8088 -1
Processor Speed	10/12 MHz
Chip Set	Tact80101
Max. Onboard DRAM	640K
SRAM Cache	None
BIOS	Unknown
Dimensions	355.6mm x 304mm
I/O Options	None
NPU Options	8087
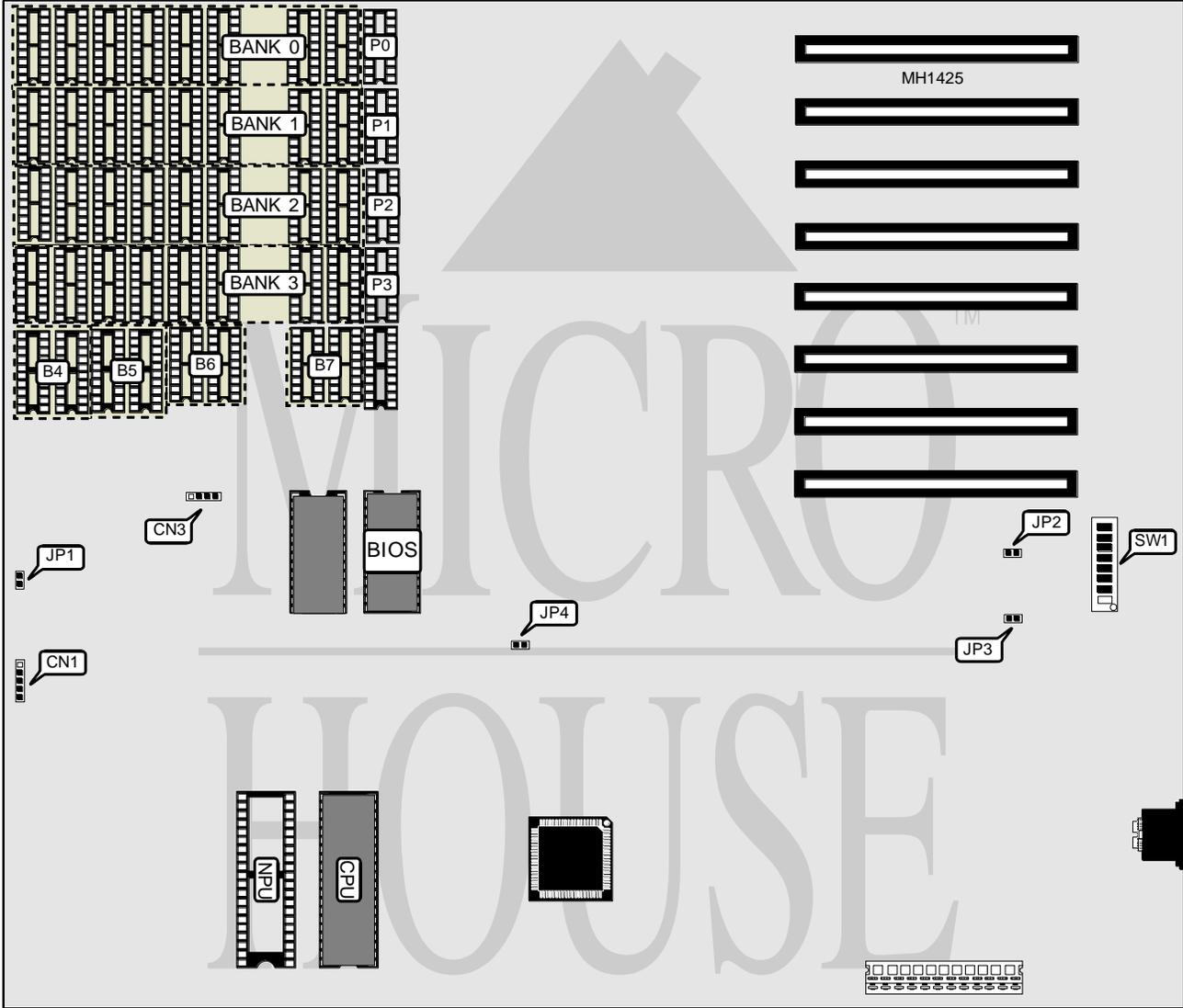
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP1	Key Lock & Power Connector	CN1
Reset Switch	JP4	Speaker Connector	CN3

UNIDENTIFIED
G W - X T I / 8 8 / V 2

USER CONFIGURABLE SETTINGS		
Function	Location	Position
í Parity checking enabled	JP2	open
Parity checking disabled	JP2	closed
í CPU speed select turbo	JP3	open
CPU speed select slow	JP3	open
í Factory configured - do not alter	SW1/switch 1	off
í NPU disabled	SW1/switch 2	on
NPU enabled	SW1/switch 2	off
í Monitor type select CGA	SW1/switch 5	on
	SW1/switch 6	off
Monitor type select EGA	SW1/switch 5	on
	SW1/switch 6	on
Monitor type select MGA	SW1/switch 5	off
	SW1/switch 6	off
í Floppy drive select 1 drive	SW2/switch 7	on
	SW2/switch 8	on
Floppy drive select 2 drives	SW2/switch 7	off
	SW2/switch 8	on
Floppy drive select 3 drives	SW2/switch 7	on
	SW2/switch 8	off
Floppy drive select 4 drives	SW2/switch 7	off
	SW2/switch 8	off
Note: The CPU speed (JP3) must be set before applying power to the computer and should not be changed while power is applied to the main board.		

DRAM CONFIGURATION								
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5	Bank 6	Bank 7
256KB	(8)41256	none	none	none	none	none	none	none
256KB	none	none	none	none	(2)44256	none	none	none
512KB	(8)41256	(8)41256	none	none	none	none	none	none
512KB	none	none	none	none	(2)44256	(2)44256	none	none
640KB	(8)41256	(8)41256	(8)4164	(8)4164	none	none	none	none
640KB	none	none	none	none	(2)44256	(2)44256	(2)4464	(2)4464
640KB	(8)41256	(8)41256	none	none	none	none	(2)4464	(2)4464
640KB	none	none	(8)4164	(8)4164	(2)44256	(2)44256	none	none
Note: DRAM parity must be configured for the appropriate memory configurations.								

UNIDENTIFIED
G W - X T I / 8 8 / V 2

PARITY CONFIGURATION				
Size	P0	P1	P2	P3
256KB	(1)41256	none	none	none
512KB	(1)41256	(1)41256	none	none
640KB	(1)41256	(1)41256	(1)4164	(1)4164
Note: DRAM parity must be configured for the appropriate memory configurations.				

DRAM SWITCH CONFIGURATION		
Size	SWI/switch 3	SW1/Switch 4
256K	off	off
512K	on	off
640K	off	on
640K	on	on