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BABY SPAT-386/NTR-386

Processor	80386DX
Processor Speed	16/20/25MHz
Chip Set	C & T
Max. Onboard DRAM	8MB
SRAM Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Parallel port, serial ports (2), 32-bit external memory card
NPU Options	80287/80387
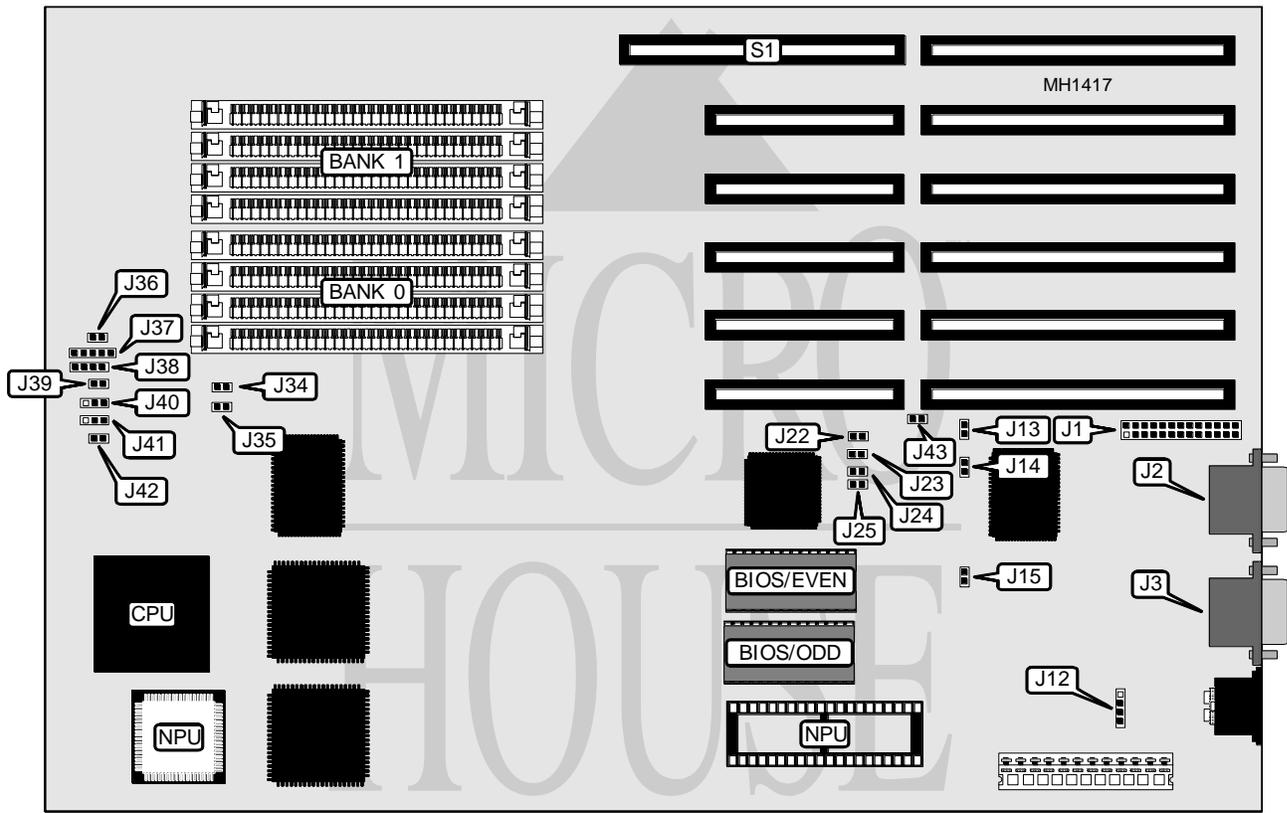
CONNECTIONS			
Purpose	Location	Purpose	Location
Parallel port	J1	Power LED & keylock	J37
Serial port (COM2)	J2	Speaker	J38
Serial port (COM1)	J3	Turbo switch	J39
External battery	J12	Turbo LED	J42
Reset switch	J34	32-bit external memory card	S1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	J36	closed
Monitor type select monochrome	J36	open

SERIAL PORT CONFIGURATION		
Function	Jumper	Position
í Serial port 2 (J2) IRQ 3 enabled	J14	closed
Serial port 2 (J2) IRQ 3 disabled	J14	open
í Serial port 1 (J3) IRQ 4 enabled	J15	closed
Serial port 1 (J3) IRQ 4 disabled	J15	open
í Serial port 1 (J3) enabled	J22	closed
Serial port 1 (J3) disabled	J22	open
í Serial port 2 (J2) enabled	J23	closed
Serial port 2 (J2) disabled	J23	open

PARALLEL PORT 1 (J1) CONFIGURATION				
Port	J13	J24	J25	J43
í LPT 1	closed	closed	closed	open
LPT 2	open	closed	open	closed
Disabled	open	open	open	open

NPU TYPE CONFIGURATION			
NPU type	J35	J40	J41
í Disabled	open	open	open
80287	open	pins 1 & 2 closed	open
80387 sync-mode	closed	pins 2 & 3 closed	pins 1 & 2 closed
80387 async-mode	closed	pins 2 & 3 closed	pins 2 & 3 closed

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32-BIT EXTERNAL MEMORY CARD
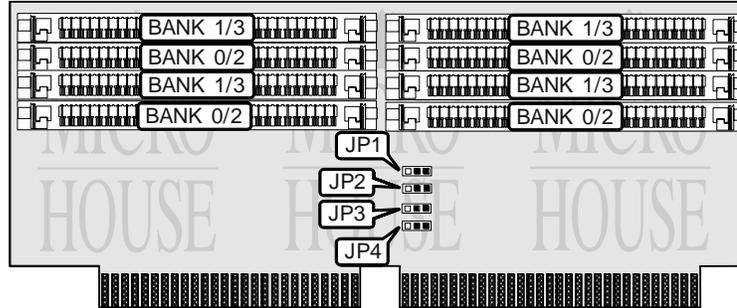
DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	NONE
10MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
Note: Expansion board can be set to replace main board memory, or function as additional banks.				

32-BIT EXTERNAL MEMORY CARD JUMPER SETTINGS		
Function	Jumper	Position
í Main board bank 0 enabled	JP1 & JP3	pins 2 & 3 closed
Main board bank 0 disabled	JP1 & JP3	pins 1 & 2 closed
í Main board bank 1 enabled	JP2 & JP4	pins 2 & 3 closed
Main board bank 1 disabled	JP2 & JP4	pins 1 & 2 closed
Notes: Pins designated are in the closed postion. Disabling bank 0 on the main board changes bank 2 on the memory card to bank 0. Disabling bank 1 on the main board changes bank 3 on the memory card to bank 1.		