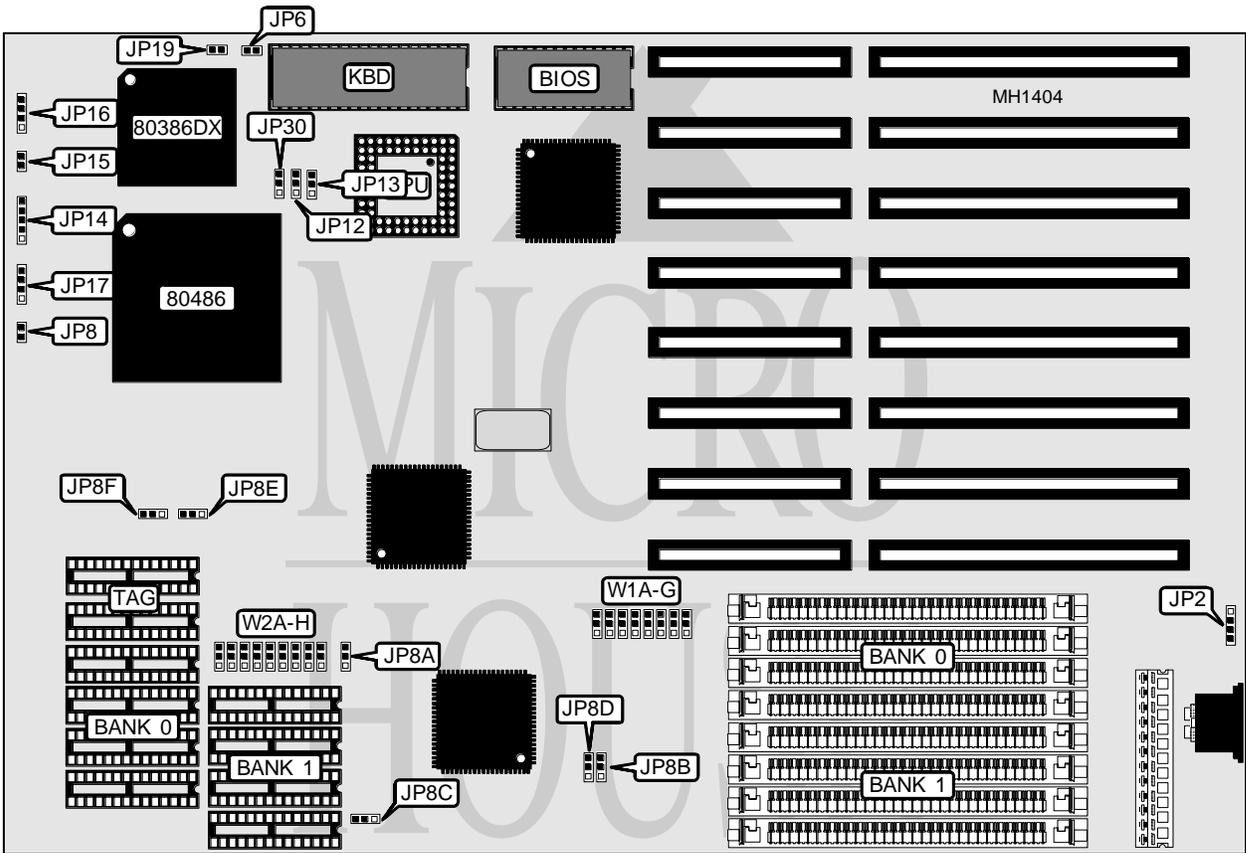


# JETPRO INFOTECH COMPANY, LTD.

## M B - 4 F

<b>Processor</b>	80386DX/CX486DLC/80486SX/80487SX/80486DX/80486DX2
<b>Processor Speed</b>	20/25/33/40/50(internal)/66(internal)
<b>Chip Set</b>	FOREX
<b>Max. Onboard DRAM</b>	32MB
<b>SRAM Cache</b>	64/128/256KB
<b>BIOS</b>	AMI/AWARD
<b>Dimensions</b>	220mm x 340mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Turbo switch	JP15
Reset switch	JP8	Turbo LED	JP16
Power LED & keylock	JP14	Speaker	JP17

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MB - 4 F

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Parity check enabled	JP5	closed
Parity check disabled	JP5	open
í Monitor type select color	JP6	closed
Monitor type select monochrome	JP6	open
í Factory configured - do not alter	JP19	open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CPU TYPE CONFIGURATION					
CPU	JP12	JP13	JP30	W1A-W1G	W2A-W2H
80486DX2	pins 1 & 2				
80486DX	pins 1 & 2				
80487SX	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2
80486SX	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2
80386DX	N/A	N/A	N/A	pins 2 & 3	pins 2 & 3

Note: Pins designated should be in the closed position.

SRAM CONFIGURATION				
Size	Cache SRAM	Location	TAG(U26)	TAG(U42)
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8	NONE
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8	NONE
256KB	(8) 32K x 8	Banks 0 & 1	(1) 8K x 8	(1) 8K x 8

Note: The exact locations of U26 and U42 are unknown.

SRAM JUMPER CONFIGURATION						
Size	JP8A	JP8B	JP8C	JP8D	JP8E	JP8F
64KB	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 2 & 3	pins 1 & 2
128KB	pins 1 & 2	pins 2 & 3	pins 1 & 2			
256KB	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3

Note: Pins designated should be in the closed position.