# EVEREX SYSTEMS, INC.
# TEMPO 486-25/33/50 EV-18173

| | |
|---|---|
| **Processor** | 80486SX/80487SX/80486DX |
| **Processor Speed** | 25/33/50MHZ |
| **Chip Set** | C & T/Suntac |
| **Max. onboard DRAM** | 32MB |
| **SRAM Cache** | 32/64KB |
| **BIOS** | AMI |
| **Dimensions** | 330mm x 218mm |
| **I/O Options** | Floppy drive interface, front panel connector, IDE interface parallel port, serial ports (2) |
| **NPU Options** | None |



| CONNECTIONS | | | |
|---|---|---|---|
| Purpose | Location | Purpose | Location |
| Parallel Port | CN1 | Reset switch | J1 |
| Serial Port (COM1) | CN2 | Speaker | J3 |
| Serial Port (COM2) | CN3 | Front panel connector | J10 |
| IDE interface | CN4 | IDE interface LED | J13 |
| Floppy drive interface | CN5 | Unknown | J18 |
| Note:    The location of J1 is unknown. | | | |
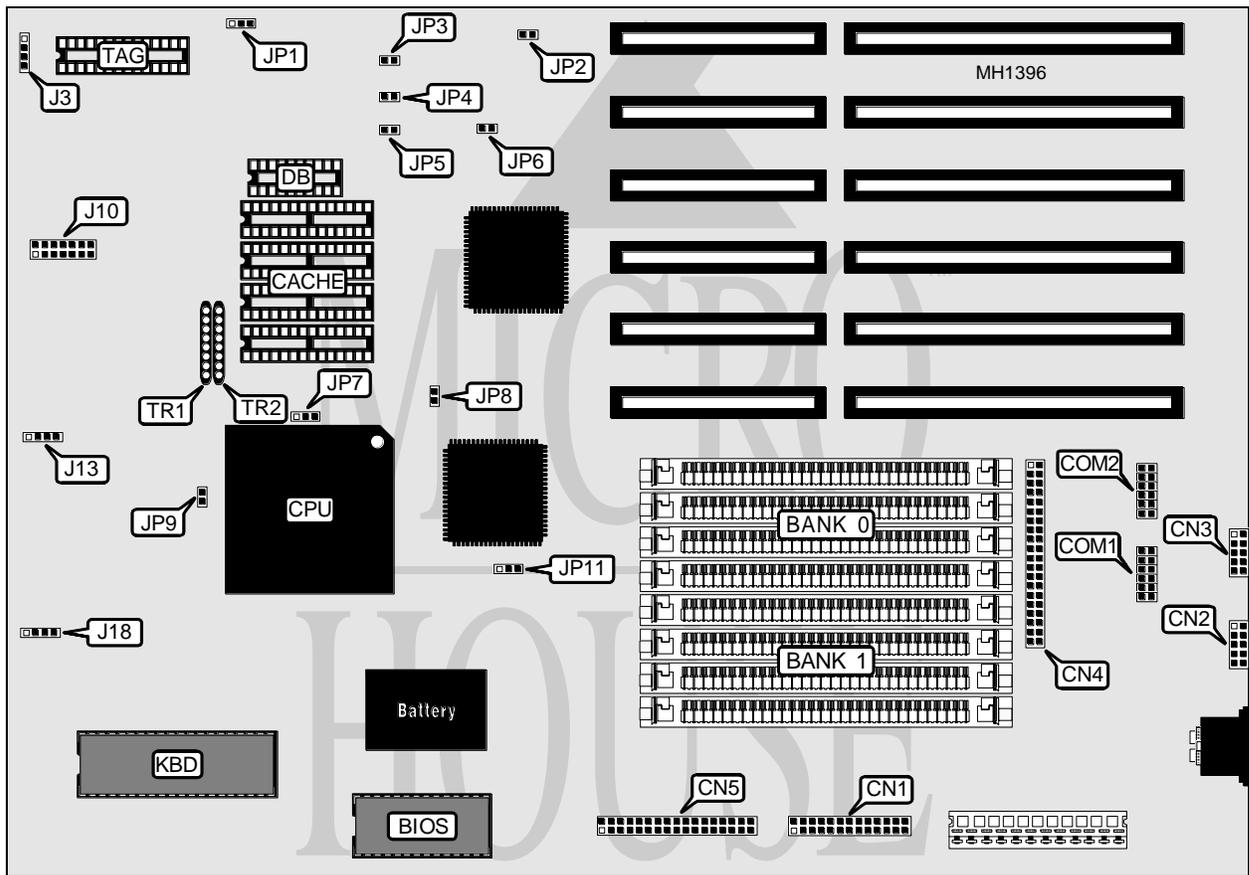
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| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| Function | Jumper/Switch | Position |
| í  Factory configured - do not alter | JP1 | Unknown |
| í  Factory configured - do not alter | JP2 | Unknown |
| í  Factory configured - do not alter | JP3 | Unknown |
| í  Factory configured - do not alter | JP4 | Unknown |
| í  I/O bus speed select 1 wait state | JP5 | Open |
| I/O bus speed select 0 wait states | JP5 | Closed |
| í  CMOS memory normal operation | JP8 | Unknown |
| CMOS memory clear | JP11 | Unknown |
| í  Factory configured - do not alter | JP11 | Unknown |

| DRAM CONFIGURATION | | |
|---|---|---|
| Size | Bank 0 | Bank 1 |
| 1MB | (4) 256K x 9 | NONE |
| 2MB | (4) 256K x 9 | (4) 256K x 9 |
| 4MB | (4) 1M x 9 | NONE |
| 5MB | (4) 1M x 9 | (4) 256K x 9 |
| 8MB | (4) 1M x 9 | (4) 1M x 9 |
| 16MB | (4) 4M x 9 | NONE |
| 17MB | (4) 4M x 9 | (4) 256K x 9 |
| 20MB | (4) 1M x 9 | (4) 4M x 9 |
| 32MB | (4) 4M x 9 | (4) 4M x 9 |

| CACHE CONFIGURATION | | | |
|---|---|---|---|
| Size | Cache | TAG | Dirty Bit (DB) |
| 32KB | (4) 8K x 8 | (1) 8K x 8 | (1) 16K x 1 |
| 64KB | (4) 16K x 8 | (1) 16K x 8 | (1) 16K x 1 |

| COM1 DTE/DCE CONFIGURATION | |
|---|---|
| Mode | Jumper Block COM1 |
| DCE | |
| DTE | |

| COM2 DTE/DCE CONFIGURATION | |
|---|---|
| Mode | Jumper Block COM2 |
| DCE | |
| DTE | |

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| CACHE RESISTOR CONFIGURATION | | |
|---|---|---|
| Size | TR1 | TR2 |
| 32KB | Not Installed | Installed |
| 64KB | Installed | Not Installed |

| CPU TYPE CONFIGURATION | | |
|---|---|---|
| TYPE | JP7 | JP9 |
| 80486SX | pins 2 & 3 closed | Open |
| 80487SX | pins 2 & 3 closed | Open |
| 80486DX | pins 1 & 2 closed | Closed |