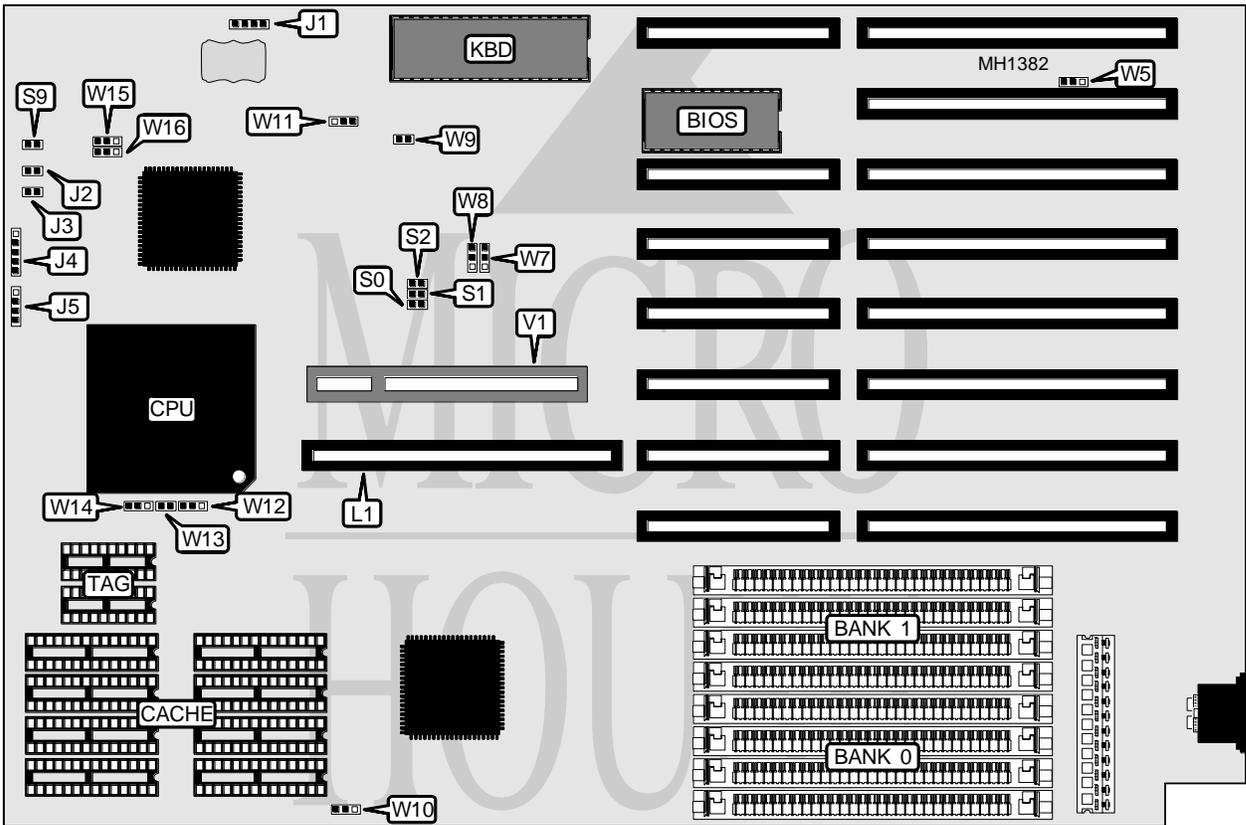


JC INFORMATION SYSTEMS CORPORATION

MODEL 2030

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	C & T
Max. Onboard DRAM	64MB
Cache	64/256KB
BIOS	C & T
Dimensions	330mm x 218mm
I/O Options	32-bit VESA card slot Multi-mode type local bus card slot
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Speaker	J5
Turbo LED	J2	Reset switch	S9
Turbo switch	J3	Multi-mode/local bus card	L1
Power LED & keylock	J4	32-bit VESA card	V1

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JC INFORMATION SYSTEMS CORPORATION

MODEL 2030

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í BIOS type select Flash ROM	W5	pins 1 & 2 closed
BIOS type select EPROM	W5	pins 2 & 3 closed
í Monitor type select monochrome	W9	open
Monitor type select color	W9	closed
í CMOS memory normal operation	W11	pins 2 & 3 closed
CMOS memory clear	W11	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

CPU TYPE CONFIGURATION			
Type	W12	W13	W14
80486DX2	pins 2 & 3 closed	closed	pins 1 & 2 closed
80486DX	pins 2 & 3 closed	closed	pins 1 & 2 closed
80487SX	pins 1 & 2 closed	closed	pins 1 & 2 closed
80486SX	N/A	N/A	pins 2 & 3 closed

CPU CONFIGURATION						
Speed	OSCI	S0	S1	S2	W7	W8
20MHz	40MHz	closed	open	closed	pins 1 & 2	pins 1 & 2
25MHz	50MHz	open	open	closed	pins 1 & 2	pins 1 & 2
33MHz	66MHz	closed	closed	open	pins 1 & 2	pins 1 & 2
40MHz	80MHz	open	closed	open	pins 1 & 2	pins 1 & 2
50(internal)MHz	50MHz	open	open	closed	pins 2 & 3	pins 2 & 3
50MHz	50MHz	open	open	closed	pins 1 & 2	pins 1 & 2
66(internal)MHz	66MHz	closed	closed	open	pins 1 & 2	pins 1 & 2

Note: Pins designated should be in the closed position.

CACHE JUMPER CONFIGURATION			
Size	W10	W15	W16
64KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
256KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

Note: The location of Jumper W15 and W16 may vary.

CACHE CONFIGURATION			
Size	Cache CACHE	Location	TAG
64KB	(8) 8K x 8	CACHE	(2) 16K x 1
256KB	(8) 32K x 8	CACHE	(2) 16K x 1