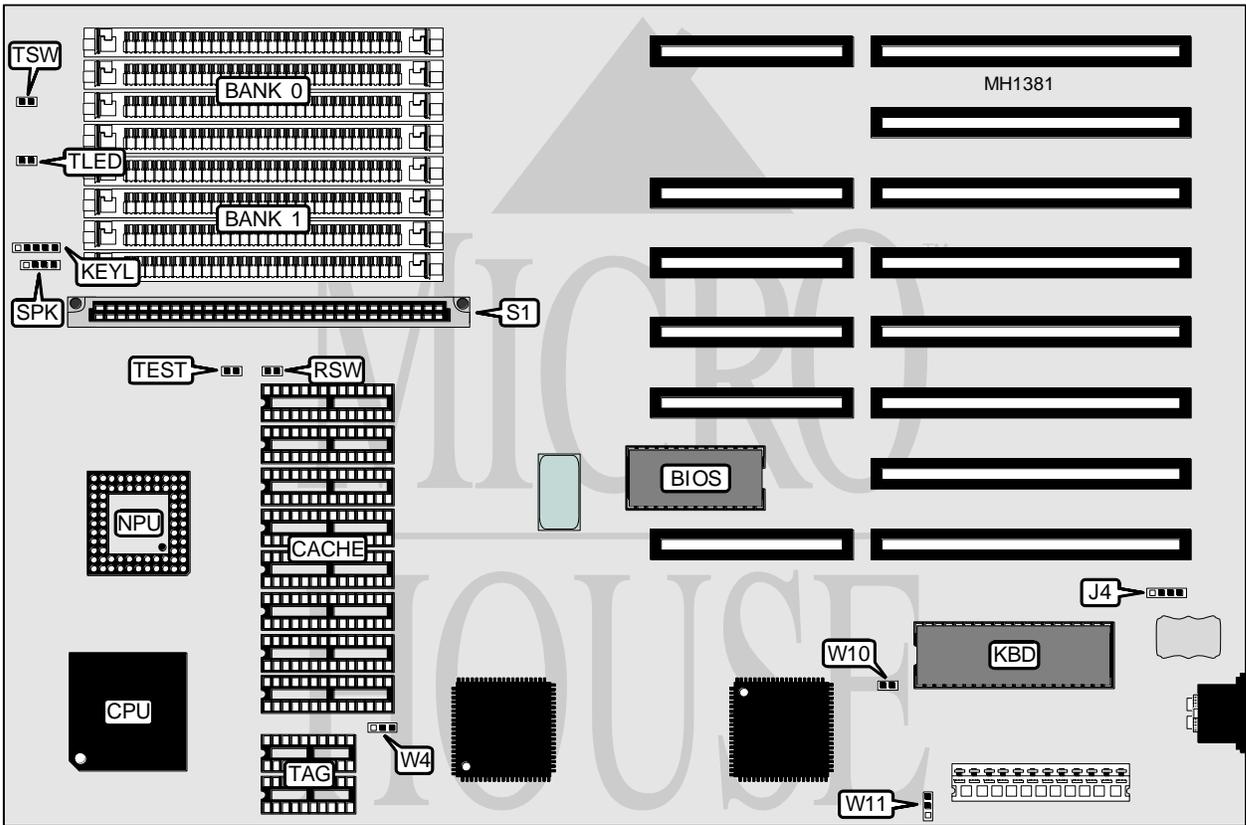


JC INFORMATION SYSTEMS CORPORATION MODEL 2020

Processor 80386DX
Processor Speed 25/33MHz
Chip Set C & T PEAK/DM
Max. Onboard DRAM 32MB
Cache 64KB
BIOS C & T/Phoenix
Dimensions 330mm x 218mm
I/O Options 32-bit external memory card slot
NPU Options 80387



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J4	Speaker	SPK
Power LED & keylock	KEYL	Self test	TEST
Reset switch	RSW	Turbo LED	TLED
32-bit external memory card	S1	Turbo switch	TSW

Continued on next page . . .

JC INFORMATION SYSTEMS CORPORATION
 386DX PEAK MODEL 2020

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	W2	open
í Factory configured - do not alter	W3	pins 1 & 2 closed
í Factory configured - do not alter	W6	closed
í Factory configured - do not alter	W9	pins 2 & 3 closed
í CMOS memory normal operation	W10	pins 2 & 3 closed
CMOS memory clear	W10	pins 1 & 2 closed
í Monitor type select color	W11	closed
Monitor type select monochrome	W11	open
Note: The locations of W2, W3, W6, and W9 are unknown.		

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	W4
1MB	(4) 256K x 9	NONE	pins 1 & 2 closed
2MB	(4) 256K x 9	(4) 256K x 9	pins 1 & 2 closed
4MB	(4) 1M x 9	NONE	pins 1 & 2 closed
5MB	(4) 1M x 9	(4) 256K x 9	pins 2 & 3 closed
8MB	(4) 1M x 9	(4) 1M x 9	pins 1 & 2 closed
16MB	(4) 4M x 9	NONE	pins 1 & 2 closed
32MB	(4) 4M x 9	(4) 4M x 9	pins 1 & 2 closed

CACHE CONFIGURATION			
Size	Cache CACHE	Location	TAG
64KB	(8) 8K x 8	CACHE	(2) 16K x 1