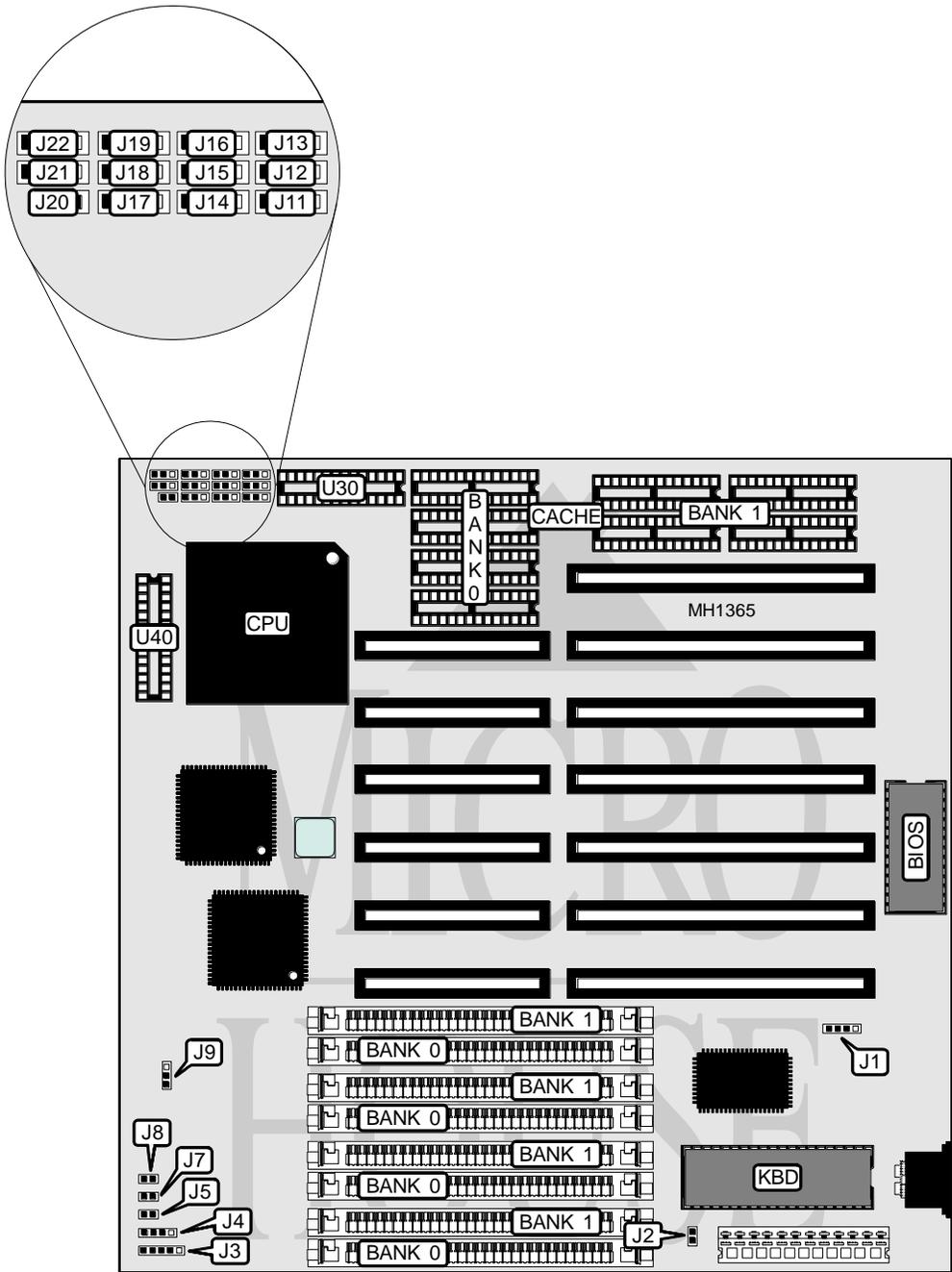


NIC TECHNOLOGY, INC.

SIH433B

Processor	80486SX/80487SX/80486DX/ODP486SX
Processor Speed	16/20/25/33/50(internal)/50/66(internal)MHz
Chip Set	UNI
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	218mm x 218mm
I/O Options	None
NPU Options	None



Continued on next page . . .

NIC TECHNOLOGY, INC.

SIH433B

... continued from previous page

CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	J5
Power LED & keylock	J3	Turbo switch	J7
Speaker	J4	Reset switch	J8

USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í CMOS memory normal operation	J1	pins 2 & 3 closed
CMOS memory clear	J1	Open
External battery select	J1	Connected
í Monitor type select color	J2	Open
Monitor type select monochrome	J2	Closed
í CPU speed select iOSC/1	J9	pins 1 & 2 closed
CPU speed select iOSC/2	J9	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CPU TYPE CONFIGURATION

Type	J20	J21	J22
80486SX	Open	pins 2 & 3 closed	Open
80487SX	Closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX	Closed	pins 1 & 2 closed	pins 1 & 2 closed
ODP486SX	Closed	pins 1 & 2 closed	pins 1 & 2 closed

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG (U40)	TAG (U30)
32KB	(4) 8K x 8	NONE	(1) 8K x 8	(1) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 16K x 4
128KB	(4) 32K x 8	NONE	(1) 32K x 8	(1) 16K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 4

CACHE JUMPER CONFIGURATION

Size	J11	J12	J13	J14	J15	J16	J17	J18	J19
32KB	1 & 2	open	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
64KB	2 & 3	open	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
128KB	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.