MICROMATION TECHNOLOGY, INC.
4 8 6 - W B H

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/50MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
SRAM Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	None
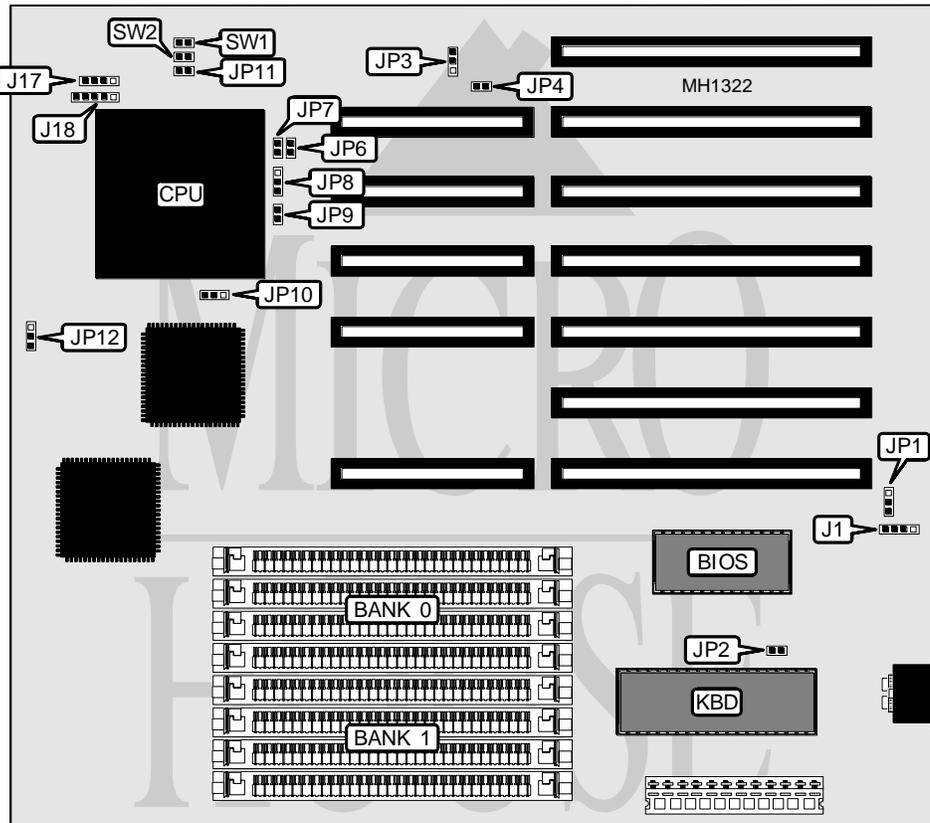
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	JP11
Speaker	J17	Reset switch	SW1
Power LED & keylock	J18	Turbo switch	SW2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	closed
Monitor type select monochrome	JP2	open
í CPU speed select OSCi/1	JP12	pins 2 & 3 closed
CPU speed select OSCi/2	JP12	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

SRAM CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
Note: This configuration is unverified. Location of Cache is unknown.			

SRAM JUMPER CONFIGURATION				
Size	Jumper JP3	Jumper JP4	Jumper JP6	Jumper JP7
64KB	pins 2 & 3 closed	open	open	open
128KB	pins 1 & 2 closed	closed	closed	open
256KB	pins 2 & 3 closed	closed	closed	closed

CPU TYPE CONFIGURATION			
CPU Type	Jumper JP8	Jumper JP9	Jumper JP10
80486DX	pins 1 & 2 closed	closed	pins 2 & 3 closed
80487SX	pins 1 & 2 closed	closed	pins 1 & 2 closed
80486SX	pins 2 & 3 closed	open	open