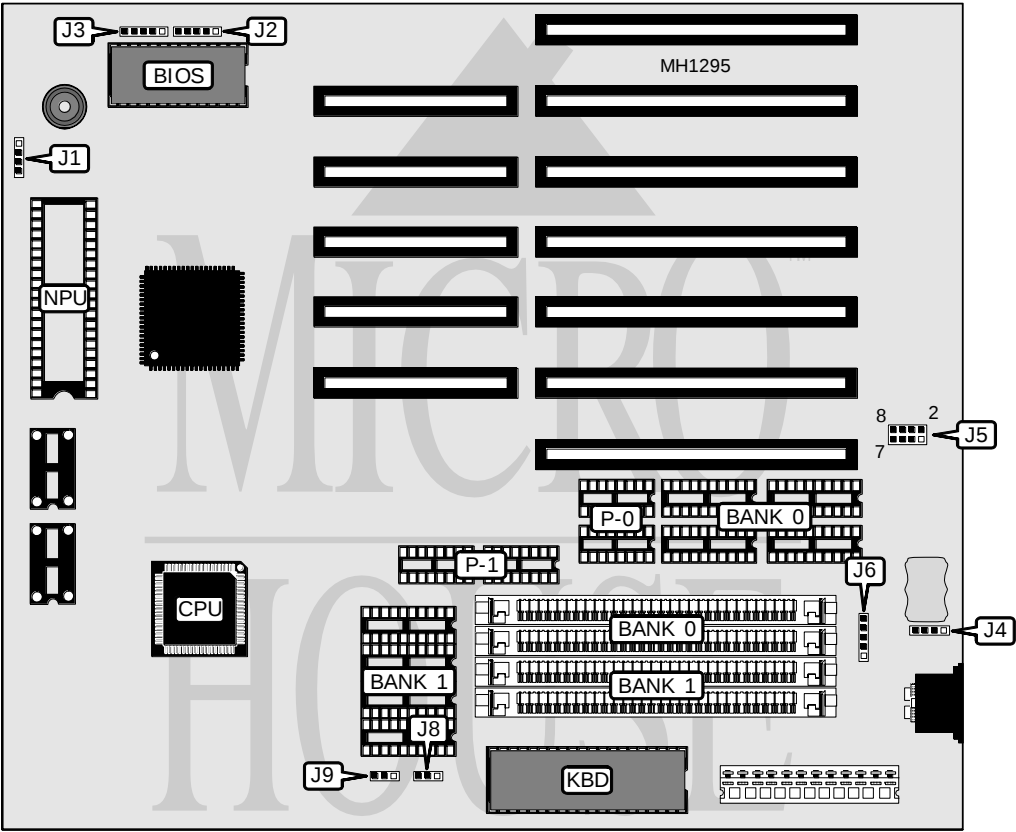


UNIDENTIFIED
2 M M / S C

Processor	80286
Processor Speed	12MHz
Chip Set	C & T
Max. Onboard DRAM	4MB
SRAM Cache	None
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1 pins 1 & 4	Turbo LED	J3 pins 3 & 4
Power LED & keylock	J2	External battery	J4
Reset switch	J3 pins 1 & 2		

Continued on next page . . .

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2 M M / S C

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Speaker select internal	J1	pins 2 & 3 closed
Speaker select external	J1	pins 1 & 4 connected
í Battery select internal	J5	pins 1 & 2 closed
Battery select external 6VDC	J5	pins 3 & 4 closed
Battery select external 4.5VDC	J5	pins 5 & 6 closed
Monitor type select color	J6	pins 1 & 2 closed
Monitor type select monochrome	J6	pins 2 & 3 closed
í Factory configured - do not alter	J7	unknown
Note: Location of Jumper J7 is unknown.		

DRAM CONFIGURATION						
Size	Bank 0(DIP)	P-0	Bank 1 (DIP)	P-1	Bank 0(SIMM)	Bank 1 (SIMM)
512KB ¹	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB ²	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
1MB ¹	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB ²	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
1.5MB ¹	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	NONE
2MB ²	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
2MB ¹	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	(2) 256K x 9
4MB ²	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9
Note ¹ : Set JP8 and JP9 pins 1 & 2 closed.						
Note ² : Set JP8 and JP9 pins 2 & 3 closed.						