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8 0 4 8 6 - 3 3 B

Processor	80486SX/80487SX/80486DX
Processor Speed	33MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
SRAM Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167
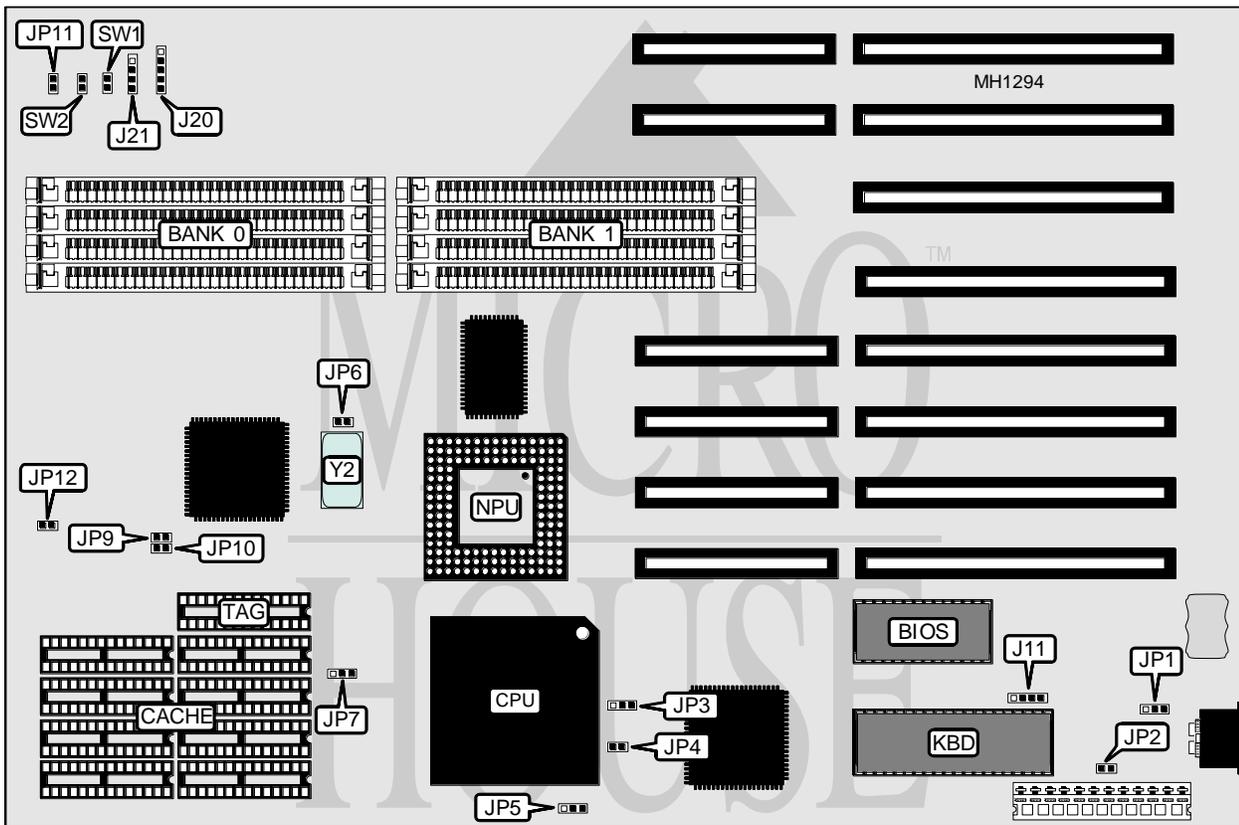
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J11	Turbo LED	JP11
Power LED & keylock	J20	Reset switch	SW1
Speaker	J21	Turbo switch	SW2

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	closed
Monitor type select monochrome	JP2	open
Factory configured - do not alter	JP6	closed
í CPU speed select iOSC(Y2)/2	JP7	pins 2 & 3 closed
CPU speed select iOSC(Y2)/1	JP7	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

SRAM CONFIGURATION					
Size	Cache SRAM	TAG	JP9	JP10	JP12
64KB	(8) 8K x 8	(1) 8K x 8	open	open	open
256KB	(8) 32K x 8	(1) 32K x 8	closed	closed	closed

CPU JUMPER CONFIGURATION			
CPU	JP3	JP4	JP5
80486DX	pins 2 & 3 closed	closed	pins 1 & 2 closed
80487SX	pins 1 & 2 closed	closed	pins 1 & 2 closed
80486SX	open	open	pins 2 & 3 closed