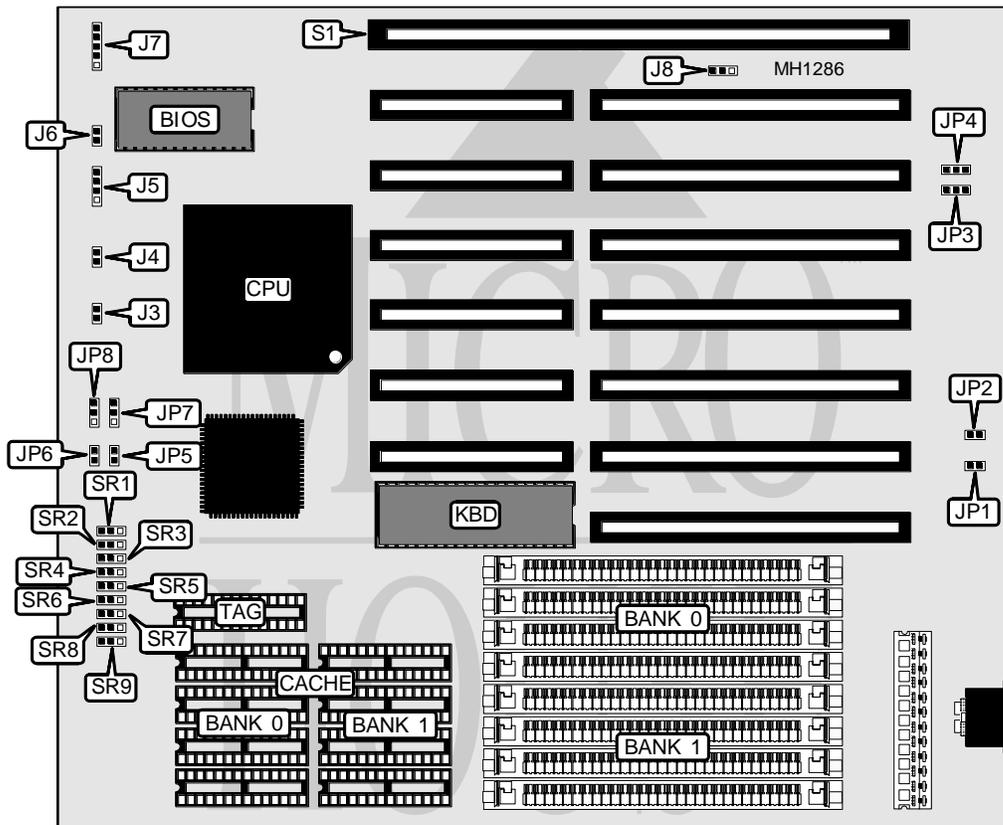


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486S14 (M-81)

Processor 80486SX/80487SX/80486DX/80486DX2
Processor Speed 20/25/33/50(internal)/50/66(internal)MHz
Chip Set C & T
Max. Onboard DRAM 32MB
SRAM Cache 128/256KB
BIOS AMI
Dimensions 254mm x 218mm
I/O Options 32-bit local bus card slot
NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J3	Reset switch	J6
Turbo switch	J4	Power LED & keylock	J7
Speaker	J5	32-bit local bus card	S1

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486SI4 (M-81)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Local bus data size select 32-bits	J8	pins 1 & 2 closed
Local bus data size select 16-bits	J8	pins 2 & 3 closed
í Factory configured - do not alter	JP1	open
í Monitor type select color	JP2	closed
Monitor type select monochrome	JP2	open
í Factory configured - do not alter	JP3	pins 2 & 3 closed
í Factory configured - do not alter	JP4	pins 2 & 3 closed
í Factory configured - do not alter	JP5	closed

CPU JUMPER CONFIGURATION			
CPU	JP6	JP7	JP8
80486DX	closed	pins 1 & 2 closed	pins 1 & 2 closed
80487SX	closed	pins 2 & 3 closed	pins 1 & 2 closed
80486SX	open	open	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

SRAM JUMPER CONFIGURATION		
Jumper	128KB	256KB
SR1	pins 2 & 3 closed	pins 2 & 3 closed
SR2	pins 2 & 3 closed	pins 2 & 3 closed
SR3	pins 2 & 3 closed	pins 2 & 3 closed
SR4	pins 1 & 2 closed	pins 2 & 3 closed
SR5	pins 1 & 2 closed	pins 2 & 3 closed
SR6	pins 2 & 3 closed	pins 2 & 3 closed
SR7	pins 2 & 3 closed	pins 2 & 3 closed
SR8	pins 1 & 2 closed	pins 2 & 3 closed
SR9	pins 1 & 2 closed	pins 2 & 3 closed

SRAM CONFIGURATION			
Size	Cache SRAM	Location	TAG
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 8K x 8