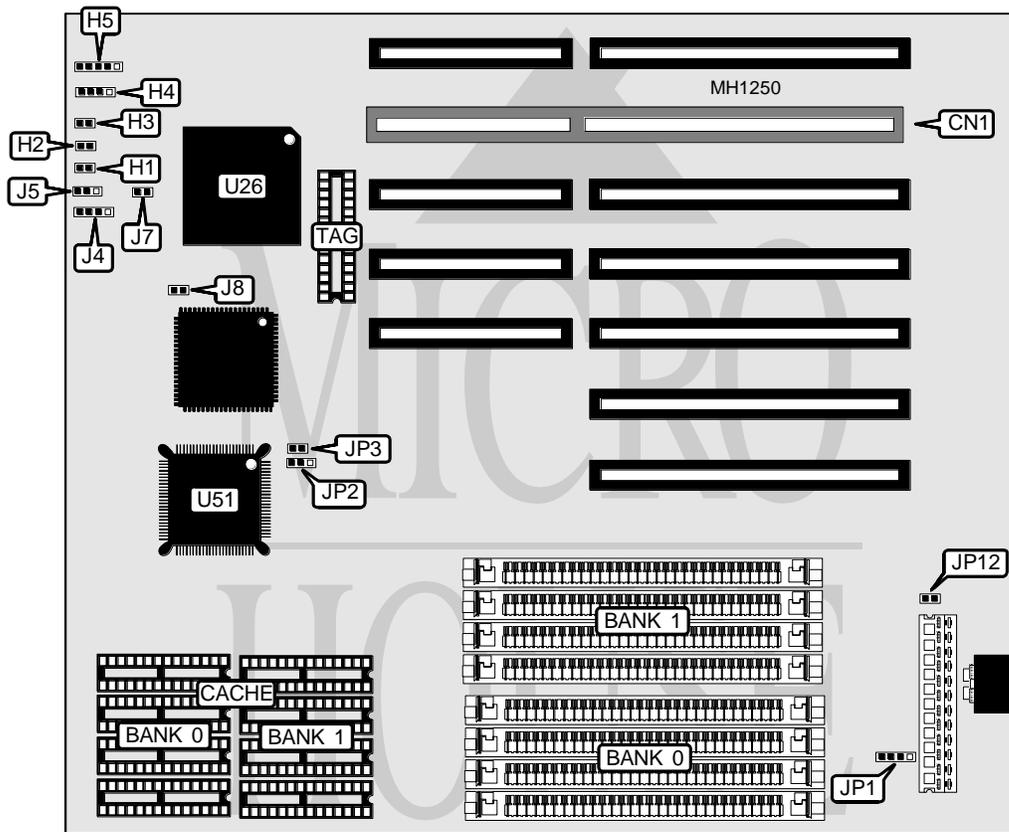


# FREE COMPUTER TECHNOLOGY, INC.

## 80486L-ISA

<b>Processor</b>	80486SX/80487SX/80486DX/ODP486SX/80486DX2
<b>Processor Speed</b>	20/25/33/50(internal)/50/66(internal)MHz
<b>Chip Set</b>	UNI
<b>Max. Onboard DRAM</b>	128MB
<b>SRAM Cache</b>	64/128/256KB
<b>BIOS</b>	AMI/Award
<b>Dimensions</b>	254mm x 218mm
<b>I/O Options</b>	32-bit local bus card slot
<b>NPU Options</b>	None



### CONNECTIONS

Purpose	Location	Purpose	Location
32-bit local bus card	CN1	Speaker	H4
Turbo switch	H1	Power LED & keylock	H5
Turbo LED	H2	External battery	JP1
Reset switch	H3		

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## 80486L-ISA

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP12	open
Monitor type select monochrome	JP12	closed

CPU TYPE CONFIGURATION					
U26	U51	J4	J5	J7	J8
80486DX2	80486DX	pins 1 & 2 and 3 & 4	pins 1 & 2	closed	open
80486DX	80486DX	pins 1 & 2 and 3 & 4	pins 1 & 2	closed	open
80487SX	80486DX	pins 1 & 2 and 3 & 4	pins 2 & 3	closed	open
80486SX	80486DX	pins 2 & 3	open	closed	open
NONE	80486DX	pins 1 & 2 and 3 & 4	pins 1 & 2	open	closed

Note: Pins designated should be in the closed position.  
 U51 is a surface mounted CPU (PQFP) that may or may not have been installed at the factory.  
 U26 is a socketed CPU (PGA) that has been installed at the factory, or can be installed by the user.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
80MB	(4) 16M x 9	(4) 4M x 9
128MB	(4) 16M x 9	(4) 16M x 9

SRAM JUMPER CONFIGURATION		
Size	JP2	JP3
64KB	open	open
128KB	pins 2 & 3 closed	closed
256KB	pins 1 & 2 closed	closed

SRAM CONFIGURATION			
Size	Cache SRAM	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 32K x 8