INFORMTECH INTERNATIONAL, INC.
IT486SL-50MHZ VER. 2.0

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/50MHz
Chip Set	OPTI
Max. Onboard DRAM	40MB
SRAM Cache	64/256KB
BIOS	OPTI
Dimensions	218mm x 330mm
I/O Options	Auxiliary keyboard connector
NPU Options	4167
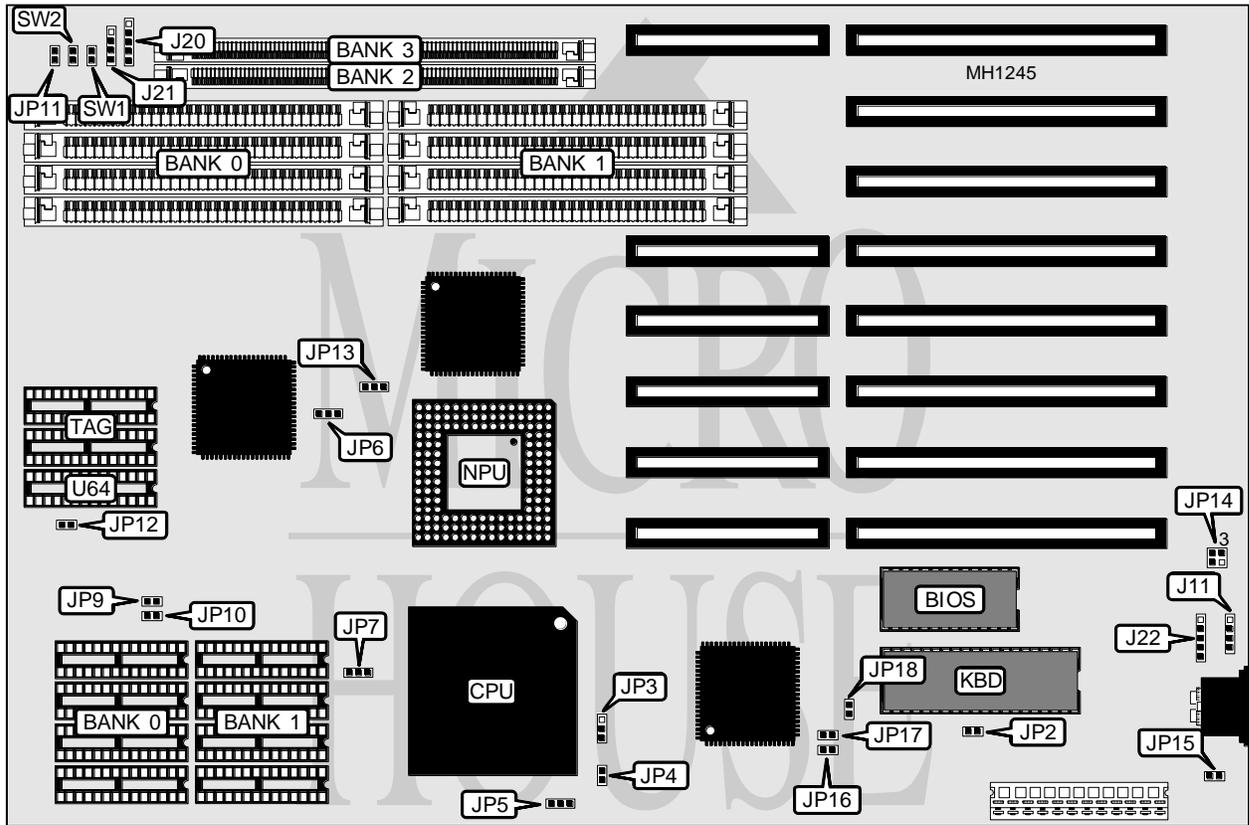
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J11	Turbo LED	JP11
Power LED & keylock	J20	Reset switch	SW1
Speaker	J21	Turbo switch	SW2
Auxiliary keyboard	J22		

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IT486SL-50MHZ VER. 2.0

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP2	open
Monitor type select color	JP2	closed
í Factory configured - do not alter	JP6	open
í External battery type select lithium 3.6VDC	JP14	pins 1 & 3 closed
External battery type select alkaline 4.5VDC	JP14	pins 1 & 2 closed
External battery type select four pack AA 6VDC	JP14	pins 3 & 4 closed
í Factory configured - do not alter	JP15	open
I/O bus select zero wait states	JP16	closed
	JP17	closed
I/O bus select one wait state	JP16	closed
	JP17	open
I/O bus select two wait states	JP16	open
	JP17	closed
I/O bus select three wait states	JP16	open
	JP17	open
í Factory configured - do not alter	JP18	open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(1) 4M x 36	NONE
16MB	(4) 4M x 9	NONE	NONE	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 4M x 9	(1) 4M x 36	NONE
28MB	(4) 1M x 9	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 4M x 9	(4) 4M x 9	(1) 4M x 36	NONE
40MB	(4) 4M x 9	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36

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IT486SL-50MHZ VER. 2.0

CPU TYPE CONFIGURATION			
CPU Type	JP3	JP4	JP5
80486DX	pins 2 & 3 closed	closed	pins 1 & 2 closed
80487SX	pins 1 & 2 closed	closed	pins 1 & 2 closed
80486SX	open	open	pins 2 & 3 closed

SRAM JUMPER CONFIGURATION			
Size	JP9	JP10	JP12
64KB	open	open	open
256KB	closed	closed	closed

SRAM CONFIGURATION			
Size	Bank 0	TAG	Dirty Bit (U64)
64KB	(8) 8K x 8	(2) 16K x 4	(1) 64K x 1
256KB	(8) 32K x 8	(2) 16K x 4	(1) 64K x 1

CPU SPEED CONFIGURATION		
CPU Speed	JP7	JP13
50MHz	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 2 & 3 closed	pins 1 & 2 closed
25MHz	pins 2 & 3 closed	pins 2 & 3 closed