ISA/EISA, INC.
NIC-WARE-486

Processor	80486DX
Processor Speed	25/33MHz
Chip Set	None
Max. Onboard DRAM	64MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	349mm x 244mm
I/O Options	None
NPU Options	4167
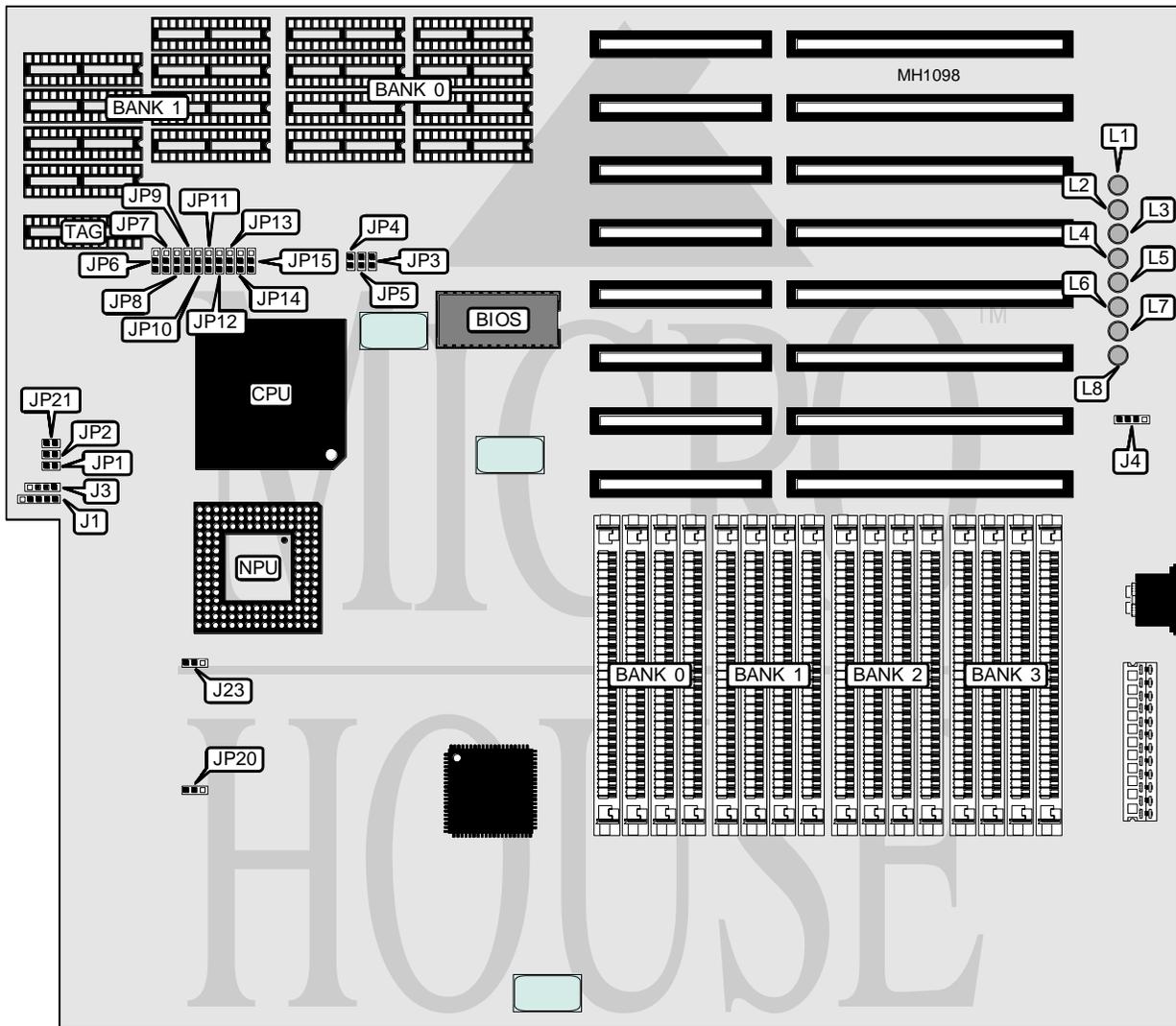
CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo switch	JP2
Speaker	J3	Turbo LED	JP21
External battery	J4	POST LEDs (L1=MSB, L8=LSB)	L1-L8
Reset switch	JP1		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í External cache enabled	JP20	pins 2 & 3 closed
External cache disabled	JP20	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 1M x 9	(4) 256K x 9	NONE	NONE
6MB	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	NONE
7MB	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	NONE
10MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
13MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 4M x 9	(4) 1M x 9	NONE	NONE
24MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	NONE
28MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	NONE
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
52MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

DRAM JUMPER CONFIGURATION	
Size	J23
1MB	pins 1 & 2 closed
2MB	pins 1 & 2 closed
3MB	pins 1 & 2 closed
4MB	pins 1 & 2 closed
4MB	pins 1 & 2 closed
5MB	pins 1 & 2 closed
6MB	pins 1 & 2 closed
7MB	pins 1 & 2 closed
8MB	pins 1 & 2 closed
9MB	pins 1 & 2 closed

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DRAM JUMPER CONFIGURATION (CON'T)	
Size	J23
10MB	pins 1 & 2 closed
12MB	pins 1 & 2 closed
13MB	pins 1 & 2 closed
16MB	pins 2 & 3 closed
16MB	pins 2 & 3 closed
20MB	pins 2 & 3 closed
24MB	pins 2 & 3 closed
28MB	pins 2 & 3 closed
32MB	pins 2 & 3 closed
36MB	pins 2 & 3 closed
40MB	pins 2 & 3 closed
48MB	pins 2 & 3 closed
52MB	pins 2 & 3 closed
64MB	pins 2 & 3 closed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(8) 8K x 8	NONE	(1) 8K x 8
128KB	(8) 8K x 8	(8) 8K x 8	(1) 8K x 8
256KB	(8) 32K x 8	NONE	(1) 32K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION										
Size	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13	JP14	JP15
64KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
128KB	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3
256KB	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
512KB	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.										

AT BUS CLOCK CONFIGURATION				
CPU Speed	Bus Speed	JP3	JP4	JP5
33MHz	16.5MHz (CPU/2)	Open	Open	Closed
33MHz	10MHz (Async)	Closed	Open	Open
33MHz	8.25MHz (CPU/4)	Open	Closed	Open
25MHz	12.5MHz (CPU/2)	Open	Open	Closed
25MHz	8MHz (Async)	Closed	Open	Open
25MHz	6.25 (CPU/4)	Open	Closed	Open