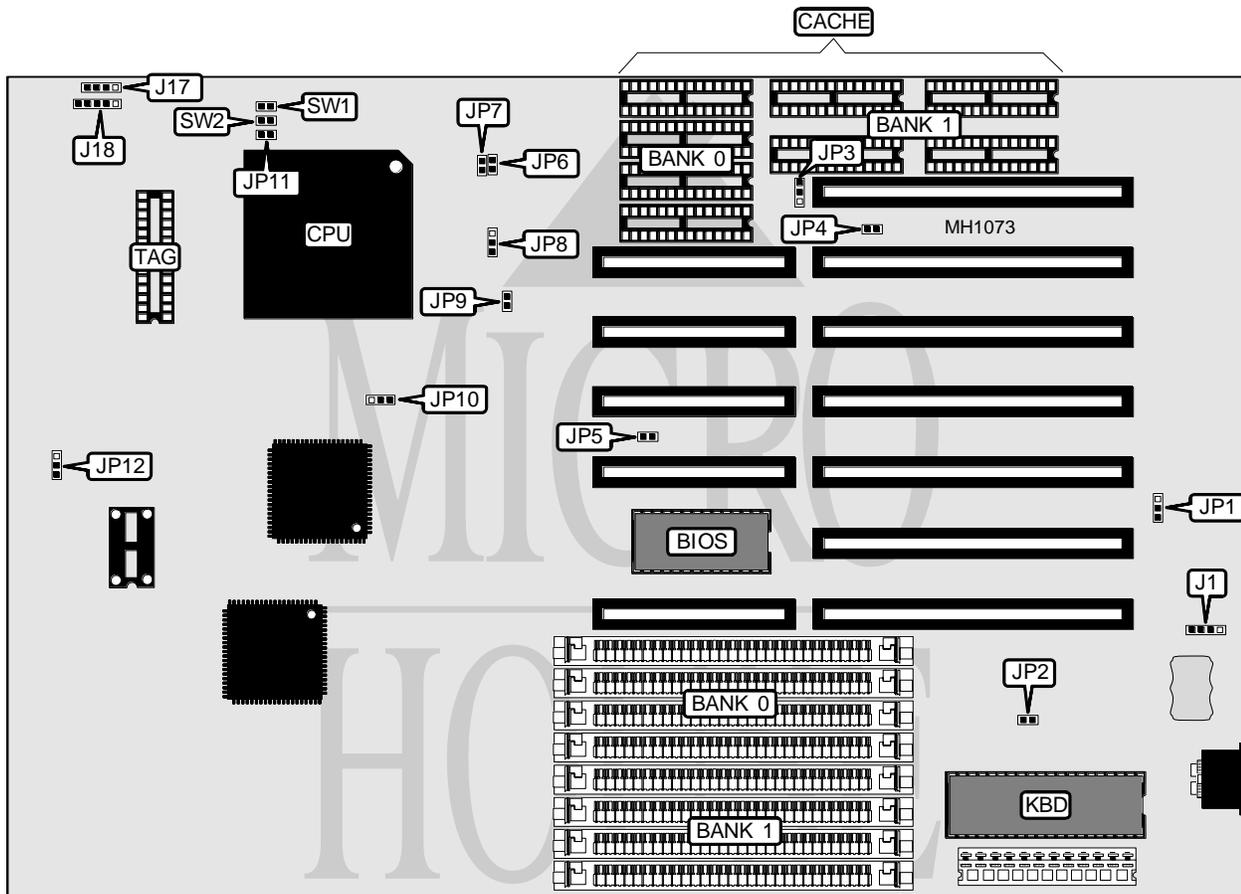


# CHICONY, INC.

## CH-486-33/50 L

<b>Processor</b>	80486SX/80487SX/80486DX
<b>Processor Speed</b>	20/25/33/50MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	None



### CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	JP11
Speaker	J17	Reset switch	SW1
Power LED & keylock	J18	Turbo switch	SW2

*Continued on next page . . .*

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CH-486-33/50 L

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP3	JP4	JP6	JP7
64KB	pins 2 & 3 closed	Open	Open	Open
128KB	pins 1 & 2 closed	Closed	Closed	Open
256KB	pins 2 & 3 closed	Closed	Closed	Closed

CPU TYPE CONFIGURATION			
Type	JP8	JP9	JP10
80486SX	pins 1 & 2 closed	Closed	pins 1 & 2 closed
80487SX	pins 2 & 3 closed	Open	Open
80486DX	pins 1 & 2 closed	Closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION	
Type	JP12
20MHz	pins 2 & 3 closed
25MHz	pins 2 & 3 closed
33MHz	pins 2 & 3 closed
50MHz	pins 1 & 2 closed