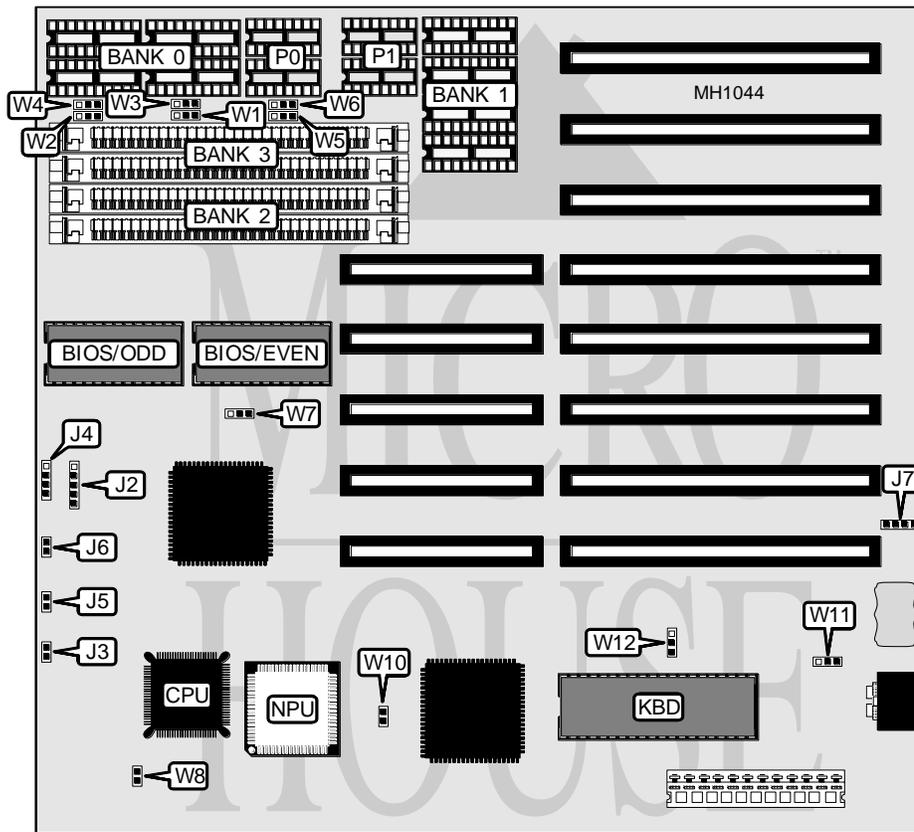


# DTK COMPUTER, INC.

## PPM - 1631D(S)/2031D(S)/2531D(S)

<b>Processor</b>	80386SX
<b>Processor Speed</b>	16/20/25MHz
<b>Chip Set</b>	DTK
<b>Max. Onboard DRAM</b>	5MB
<b>Cache</b>	None
<b>BIOS</b>	DTK/AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J2	Turbo LED	J5
Turbo switch	J3	Reset switch	J6
Speaker	J4	External battery	J7

Continued on next page . . .

## DTK COMPUTER, INC.

## P P M - 1 6 3 1 D ( S ) / 2 0 3 1 D ( S ) / 2 5 3 1 D ( S )

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í BIOS type select 27256	W7	pins 1 & 2 closed
BIOS type select 27128	W7	pins 2 & 3 closed
í NPU disabled	W8	Closed
NPU enabled	W8	Open
í NPU mode select synchronous with CPU	W10	Open
NPU mode select asynchronous with CPU	W10	Closed
í Battery type select internal	W11	pins 2 & 3 closed
Battery type select external	W11	pins 1 & 2 closed
í Monitor type select monochrome	W12	pins 1 & 2 closed
Monitor type select color	W12	pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB <sup>1</sup>	(4) 44256	(2) 4464	NONE	NONE	NONE	NONE
640KB <sup>1</sup>	(4) 44256	(2) 4464	(4) 41256	(2) 4164	NONE	NONE
1MB <sup>1</sup>	(4) 44256	(2) 4464	(4) 44256	(2) 4464	NONE	NONE
1.5MB <sup>2</sup>	(4) 44256	(2) 4464	(4) 41256	(2) 4164	(2) 256K x 9	NONE
2MB <sup>2</sup>	(4) 44256	(2) 4464	(4) 44256	(2) 4464	(2) 256K x 9	(2) 256K x 9
2MB <sup>1</sup>	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
3MB <sup>2</sup>	(4) 44256	(2) 4464	(4) 44256	(2) 4464	(2) 1M x 9	NONE
4MB <sup>1</sup>	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9
5MB <sup>2</sup>	(4) 44256	(2) 4464	(4) 44256	(2) 4464	(2) 1M x 9	(2) 1M x 9

DRAM JUMPER CONFIGURATION						
Size	W1	W2	W3	W4	W5	W6
512KB <sup>1</sup>	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
640KB <sup>1</sup>	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1MB <sup>1</sup>	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1.5MB <sup>2</sup>	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB <sup>2</sup>	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB <sup>1</sup>	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
3MB <sup>2</sup>	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB <sup>1</sup>	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
5MB <sup>2</sup>	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.