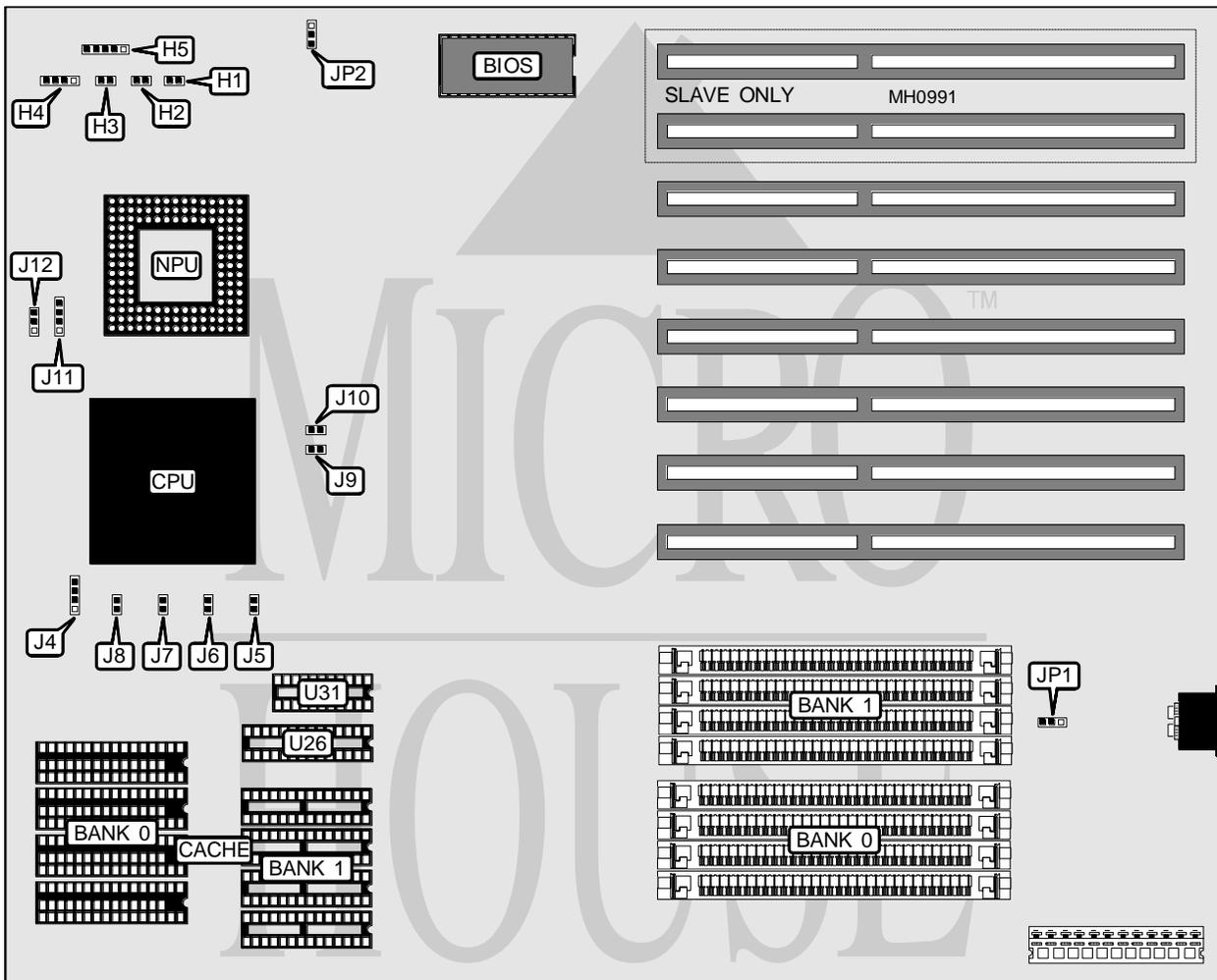


FREE COMPUTER TECHNOLOGY, INC.

80486EISA (VER. 2.0)

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	330mm x 365mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	H1	Speaker	H4
Reset switch	H2	Power LED & keylock	H5
Turbo switch	H3		

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80486ISA (VER. 2.0)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	Open
í Factory configured - do not alter	JP2	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U26)	TAG (U31)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 16K x 1
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 16K x 1
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 1
512KB	(4) 128K x 8	NONE	(1) 32K x 8	(1) 64K x 1

CACHE JUMPER CONFIGURATION					
Size	J4	J5	J6	J7	J8
64KB	pins 2 & 3 closed	Open	Open	Open	Open
128KB	pins 1 & 2 closed	Closed	Open	Open	Closed
256KB	pins 2 & 3 closed	Closed	Closed	Open	Closed
512KB	pins 1 & 2 closed	Closed	Closed	Closed	Closed

CPU SPEED CONFIGURATION		
Speed	J9	J10
20MHz	Open	Open
25MHz	Open	Open
33MHz	Open	Closed
40MHz	Closed	Open
50iMHz	Open	Open
50MHz	Closed	Closed
66iMHz	Open	Closed

CPU TYPE CONFIGURATION		
Type	J11	J12
80486SX	pins 2 & 3 closed	open
80487SX	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed
80486DX	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed
80486DX2	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed