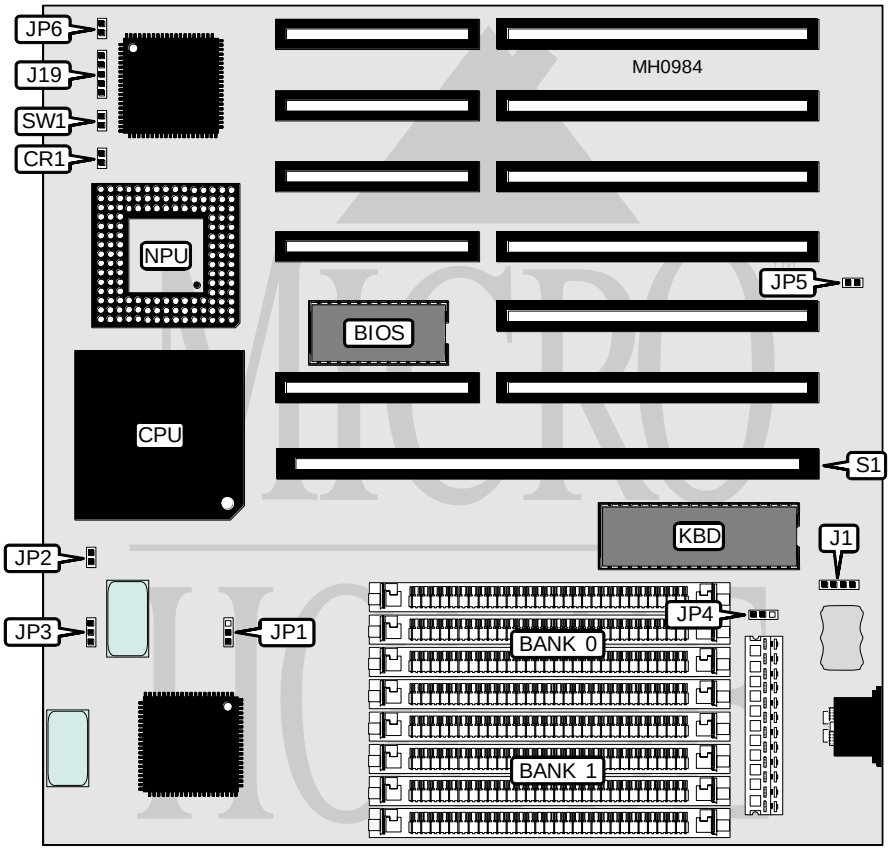
BEK-TRONIC TECHNOLOGY, INC.
B E K - 4 8 0 2 L

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/50(internal)/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	Local bus slot
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	CR1	Turbo switch	JP6
External battery	J1	Local bus	S1
Power LED & keylock	J19	Reset switch	SW1
Speaker	J20		
Note: The location of J20 is unidentified.			

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP4	pins 1 & 2 closed
CMOS memory clear	JP4	pins 2 & 3 closed
í Monitor type select color	JP5	Closed
Monitor type select monochrome	JP5	Open
Note: These settings are unverified.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CPU TYPE CONFIGURATION			
Type	JP1	JP2	JP3
80486SX	Open	Open	pins 2 & 3 closed
80487SX	pins 2 & 3 closed	Closed	pins 1 & 2 closed
80486DX	pins 1 & 2 closed	Closed	pins 1 & 2 closed
80486DX2	pins 1 & 2 closed	Closed	pins 1 & 2 closed

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