SQUARE ONE INDUSTRIES, INC.
S 1 - 4 8 6

Processor	80486SX/80486DX/80486DX2
Processor Speed	20/25/33/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot
NPU Options	4167
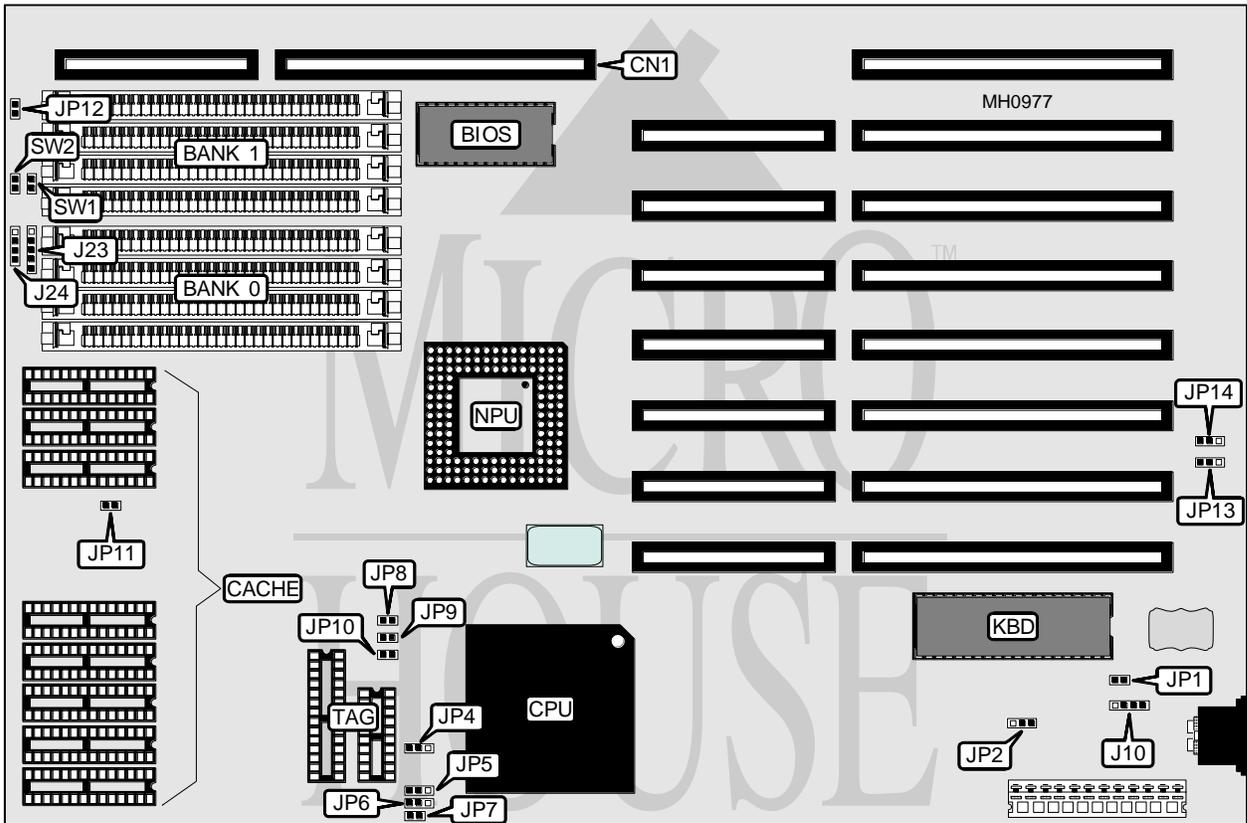
CONNECTIONS			
Purpose	Location	Purpose	Location
32-bit external memory card	CN1	Turbo LED	JP12
External battery	J10	Reset switch	SW1
Power LED & keylock	J23	Turbo switch	SW2
Speaker	J24		

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S1-486

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Bus speed select CLK2/6	JP3	Closed
Bus speed select CLK2/4	JP3	Open
í CPU oscillator select single phase	JP4	pins 1 & 2 closed
CPU oscillator select double phase	JP4	pins 2 & 3 closed
í Factory configured - do not alter	JP10	Open
í Factory configured - do not alter	JP13	Open
í Factory configured - do not alter	JP14	Open
Note: Jumper JP3 may not be present on all board revisions.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION	
Size	Bank 0
64KB	(8) 8K x 8
256KB	(8) 32K x 8
Note: TAG is not user configurable.	

CACHE JUMPER CONFIGURATION			
Size	JP8	JP9	JP11
64KB	Open	Open	Open
256KB	Closed	Closed	Closed

CPU TYPE CONFIGURATION			
Type	JP5	JP6	JP7
80486SX	Open	pins 2 & 3 closed	Open
80486DX	pins 2 & 3 closed	pins 1 & 2 closed	Closed
80486DX2	pins 2 & 3 closed	pins 1 & 2 closed	Closed