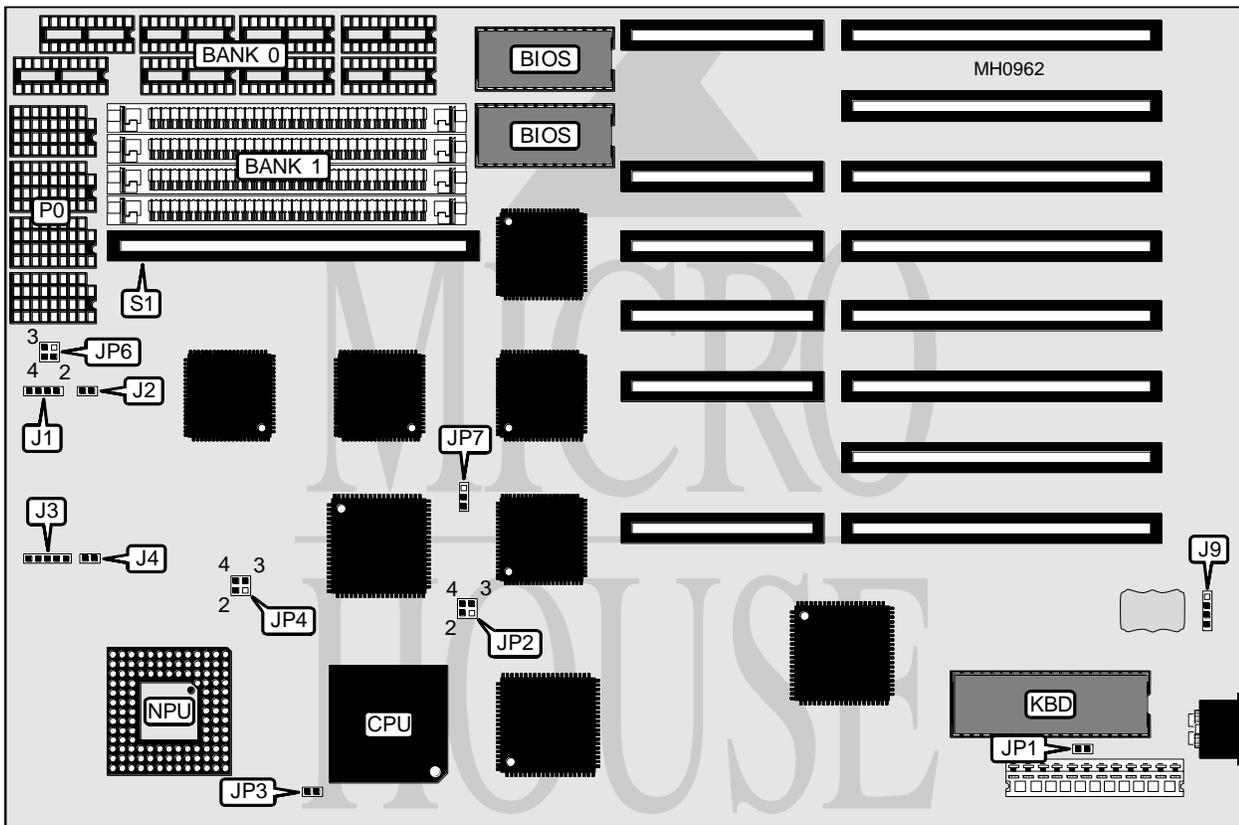


JC INFORMATION SYSTEMS CORPORATION HIGH PERFORMANCE 386 (NON-CACHE)

Processor 80386DX
Processor Speed 20/25MHz
Chip Set C & T
Max. Onboard DRAM 8MB
Cache None
BIOS Phoenix
Dimensions 330mm x 218mm
I/O Options 32-bit external memory card slot
NPU Options 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Turbo LED	J4
Reset switch	J2	External battery	J9
Power LED & keylock	J3	32-bit external memory card	S1

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JC INFORMATION SYSTEMS CORPORATION

HIGH PERFORMANCE 386 (NON-CACHE)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery select internal	J9	pins 3 & 4 closed
Battery select external	J9	Closed
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í NPU disabled	JP2	Open
NPU enabled	JP2	pins 1 & 2, 3 & 4 closed
í Factory configured - do not alter	JP3	N/A

DRAM CONFIGURATION			
Size	Bank 0	P-0	Bank 1
1MB	(8) 44256	(4) 41256	NONE
1MB	NONE	NONE	(4) 256K x 9
2MB	(8) 44256	(4) 41256	(4) 256K x 9
4MB	(8) 441000	(4) 411000	NONE
4MB	NONE	NONE	(4) 1M x9
5MB	(8) 441000	(4) 411000	(4) 256K x 9
5MB	(8) 44256	(4) 41256	(4) 1M x9
8MB	(8) 441000	(4) 411000	(4) 1M x9

DRAM JUMPER CONFIGURATION			
Function	JP4	JP6	JP7
Bank 0 as DIPP memory	pins 1 & 2 closed	pins 3 & 4 closed	pins 2 & 3 closed
Bank 0 as SIMM memory	pins 3 & 4 closed	pins 1 & 2 closed	pins 1 & 2 closed
Note : When using both DIPP & SIMM together, the DIPP's must be set as Bank 0.			